

ZXMP10A13F

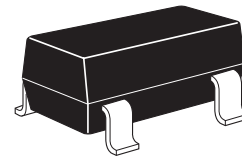
100V P-CHANNEL ENHANCEMENT MODE MOSFET

SUMMARY

$V_{(BR)DSS} = -100V$; $R_{DS(on)} = 1\Omega$; $I_D = -0.7A$

DESCRIPTION

This new generation of Trench MOSFETs from Zetex utilizes a unique structure that combines the benefits of low on-resistance with fast switching speed. This makes them ideal for high efficiency, low voltage, power management applications.



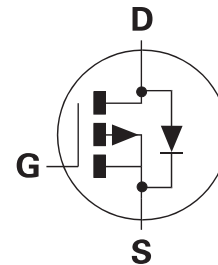
SOT23

FEATURES

- Low on-resistance
- Fast switching speed
- Low threshold
- Low gate drive
- SOT23 package

APPLICATIONS

- DC-DC Converters
- Power Management functions
- Disconnect switches
- Motor control



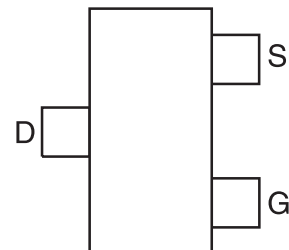
ORDERING INFORMATION

DEVICE	REEL SIZE	TAPE WIDTH	QUANTITY PER REEL
ZXMP10A13FTA	7"	8mm	3000 units
ZXMP10A13FTC	13"	8mm	10000units

DEVICE MARKING

- 7P1

PINOUT



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ABSOLUTE MAXIMUM RATINGS

PARAMETER	SYMBOL	LIMIT	UNIT
Drain-Source Voltage	V_{DSS}	-100	V
Gate-Source Voltage	V_{GS}	± 20	V
Continuous Drain Current @ $V_{GS}=10V$; $T_A=25^\circ C$ ^(b)	I_D	-0.7	A
@ $V_{GS}=10V$; $T_A=70^\circ C$ ^(b)		-0.5	A
@ $V_{GS}=10V$; $T_A=25^\circ C$ ^(a)		-0.6	A
Pulsed Drain Current ^(c)	I_{DM}	-3.1	A
Continuous Source Current (Body Diode) ^(b)	I_S	-1.1	A
Pulsed Source Current (Body Diode) ^(c)	I_{SM}	-3.1	A
Power Dissipation at $T_A=25^\circ C$ ^(a)	P_D	625	mW
Linear Derating Factor		5	mW/ $^\circ C$
Power Dissipation at $T_A=25^\circ C$ ^(b)	P_D	806	mW
Linear Derating Factor		6.4	mW/ $^\circ C$
Operating and Storage Temperature Range	T_J, T_{stg}	-55 to +150	$^\circ C$

THERMAL RESISTANCE

PARAMETER	SYMBOL	VALUE	UNIT
Junction to Ambient ^(a)	$R_{\theta JA}$	200	$^\circ C/W$
Junction to Ambient ^(b)	$R_{\theta JA}$	155	$^\circ C/W$

NOTES

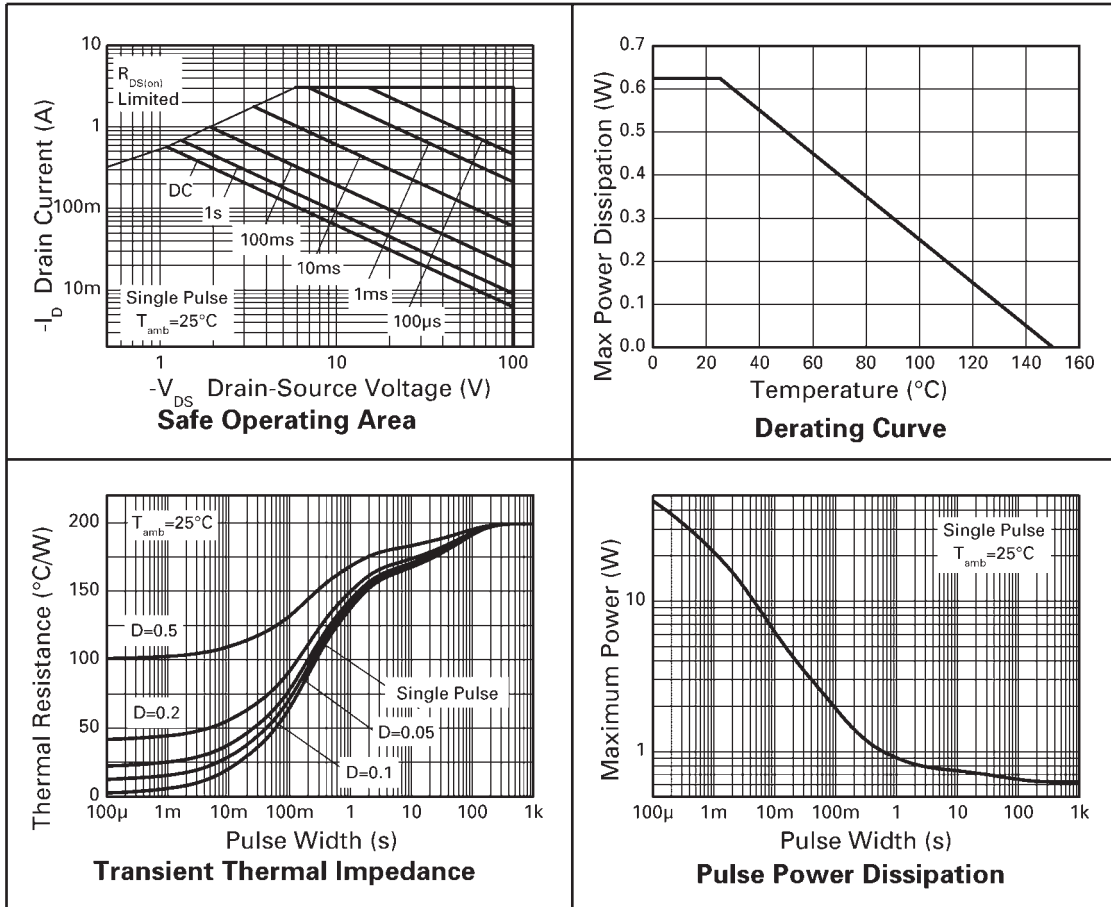
(a) For a device surface mounted on 25mm x 25mm FR4 PCB with high coverage of single sided 1oz copper, in still air conditions.

(b) For a device surface mounted on FR4 PCB measured at $t \leq 5$ sec.

(c) Repetitive rating 25mm x 25mm FR4 PCB, $D = 0.02$, pulse width 300 μs - pulse width limited by maximum junction temperature.

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CHARACTERISTICS



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ELECTRICAL CHARACTERISTICS (at $T_{amb} = 25^{\circ}\text{C}$ unless otherwise stated)

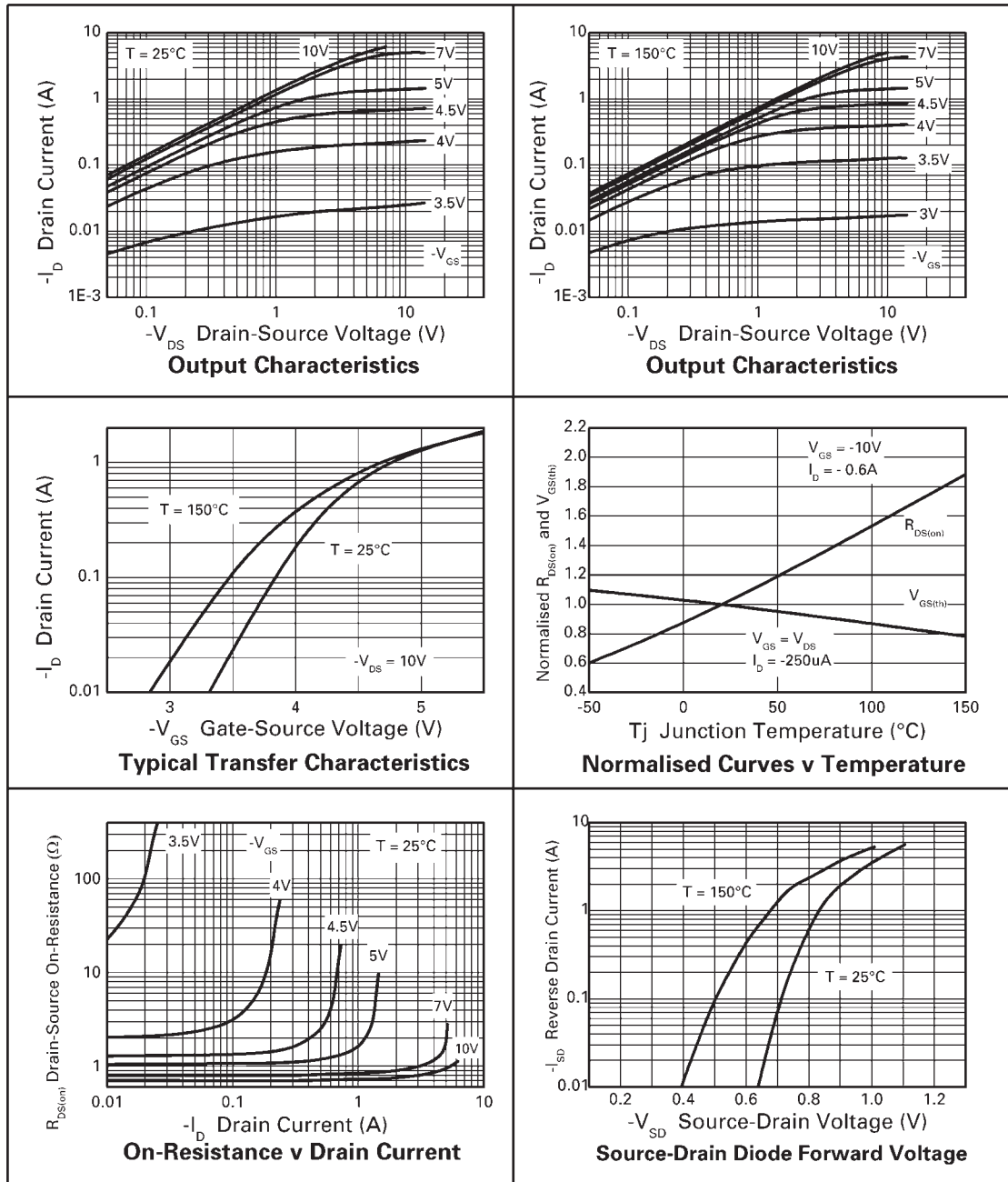
PARAMETER	SYMBOL	MIN.	TYP.	MAX.	UNIT	CONDITIONS
STATIC						
Drain-Source Breakdown Voltage	V _{(BR)DSS}	-100			V	I _D = -250μA, V _{GS} =0V
Zero Gate Voltage Drain Current	I _{DSS}			-1.0	μA	V _{DS} = -100V, V _{GS} =0V
Gate-Body Leakage	I _{GSS}			100	nA	V _{GS} =±20V, V _{DS} =0V
Gate-Source Threshold Voltage	V _{GS(th)}	-2.0		-4.0	V	I _D = -250μA, V _{DS} =V _{GS}
Static Drain-Source On-State Resistance ⁽¹⁾	R _{DS(on)}			1	Ω	V _{GS} = -10V, I _D = -0.6A
				1.45	Ω	V _{GS} = -6V, I _D = -0.5A
Forward Transconductance ⁽¹⁾⁽³⁾	g _{fs}		1.2		S	V _{DS} = -15V, I _D = -0.6A
DYNAMIC ⁽³⁾						
Input Capacitance	C _{iss}		141		pF	V _{DS} = -50V, V _{GS} =0V f=1MHz
Output Capacitance	C _{oss}		13.1		pF	
Reverse Transfer Capacitance	C _{rss}		10.8		pF	
SWITCHING ^{(2) (3)}						
Turn-On Delay Time	t _{d(on)}		1.6		ns	V _{DD} = -50V, I _D = -1A R _G ≅ 6.0Ω, V _{GS} = -10V
Rise Time	t _r		2.1		ns	
Turn-Off Delay Time	t _{d(off)}		5.9		ns	
Fall Time	t _f		3.3		ns	
Gate Charge	Q _g		1.8		nC	V _{DS} = -50V, V _{GS} = -5V I _D = -0.6A
Total Gate Charge	Q _g		3.5		nC	V _{DS} = -50V, V _{GS} = -10V I _D = -0.6A
Gate-Source Charge	Q _{gs}		0.6		nC	
Gate-Drain Charge	Q _{gd}		1.6		nC	
SOURCE-DRAIN DIODE						
Diode Forward Voltage ⁽¹⁾	V _{SD}		-0.85	-0.95	V	T _J =25°C, I _S = -0.75A, V _{GS} =0V
Reverse Recovery Time ⁽³⁾	t _{rr}		29		ns	T _J =25°C, I _S = -0.9A,
Reverse Recovery Charge ⁽³⁾	Q _{rr}		31		nC	di/dt=100A/μs

NOTES

- (1) Measured under pulsed conditions. Pulse width $\leq 300\text{ms}$; duty cycle $\leq 2\%$.
 (2) Switching characteristics are independent of operating junction temperature.
 (3) For design aid only, not subject to production testing.

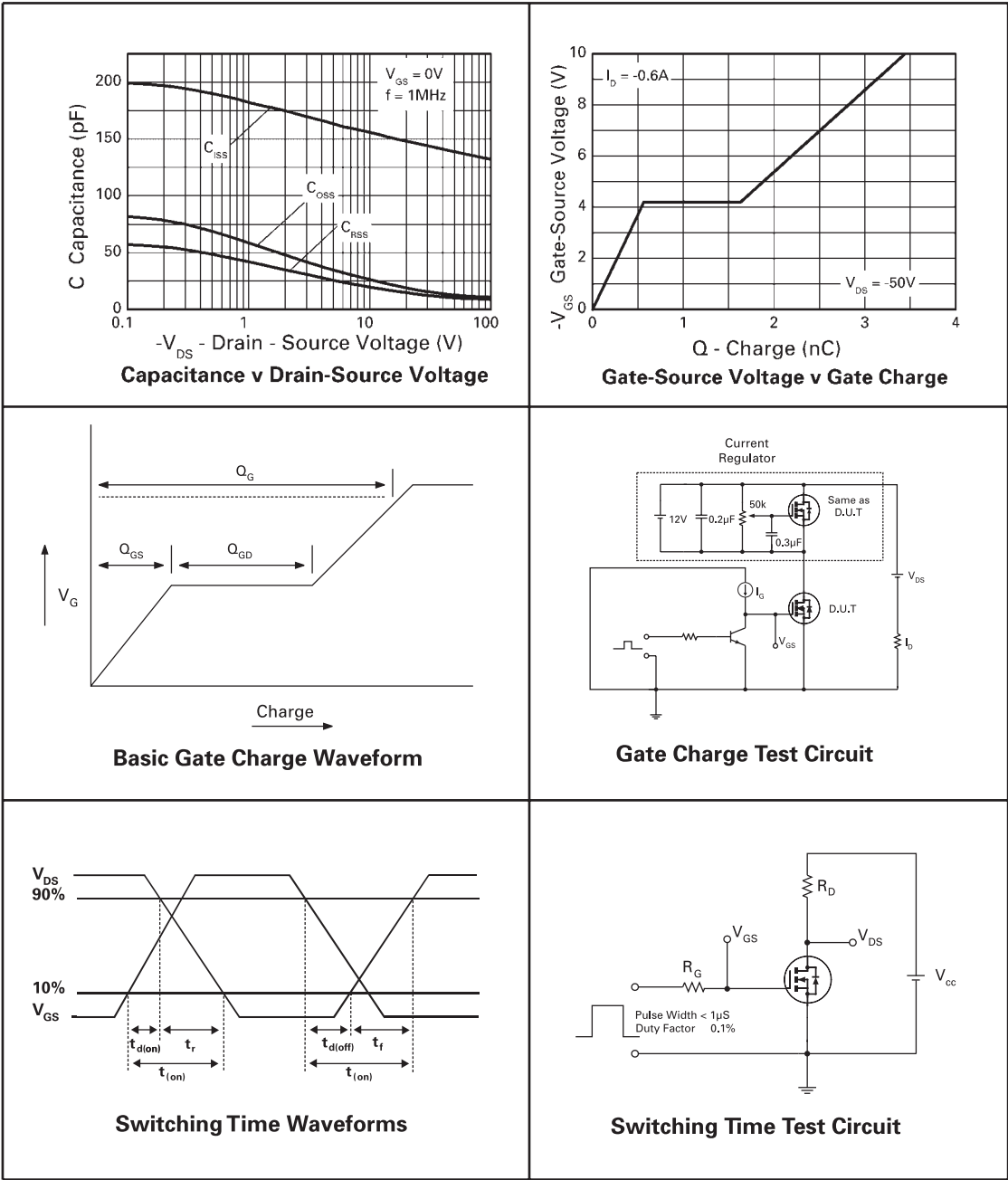
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TYPICAL CHARACTERISTICS



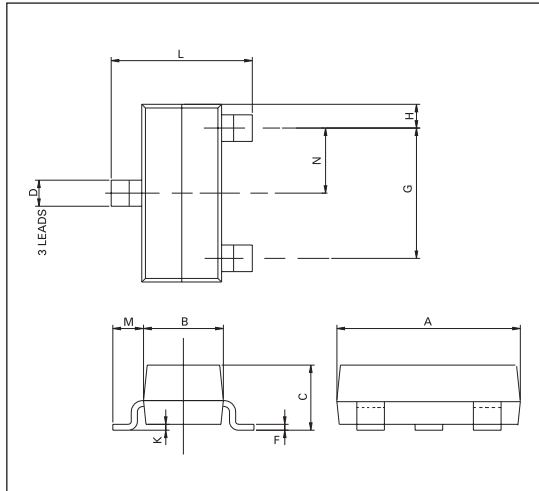
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TYPICAL CHARACTERISTICS

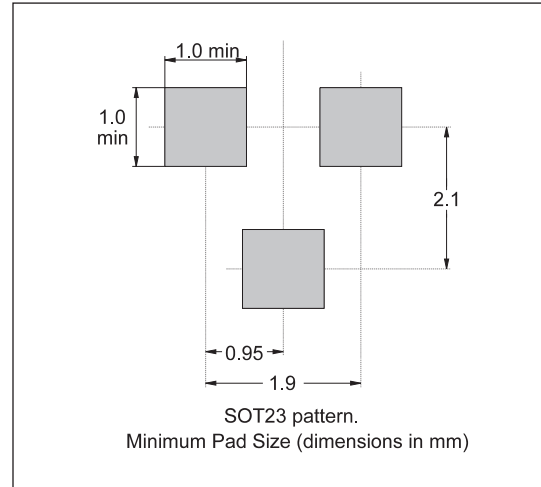


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PACKAGE OUTLINE



PAD LAYOUT



Controlling dimensions are in millimetres. Approximate conversions are given in inches

PACKAGE DIMENSIONS

DIM	MILLIMETRES		INCHES		DIM	MILLIMETRES		INCHES	
	MIN	MAX	MIN	MAX		MIN	MAX	MIN	MAX
A	2.67	3.05	0.105	0.120	H	0.33	0.51	0.013	0.020
B	1.20	1.40	0.047	0.055	K	0.01	0.10	0.0004	0.004
C	—	1.10	—	0.043	L	2.10	2.50	0.083	0.0985
D	0.37	0.53	0.015	0.021	M	0.45	0.64	0.018	0.025
F	0.085	0.15	0.0034	0.0059	N	0.95 NOM		0.0375 NOM	
G	1.90 NOM		0.075 NOM		Θ	10° TYP		10° TYP	

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