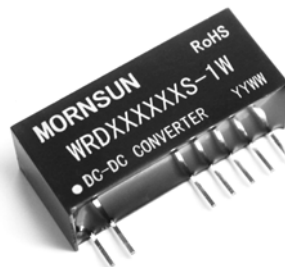


WRD_S-1W Series

WIDE INPUT ISOLATED & REGULATED
1W OUTPUT TWIN OUTPUT
MINIATURE SIP PACKAGE



RoHS

multi-country patent protection

FEATURES

- Wide (2:1) Input Range
- Efficiency to 81%
- Operating Temperature: -40°C~+85°C
- 1KVDC Isolation
- Twin Output
- UL94-V0 Package
- No Heat Sink Required
- Industry Standard Pin out
- MTBF>1,000,000 hours
- Custom Service Available
- RoHS Compliance

APPLICATIONS

The WRD_S-1W Series are specially designed for applications where a wide range input voltage power supplies are isolated from the input power supply in a distributed power supply system on a circuit board.

These products apply to:

- 1) Where the voltage of the input power supply is wide range (voltage range: 2:1);
- 2) Where isolation is necessary between input and output (Isolation Voltage =1000VDC);
- 3) Where the regulation of the output voltage and the output ripple noise are demanded.

PRODUCT PROGRAM

Part Number	Input			Output			Efficiency (%, Typ)	Package Style
	Voltage (VDC)			Vo1,V02 (VDC)	Io1,Io2 (mA)			
	Nominal	Range	Max*		Max	Min		
WRD050505S-1W	5	4.5~9VDC	11	5	100	10	67	SIP
WRD050909S-1W	5	4.5~9VDC	11	9	55	5	71	SIP
WRD051212S-1W	5	4.5~9VDC	11	12	42	4	75	SIP
WRD051515S-1W	5	4.5~9VDC	11	15	33	3	70	SIP
WRD052424S-1W	5	4.5~9VDC	11	24	20	2	70	SIP
WRD120505S-1W	12	9~18VDC	22	5	100	10	74	SIP
WRD120909S-1W	12	9~18VDC	22	9	55	5	78	SIP
WRD121212S-1W	12	9~18VDC	22	12	42	4	78	SIP
WRD121515S-1W	12	9~18VDC	22	15	33	3	77	SIP
WRD122424S-1W	12	9~18VDC	22	24	20	2	75	SIP
WRD240505S-1W	24	18~36VDC	40	5	100	10	75	SIP
WRD240909S-1W	24	18~36VDC	40	9	55	5	78	SIP
WRD241212S-1W	24	18~36VDC	40	12	42	4	80	SIP
WRD241515S-1W	24	18~36VDC	40	15	33	3	81	SIP
WRD242424S-1W	24	18~36VDC	40	24	20	2	78	SIP
WRD480505S-1W	48	36~72VDC	80	5	100	10	73	SIP
WRD480909S-1W	48	36~72VDC	80	9	55	5	76	SIP
WRD481212S-1W	48	36~72VDC	80	12	42	4	78	SIP
WRD481515S-1W	48	36~72VDC	80	15	33	3	78	SIP
WRD482424S-1W	48	36~72VDC	80	24	20	2	80	SIP

ISOLATION SPECIFICATIONS

Item	Test conditions	Min	Typ	Max	Units
Isolation voltage	Flash tested for 60 seconds	1000			VDC
Isolation resistance	Test at 500VDC	1000			MΩ

OUTPUT SPECIFICATIONS

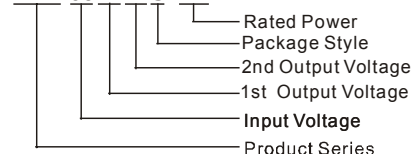
Item	Test Conditions	Min	Typ	Max	Units
1W Output Power	See Below Products Program	0.1		1	W
Output Voltage Accuracy	Refer To Recommended Circuit		±1	±3	%
Load Regulation	From 10% To 100% Load		±0.5	±1	
Line Regulation	Input Voltage From Low To High		±0.2	±0.5	
Temperature Drift(Vout)	Refer To Recommended Circuit			0.03	%/°C
Ripple	20Hz-300KHz Bandwidth			50	mVp-p
Noise	DC-20MHz Bandwidth		50	100	
Switching Frequency	100% Load, Nominal Input Voltage	100		650	KHz

Note:

- 1.All specifications measured at T_A=25°C, humidity<75%, nominal input voltage and rated output load unless otherwise specified.
- 2.See below recommended circuits for more details.

MODEL SELECTION

WRD051212S-1W



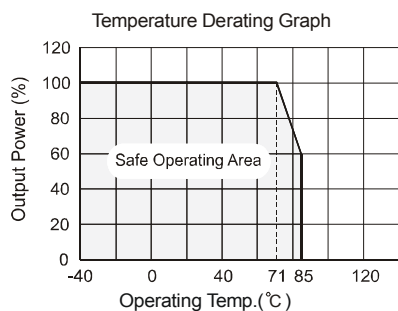
MORNSUN Science& Technology Ltd.

Address: 8th floor 8th building, Huangzhou Industrial District, Guangzhou, China
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Fax: 86-20-38601272
Http://www.mornsun-power.com

COMMON SPECIFICATION

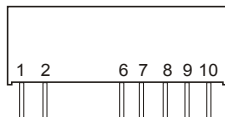
Output Short Circuit Protection	Continuous
Temperature Rise at Full Load	30°C (TYP)
Cooling	Free Air Convection
No-load Power Consumption	100mW (typical)
Operating Temperature Range	-40°C~+85°C
Storage Temperature Range	-55°C~+125°C
Lead Temperature***	300°C Max.
Storage Humidity Range	≤ 95%
Case Material	Plastic (UL94-V0)
MTBF	>1,000,000 hours
***Lead Temperature 1.5mm from case for 10 seconds.	

TYPICAL CHARECTERISTICS



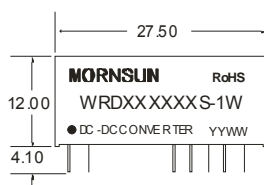
FOOTPRINT DETAILS

Pin	Function
1	GND
2	Vin
6	Voi
7	0V1
8	CS
9	0V2
10	V02



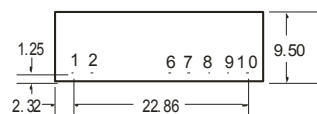
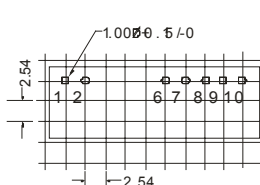
OUTLINE DIMENSIONS & RECOMMENDED FOOTPRINT

WRDXXXXS-1W Package



Side View

WRDXXXXXS-1W Footprint



Bottom View

Note: All Pins on a 2.54mm pitch; All Pin diameters are 0.50 mm(Tolerance:±0.10); all dimensions in mm.

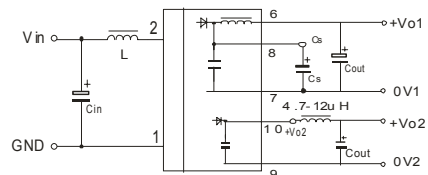
APPLICATION NOTE

Recommended Circuit

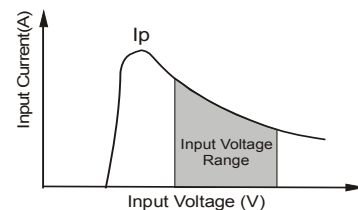
All the WRD_S-1W Series have been tested according to the following recommended testing circuit before leaving factory. This series should be tested under load. Never be tested under no load (See Figure 1). If you want to further decrease the input/output ripple, you can increase capacitance properly or choose capacitors with low ESR. However, the capacitance should not be too high.(See table 2).If you want to use the products in high EMI, please choose our metal packaged products.

CS Capacitor Table(Table 1)

Vout	5V	9V	12V	15V	24V
CS	47uF-100uF		22uF-47uF		4.7 uF (max)



(Figure 1)



(Figure 2)

CS Pin

By connecting a low ESR capacitor between this terminal and the pin-7 (connecting to the anode of the capacitor), the output ripple and noise may be further improved. Generally, the capacitance is no greater than 100uF

Input Current

Nominal input voltage range. The input current of the power supply must be sufficient to the startup current (Ip) of the DC/DC module (see Figure 2)

Output Load

In order to ensure the product operate efficiently and reliably, in addition to a max load (namely full load), a minimum load is specified for this kind of DC/DC converter. Make sure the specified range of input voltage is not exceeded, the minimum output load **no less than 10% full load**. If the actual load is less than the specified minimum load, the output ripple will increase sharply while its efficiency and reliability will reduce greatly. If the actual output power is very small, a proper resistor is needed at the output end in order to increasing the load, or contact our company for other lower output power products.

No parallel connection or plug and play.

External Capacitor Table(Table 2)

Vin	Cin	Cout (0+70°C)	Cout (-40+85°C)
5V & 12V	100uF	100uF (electrolytic capacitor)	47uF (tantalum capacitor)
24V & 48V	10uF		



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