

## FDD8445\_F085

### N-Channel PowerTrench® MOSFET 40V, 50A, 8.7mΩ

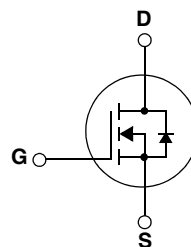
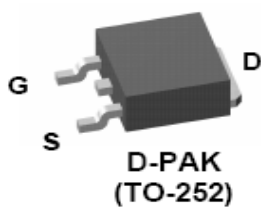
#### Features

- $R_{DS(ON)} = 6.7 \text{ m}\Omega$  (Typ),  $V_{GS} = 10\text{V}$ ,  $I_D = 50\text{A}$
- $Q_{g(10)} = 45\text{nC}$  (Typ),  $V_{GS} = 10\text{V}$
- Low Miller Charge
- Low  $Q_{rr}$  Body Diode
- UIS Capability (Single Pulse/ Repetitive Pulse)
- Qualified to AEC Q101
- RoHS Compliant



#### Applications

- Automotive Engine Control
- Powertrain Management
- Solenoid and Motor Drivers
- Electronic Transmission
- Distributed Power Architecture and VRMs
- Primary Switch for 12V Systems



**Absolute Maximum Ratings**  $T_c = 25^\circ\text{C}$  unless otherwise noted

Symbol	Parameter	Ratings	Units
$V_{DS}$	Drain to Source Voltage	40	V
$V_{GS}$	Gate to Source Voltage	$\pm 20$	V
$I_D$	Drain Current Continuous ( $V_{GS}=10\text{V}$ ) (Note 1)	70	A
	Continuous ( $V_{GS}=10\text{V}$ , with $R_{\theta JA} = 52^\circ\text{C/W}$ )	15.2	A
	Pulsed	Figure 4	
$E_{AS}$	Single Pulse Avalanche Energy (Note 2)	144	mJ
$P_D$	Power Dissipation	79	W
	Derate above $25^\circ\text{C}$	0.53	W/ $^\circ\text{C}$
$T_J, T_{STG}$	Operating and Storage Temperature	-55 to +175	$^\circ\text{C}$

**Thermal Characteristics**

$R_{\theta JC}$	Thermal Resistance, Junction to Case	1.9	$^\circ\text{C/W}$
$R_{\theta JA}$	Thermal Resistance, Junction to Ambient TO-252, $\text{lin}^2$ copper pad area	52	$^\circ\text{C/W}$

**Package Marking and Ordering Information**

Device Marking	Device	Package	Reel Size	Tape Width	Quantity
FDD8445	FDD8445_F085	TO-252AA	13"	12mm	2500 units

**Electrical Characteristics**  $T_J = 25^\circ\text{C}$  unless otherwise noted

Symbol	Parameter	Test Conditions	Min	Typ	Max	Units
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**Off Characteristics**

$BV_{DSS}$	Drain to Source Breakdown Voltage	$I_D = 250\mu\text{A}, V_{GS} = 0\text{V}$	40	-	-	V
$I_{DSS}$	Zero Gate Voltage Drain Current	$V_{DS} = 32\text{V}$ $V_{GS} = 0\text{V}$ $T_J = 150^\circ\text{C}$	-	-	1	$\mu\text{A}$
$I_{GSS}$	Gate to Source Leakage Current	$V_{GS} = \pm 20\text{V}$	-	-	$\pm 100$	nA

**On Characteristics**

$V_{GS(th)}$	Gate to Source Threshold Voltage	$V_{DS} = V_{GS}, I_D = 250\mu\text{A}$	2	2.8	4	V
$R_{DS(on)}$	Drain to Source On Resistance	$I_D = 50\text{A}, V_{GS} = 10\text{V}$	-	6.7	8.7	m $\Omega$
		$I_D = 50\text{A}, V_{GS} = 10\text{V},$ $T_J = 175^\circ\text{C}$	-	12.5	16.3	

**Dynamic Characteristics**

C <sub>ISS</sub>	Input Capacitance	V <sub>DS</sub> = 25V, V <sub>GS</sub> = 0V, f = 1MHz		-	3040	4050	pF
C <sub>OSS</sub>	Output Capacitance			-	295	390	pF
C <sub>RSS</sub>	Reverse Transfer Capacitance			-	178	270	pF
R <sub>G</sub>	Gate Resistance	f = 1MHz		-	1.7	-	Ω
Q <sub>g(TOT)</sub>	Total Gate Charge at 10V	V <sub>GS</sub> = 0 to 10V	V <sub>DD</sub> =20V, I <sub>D</sub> = 50A	-	45	59	nC
Q <sub>g(5)</sub>	Total Gate Charge at 5V	V <sub>GS</sub> = 0 to 5V		-	17	22	nC
Q <sub>g(TH)</sub>	Threshold Gate Charge	V <sub>GS</sub> = 0 to 2V		-	5.8	7.6	nC
Q <sub>gs</sub>	Gate to Source Gate Charge			-	12.5	-	nC
Q <sub>gs2</sub>	Gate Charge Threshold to Plateau			-	9.5	-	nC
Q <sub>gd</sub>	Gate to Drain “Miller” Charge			-	10.5	-	nC

**Electrical Characteristics**  $T_J = 25^\circ\text{C}$  unless otherwise noted

Symbol	Parameter	Test Conditions	Min	Typ	Max	Units
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**Switching Characteristics**

$t_{(on)}$	Turn-On Time	$V_{DD} = 20\text{V}, I_D = 50\text{A}$ $V_{GS} = 10\text{V}, R_{GS} = 2\Omega$	-	-	138	ns
$t_{d(on)}$	Turn-On Delay Time		-	10	-	ns
$t_r$	Turn-On Rise Time		-	82	-	ns
$t_{d(off)}$	Turn-Off Delay Time		-	26	-	ns
$t_f$	Turn-Off Fall Time		-	9.6	-	ns
$t_{off}$	Turn-Off Time		-	-	53	ns

**Drain-Source Diode Characteristics**

$V_{SD}$	Source to Drain Diode Voltage	$I_{SD}=50\text{A}$	-	-	1.25	V
		$I_{SD}=25\text{A}$	-	-	1.0	V
$t_{rr}$	Reverse Recovery Time	$I_F = 50\text{A}, dI_F/dt=100\text{A}/\mu\text{s}$	-	-	39	ns
$Q_{rr}$	Reverse Recovery Charge	$I_F = 50\text{A}, dI_F/dt=100\text{A}/\mu\text{s}$	-	-	38	nC

**Notes:**

- 1: Maximum package current capability is 50A.  
 2: Starting  $T_J = 25^\circ\text{C}$ ,  $L=0.18\text{mH}$ ,  $I_{AS}=40\text{A}$ .

This product has been designed to meet the extreme test conditions and environment demanded by the automotive industry. For a copy of the requirements, see AEC Q101 at: <http://www.aecouncil.com/>  
 All Fairchild Semiconductor products are manufactured, assembled and tested under ISO9000 and QS9000 quality systems certification.

## Typical Characteristics

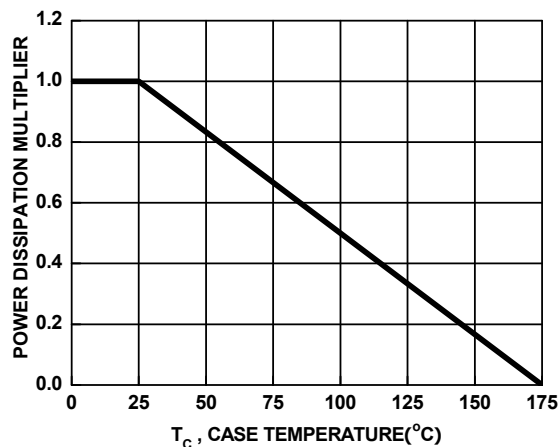


Figure 1. Normalized Power Dissipation vs Case Temperature

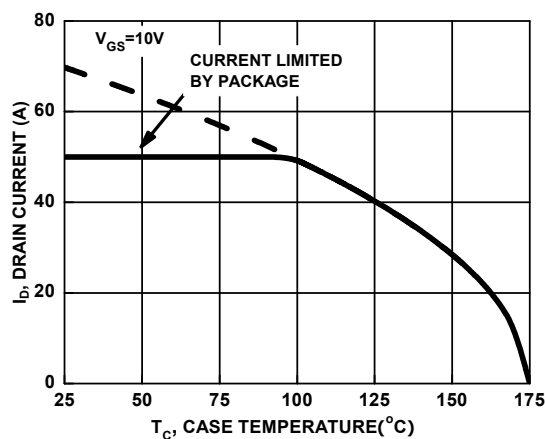


Figure 2. Maximum Continuous Drain Current vs Case Temperature

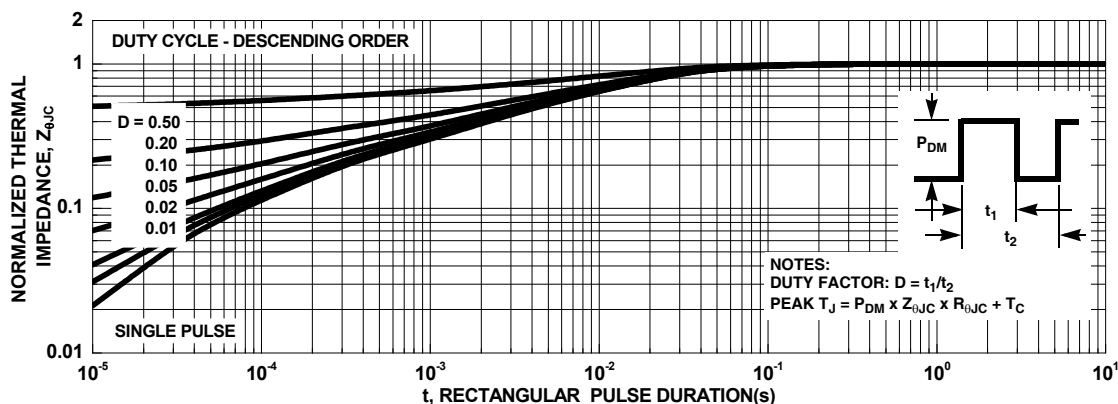


Figure 3. Normalized Maximum Transient Thermal Impedance

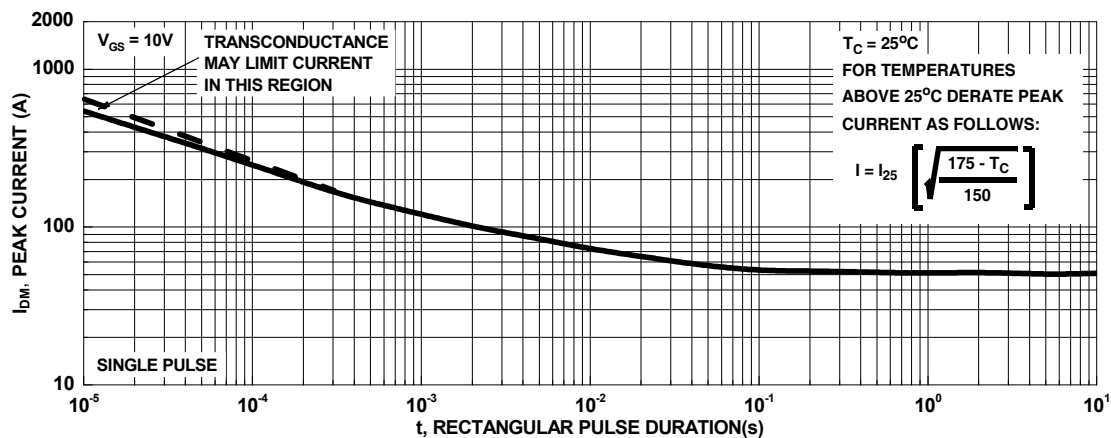


Figure 4. Peak Current Capability

## Typical Characteristics

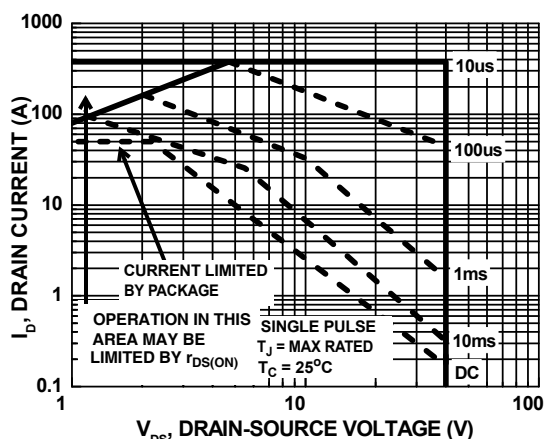
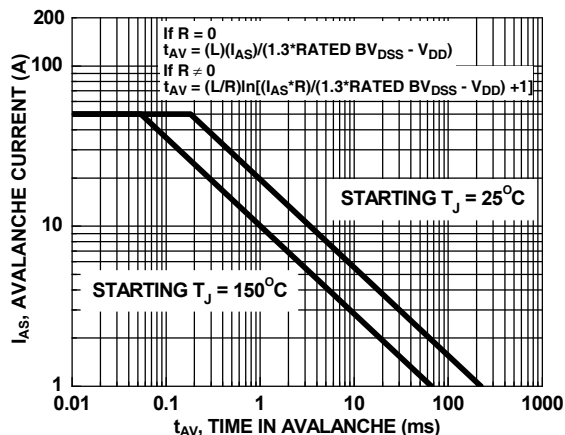


Figure 5. Forward Bias Safe Operating Area



NOTE: Refer to Fairchild Application Notes AN7514 and AN7515

Figure 6. Unclamped Inductive Switching Capability

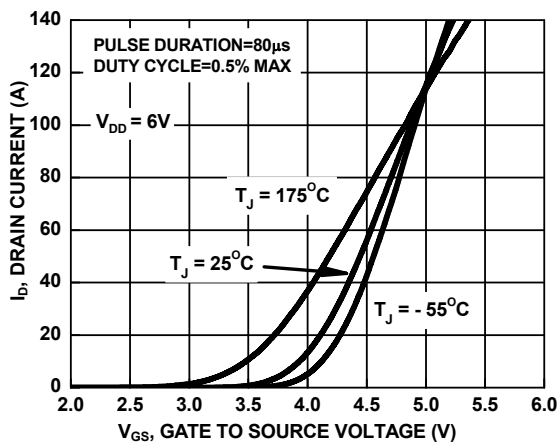


Figure 7. Transfer Characteristics

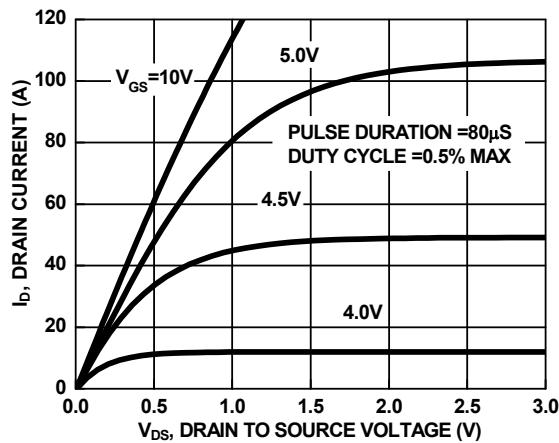


Figure 8. Saturation Characteristics

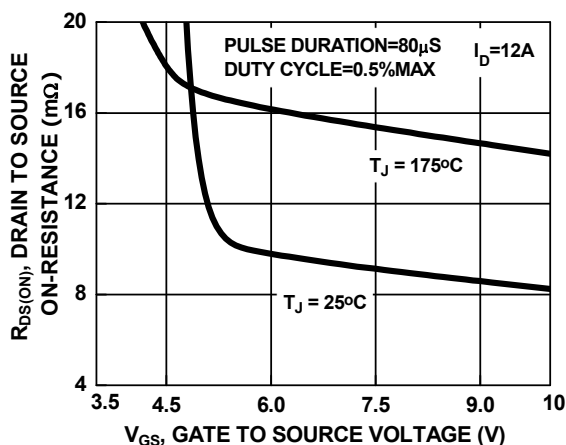


Figure 9. Drain to Source On-Resistance Variation vs. Gate to Source Voltage

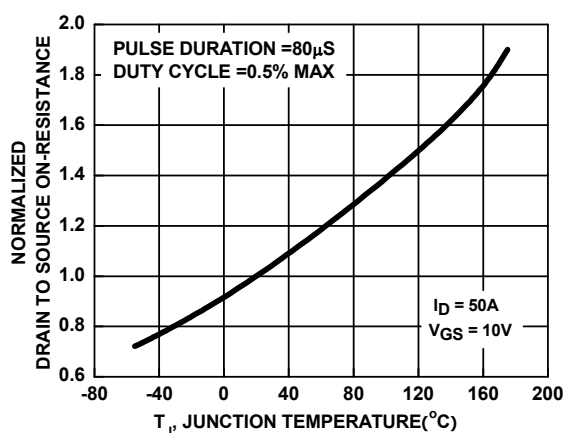


Figure 10. Normalized Drain to Source On-Resistance vs. Junction Temperature

## Typical Characteristics

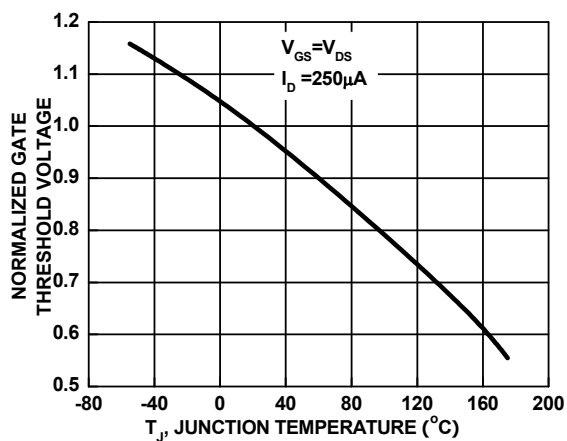


Figure 11. Normalized Gate Threshold Voltage vs Junction Temperature

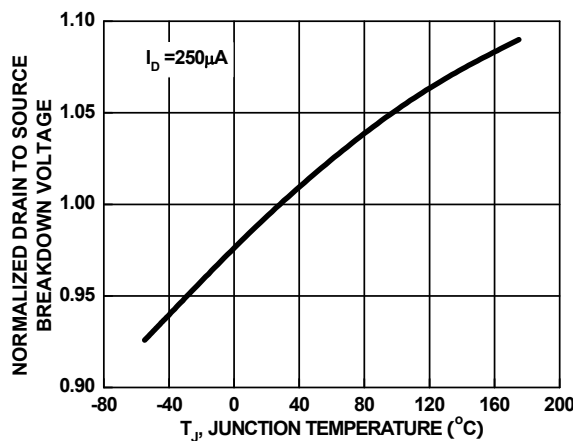


Figure 12. Normalized Drain to Source Breakdown Voltage vs Junction Temperature

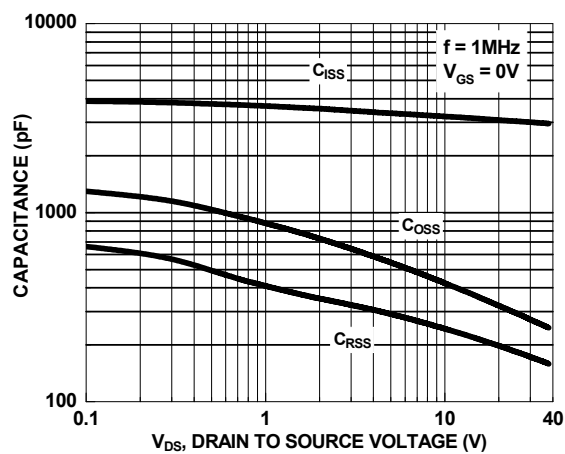


Figure 13. Capacitance vs Drain to Source Voltage

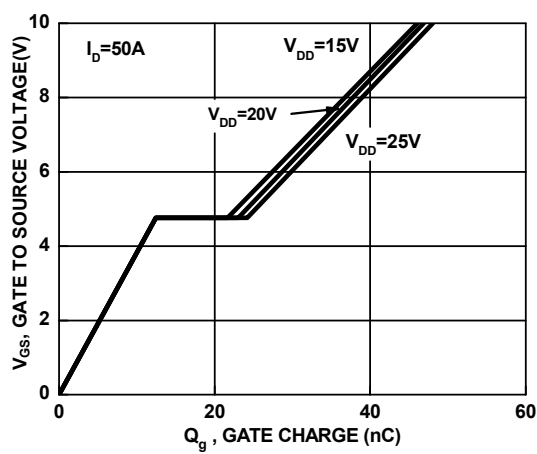


Figure 14. Gate Charge vs Gate to Source Voltage



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