

RoHS Compliant Product
 A suffix of "-C" specifies halogen & lead-free

DESCRIPTION

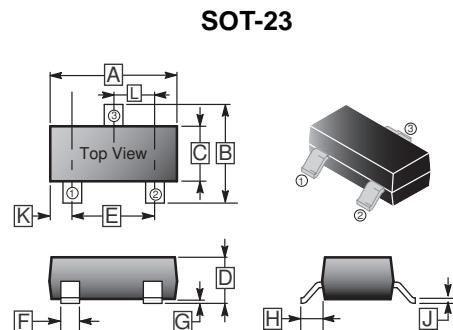
The SMS2312 provide the designer with the best combination of fast switching, ruggedized device design, low on-resistance and cost-effectiveness. The SOT-23 package is universally preferred for all commercial-industrial surface mount applications and suited for low voltage applications such as DC/DC converters.

FEATURES

- Lower Gate Charge
- Simple Drive Requirement
- Fast Switching Characteristic

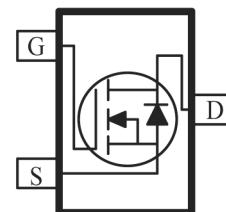
MARKING

S12



REF.	Millimeter		REF.	Millimeter	
	Min.	Max.		Min.	Max.
A	2.70	3.10	G	0	0.18
B	2.10	2.95	H	0.55	REF.
C	1.20	1.7	J	0.08	0.20
D	0.89	1.3	K	0.6	REF.
E	1.70	2.3	L	0.95	BSC.
F	0.30	0.50			

Top View



PACKAGE INFORMATION

Package	MPQ	Leader Size
SOT-23	3K	7 inch

ABSOLUTE MAXIMUM RATINGS ($T_A=25^\circ\text{C}$ unless otherwise specified)

Parameter	Symbol	Rating	Unit
Drain-Source Voltage	V_{DS}	20	V
Gate-Source Voltage	V_{GS}	± 8	V
Continuous Drain Current ¹	I_D	5	A
Pulsed Drain Current ³	I_{DM}	20	A
Maximum Power Dissipation ¹	P_D	1.4	W
		0.9	
Thermal Resistance Junction-Ambient	$R_{\theta JA}^1$	$t \leq 10\text{s}, 89$	$^\circ\text{C} / \text{W}$
	$R_{\theta JA}^2$	357	
Operating Junction & Storage Temperature	T_J, T_{STG}	150, -55~150	$^\circ\text{C}$

ELECTRICAL CHARACTERISTICS ($T_A=25^\circ C$ unless otherwise specified)

Parameter	Symbol	Min.	Typ.	Max.	Unit	Test Conditions
Static						
Drain-Source Breakdown Voltage	BV_{DSS}	20	-	-	V	$V_{GS}=0$, $I_D=250\mu A$
Gate-Threshold Voltage	$V_{GS(th)}$	0.45	-	1	V	$V_{DS}=V_{GS}$, $I_D=250\mu A$
Gate-Source Leakage Current	I_{GSS}	-	-	± 100	nA	$V_{GS} = \pm 8V$, $V_{DS}=0$
Drain-Source Leakage Current	I_{DS}	-	-	1	μA	$V_{DS}=20V$, $V_{GS}=0$
Forward Transfer conductance	g_{fs}	6	-	-	S	$V_{DS}=10V$, $I_D=5A$
Diode Forward Voltage ⁴	V_{SD}	-	0.75	1.2	V	$I_S=4A$, $V_{GS}=0$
Static Drain-Source On-Resistance ⁴	$R_{DS(ON)}$	-	-	32	mΩ	$V_{GS}=4.5V$, $I_D=5A$
		-	-	36		$V_{GS}=2.5V$, $I_D=4.7A$
		-	-	42		$V_{GS}=1.8V$, $I_D=4.3A$
Switching Parameters						
Input Capacitance	C_{iss}	-	865	-	pF	$V_{GS}=0$ $V_{DS}=10V$ $f=1.0MHz$
Output Capacitance	C_{oss}	-	105	-		
Reverse Transfer Capacitance	C_{rss}	-	55	-		
Turn-on Delay Time	$T_{d(on)}$	-	10	-	nS	$V_{DD}=10V$ $V_{GEN}=5V$ $R_G=1\Omega$ $R_L=2.2\Omega$ $I_D=4A$
Rise Time	T_r	-	20	-		
Turn-off Delay Time	$T_{d(off)}$	-	32	-		
Fall Time	T_f	-	12	-		
Gate Resistance	R_g	0.5	-	4.8	Ω	$f=1.0MHz$

Notes:

1. . The data tested by surface mounted on a 1 inch² FR4 board with 2OZ copper.
2. Surface mounted on min. copper pad.
3. Pulse width limited by Max. junction temperature.
4. Pulse Test : Pulse Width≤300μs, Duty Cycle ≤ 2%.

CHARACTERISTIC CURVES

