TGA2622-CP

9 – 10 GHz 35 W GaN Power Amplifier

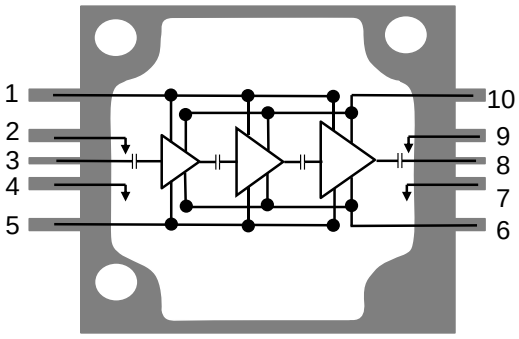
Applications

- Weather and Marine Radar

Product Features

- Frequency Range: 9 – 10 GHz
- P_{SAT}: 45.5 dBm @ P_{IN} = 18 dBm
- PAE: >43% @ P_{IN} = 18 dBm
- Power Gain: 27.5 dB @ P_{IN} = 18 dBm
- Bias: V_D = 28 V, I_{DQ} = 290 mA, V_G = -2.7 V Typical (Pulsed V_D: PW = 100 us and DC = 10 %)
- Package Dimensions: 15.2 x 15.2 x 3.5 mm
- Package base is pure Cu offering superior thermal management

Functional Block Diagram



General Description

TriQuint's TGA2622-CP is a packaged, high power X-band amplifier fabricated on TriQuint's TQGaN25 0.25 um GaN on SiC production process. Operating from 9 – 10 GHz, the TGA2622-CP achieves 35 W saturated output powers, a power-added efficiency of greater than 43 %, and power gain of 27.5 dB.

The TGA2622-CP is packaged in a 10-lead 15x15 mm bolt-down package with a Cu base for superior thermal management. It can support a range of bias voltages and performs well under both pulsed and CW conditions. Both RF ports are internally DC blocked and matched to 50 ohms allowing for simple system integration.

The TGA2622-CP is ideally suited for both commercial and defense applications.

Lead-free and RoHS compliant.

Evaluation boards are available upon request.

Pad Configuration

Pad No.	Symbol
1, 5	V_G
2, 4, 7, 9	GND
3	RF In
6, 10	V_D
8	RF Out

Ordering Information

Part	ECCN	Description
TGA2622-CP	3A001.b.2.b	9 – 10 GHz 35 W GaN Power Amplifier

Absolute Maximum Ratings

Parameter	Value
Drain Voltage (V_D)	40 V
Gate Voltage Range (V_G)	-8 to 0V
Drain Current (I_D)	4.3 A
Gate Current (I_G)	-11 to 45 mA (1)
Power Dissipation (P_{DISS}), 85°C, CW	107 W
Input Power (P_{IN}), CW, 50Ω, V_D = 28V, 85°C	24 dBm
Input Power (P_{IN}), CW, VSWR 3:1, V_D = 28V, 85°C	24 dBm
Channel Temperature (T_{CH})	275 °C
Mounting Temperature (30 seconds)	260 °C
Storage Temperature	-55 to 150 °C

Operation of this device outside the parameter ranges given above may cause permanent damage. These are stress ratings only, and functional operation of the device at these conditions is not implied.

Notes:

(1) Max rating for I_G is at Channel Temperature (T_{CH}) of 200 °C

Recommended Operating Conditions

Parameter	Value
Drain Voltage (V_D): Pulsed	28 V
Drain Current (I_{DQ})	290 mA
Drain Current Under RF Drive (I_{D_DRIVE})	See Plots p. 6
Gate Voltage (V_G)	-2.7 V (Typ.)
Gate Current Under RF Drive (I_{G_DRIVE})	See Plots p. 6
Temperature (T_{BASE})	-40 to 85 °C

Electrical specifications are measured at specified test conditions. Specifications are not guaranteed over all recommended operating conditions.

Electrical Specifications

Test conditions unless otherwise noted: 25 ^{0}C, V_D = 28 V, I_{DQ} = 290 mA, V_G = -2.7 V Typical, Pulsed V_D: PW = 100 us, DC = 10 %

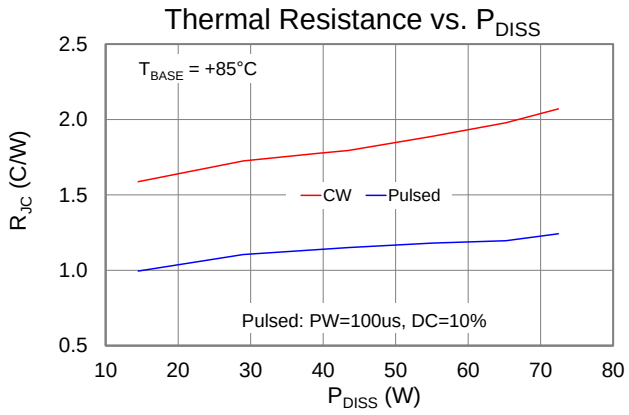
Parameter		Min	Typical	Max	Units
Operational Frequency Range		9		10	GHz
Small Signal Gain			30		dB
Input Return Loss			>11		dB
Output Return Loss			>8		dB
Output Power (Pin = 18dBm)			45.5		dBm
Power Added Efficiency (Pin = 18dBm)			>43		%
Power Gain (Pin = 18dBm)			27.5		dB
Output Power Temperature Coefficient (calculated from 25 °C to 85 °C)	Pulsed CW		-0.019 -0.023		dBm/°C
Recommended Operating Voltage:		20	28	32	V

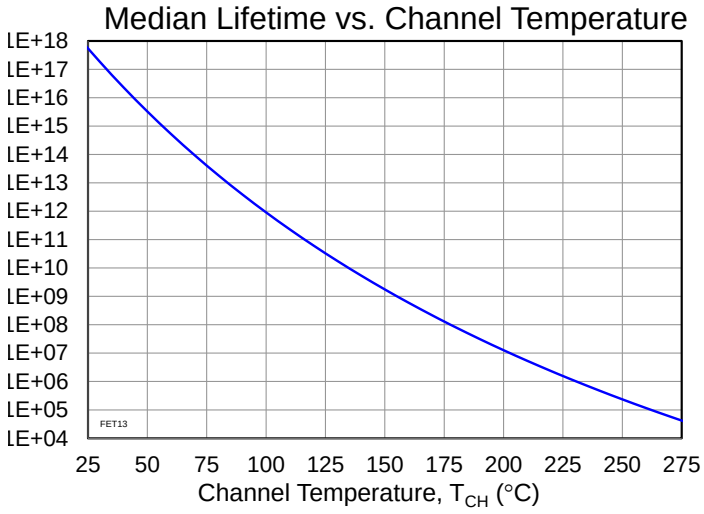
Thermal and Reliability Information

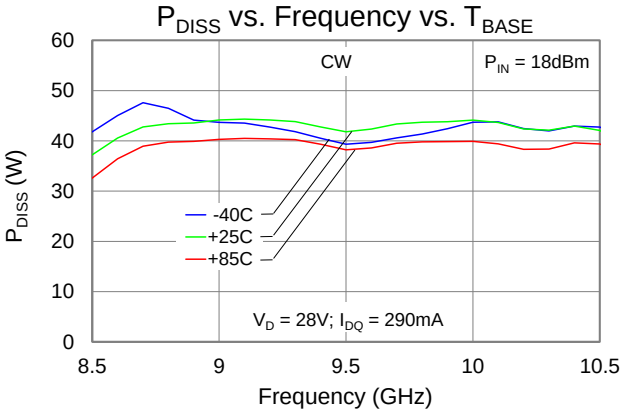
Parameter	Test Conditions	Value	Units
Thermal Resistance (θ_{JC}) (1)	V_D = 28 V, I_{DQ} = 290 mA, (Pulsed V_D : PW = 100 us, DC = 10 %), T_{base} = 85 °C, V_D = 28 V, I_{D_Drive} = 3.2 A, P_{IN} = 20 dBm, P_{OUT} = 45.6 dBm, P_{DISS} = 52 W	1.15	ºC/W
Channel Temperature (T_{CH}) (Under RF drive)		145	°C
Median Lifetime (T_M)		3.07 x 10^10	Hrs
Thermal Resistance (θ_{JC}) (1)	CW, V_D = 28 V, I_{DQ} = 290 mA, T_{base} = 85 °C, V_D = 28 V, I_{D_Drive} = 2.7 A, P_{IN} = 20 dBm, P_{OUT} = 44.8 dBm, P_{DISS} = 45 W	1.78	ºC/W
Channel Temperature (T_{CH}) (Under RF drive)		166	°C
Median Lifetime (T_M)		3.21 x 10^8	Hrs

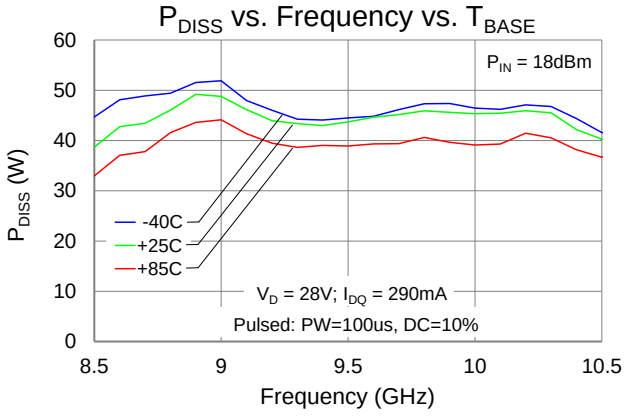
Notes:
1. Thermal Resistance measured to back of package.

Test Conditions: V_D = 40 V; Failure Criteria = 10% reduction in I_{D_MAX}



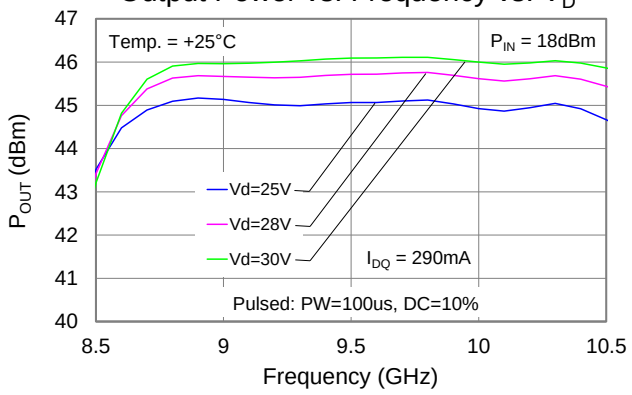




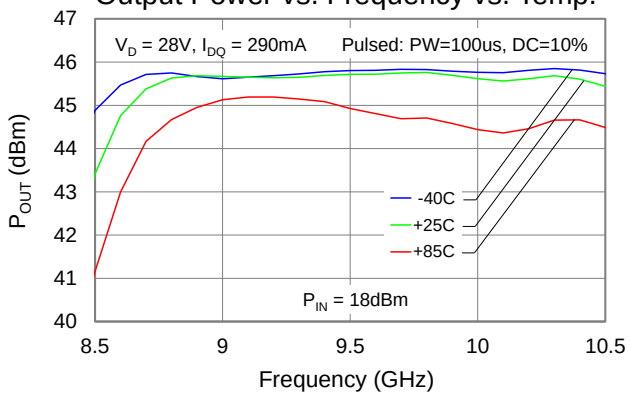


Typical Performance: Large Signal (Pulsed Operation)

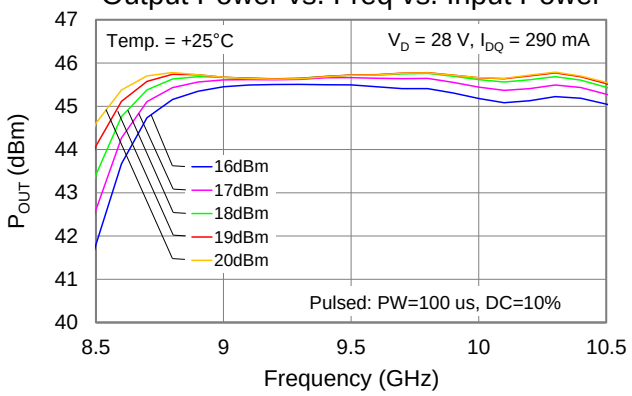
Output Power vs. Frequency vs. V_D



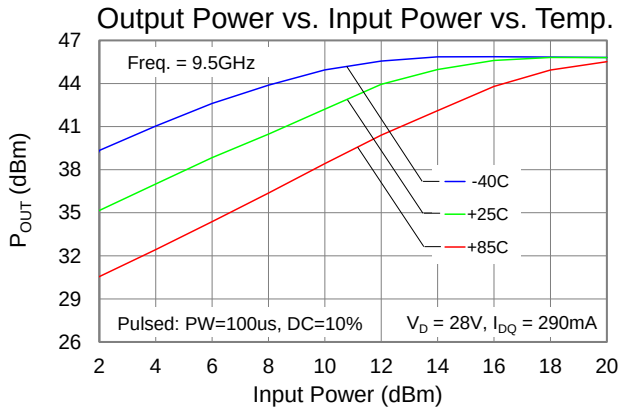
Output Power vs. Frequency vs. Temp.



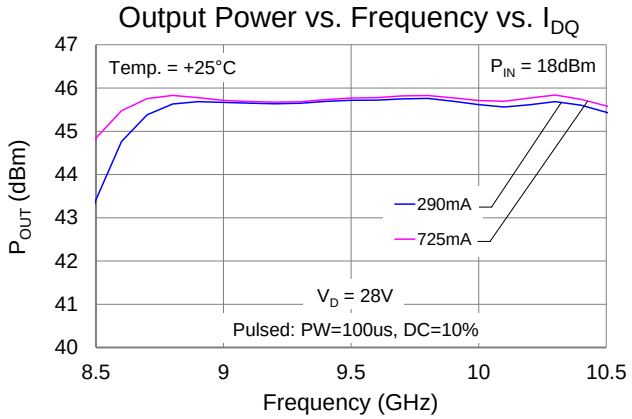
Output Power vs. Freq vs. Input Power



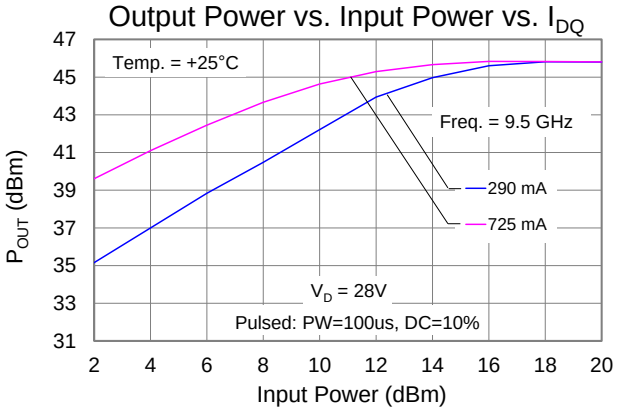
Output Power vs. Input Power vs. Temp.



Output Power vs. Frequency vs. I_{DQ}

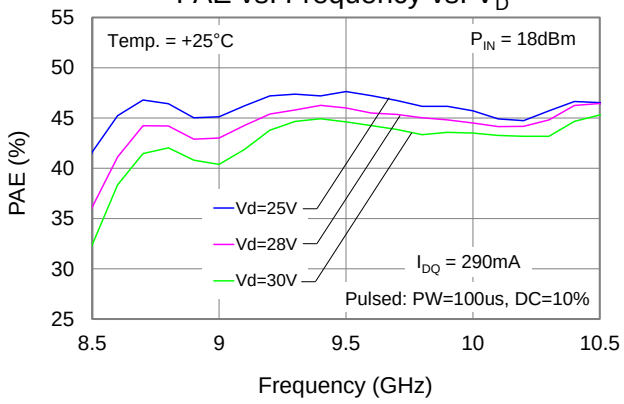


Output Power vs. Input Power vs. I_{DQ}

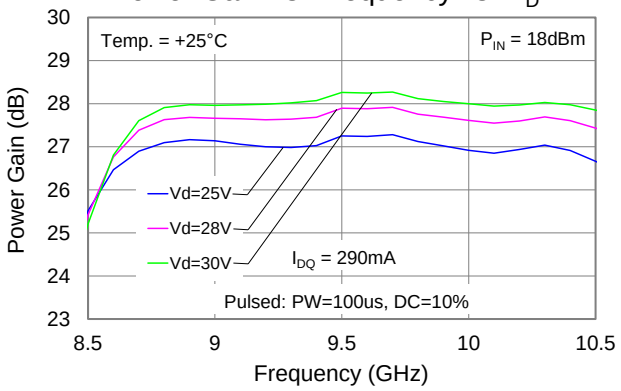


Typical Performance: Large Signal (Pulsed Operation)

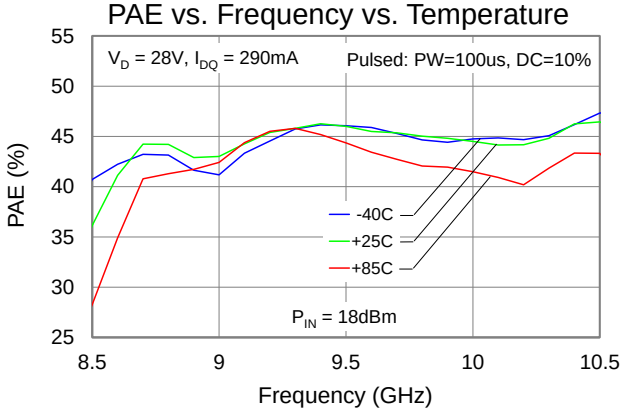
PAE vs. Frequency vs. V_D



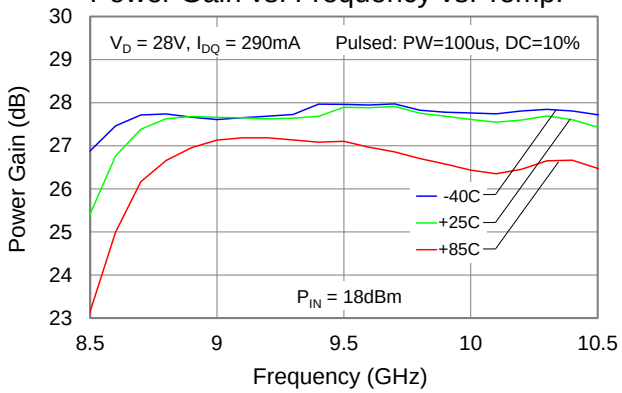
Power Gain vs. Frequency vs. V_D



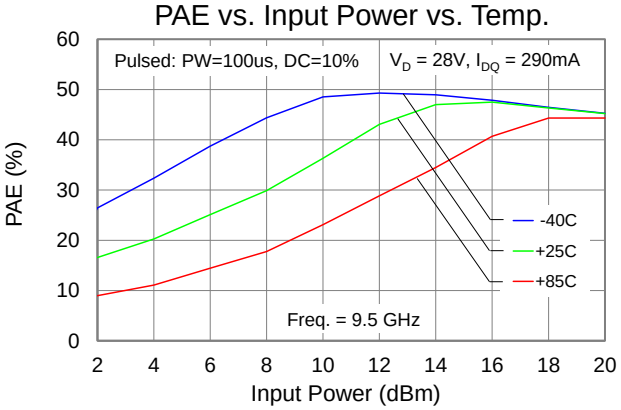
PAE vs. Frequency vs. Temperature



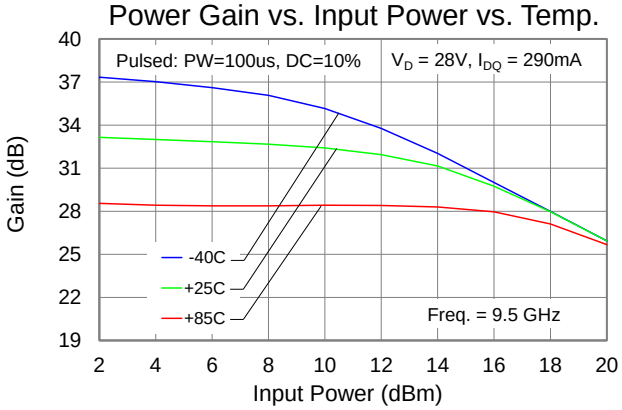
Power Gain vs. Frequency vs. Temp.



PAE vs. Input Power vs. Temp.

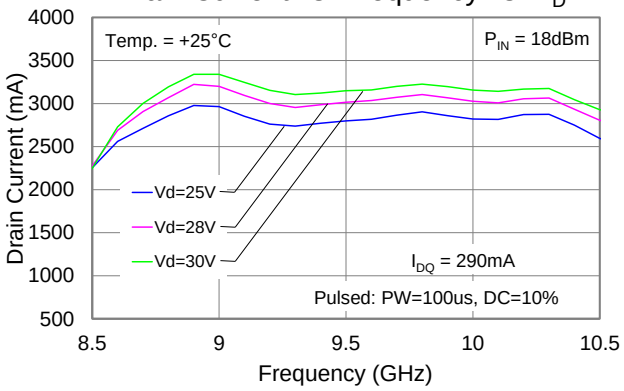


Power Gain vs. Input Power vs. Temp.

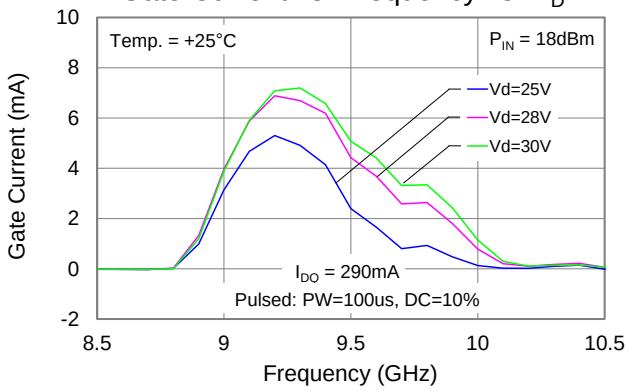


Typical Performance: Large Signal (Pulsed Operation)

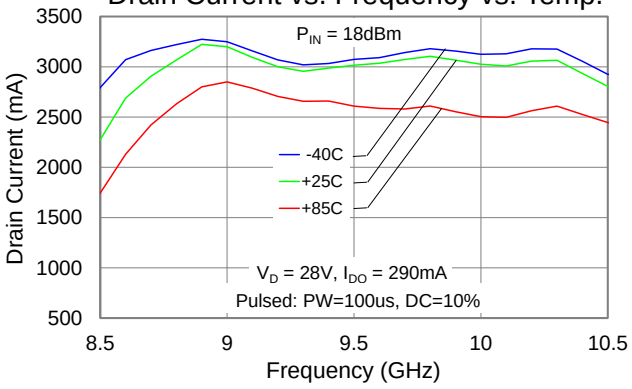
Drain Current vs. Frequency vs. V_D



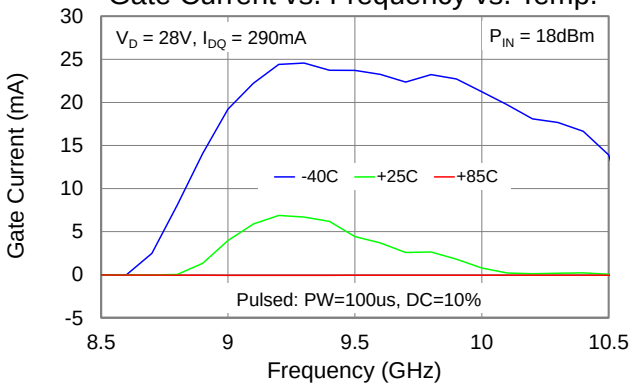
Gate Current vs. Frequency vs. V_D



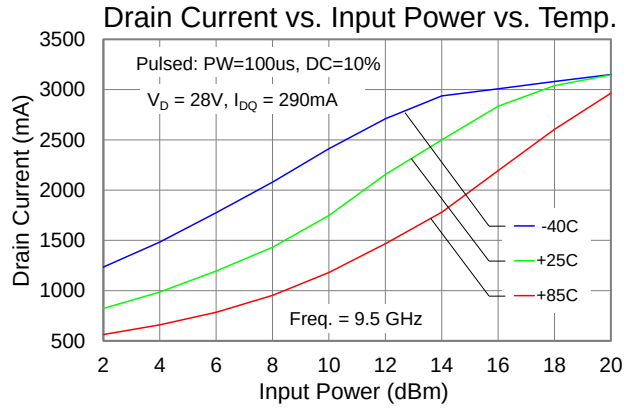
Drain Current vs. Frequency vs. Temp.



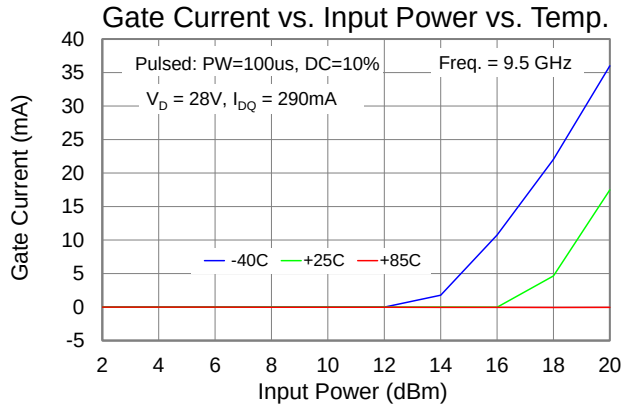
Gate Current vs. Frequency vs. Temp.



Drain Current vs. Input Power vs. Temp.

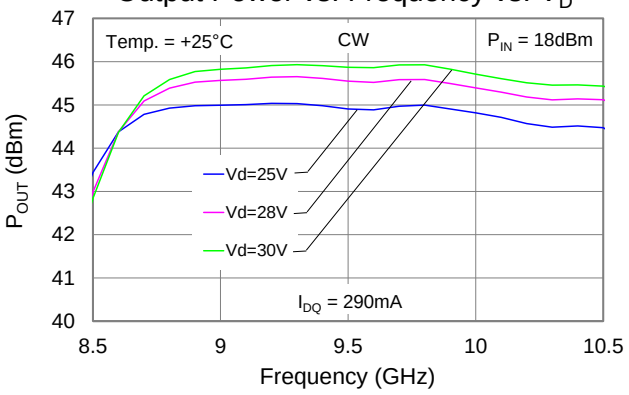


Gate Current vs. Input Power vs. Temp.

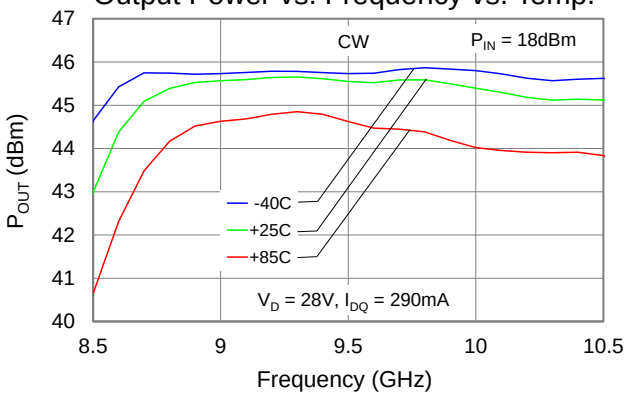


Typical Performance: Large Signal (CW Operation)

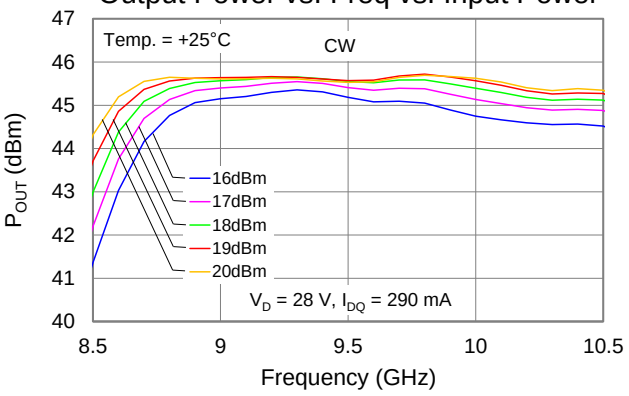
Output Power vs. Frequency vs. V_D



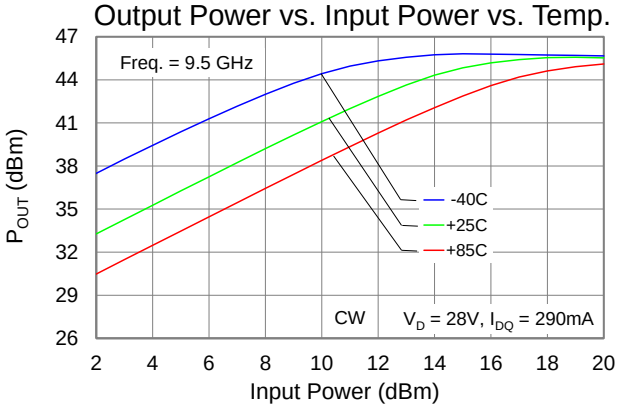
Output Power vs. Frequency vs. Temp.



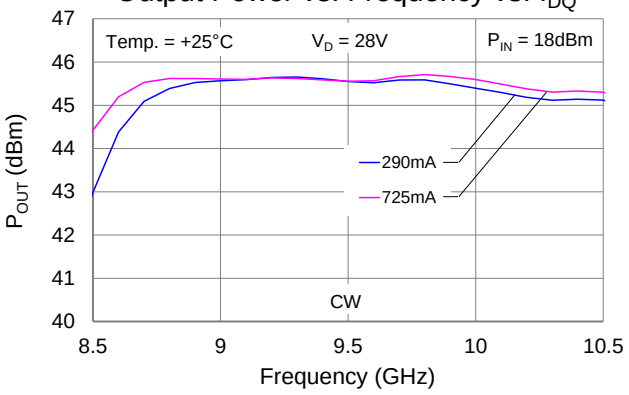
Output Power vs. Freq vs. Input Power



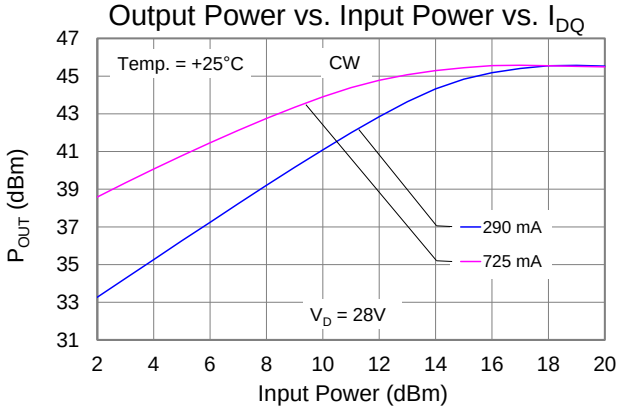
Output Power vs. Input Power vs. Temp.



Output Power vs. Frequency vs. I_{DQ}

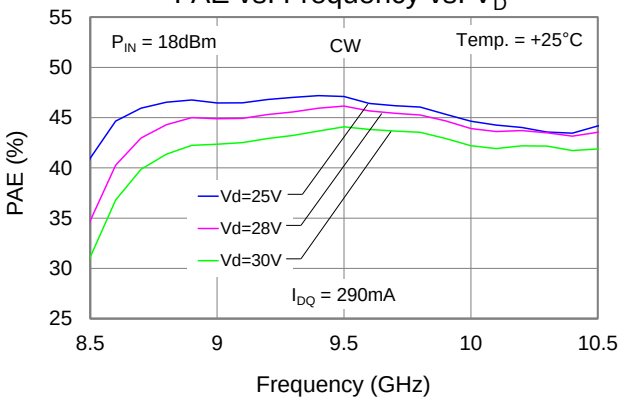


Output Power vs. Input Power vs. I_{DQ}

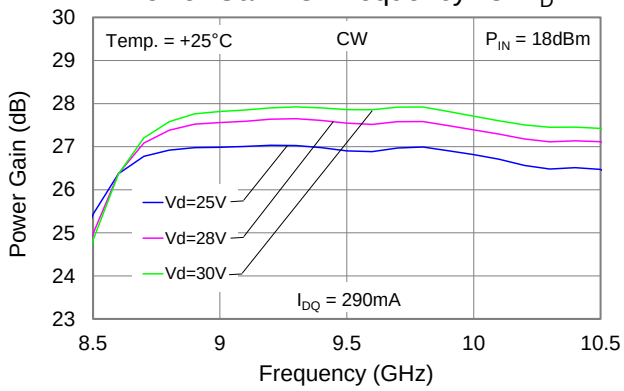


Typical Performance: Large Signal (CW Operation)

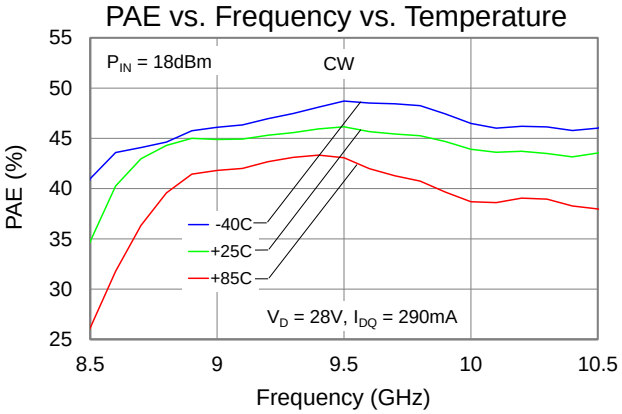
PAE vs. Frequency vs. V_D



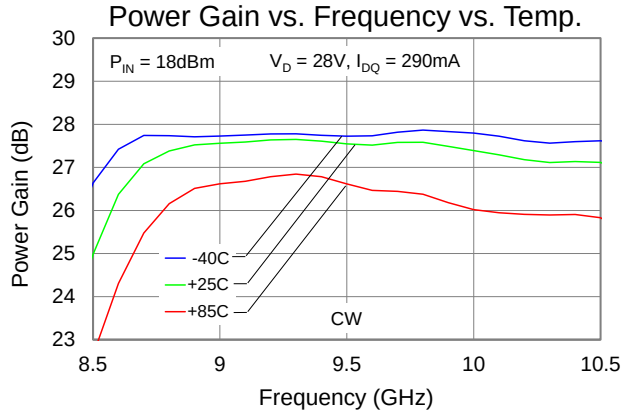
Power Gain vs. Frequency vs. V_D



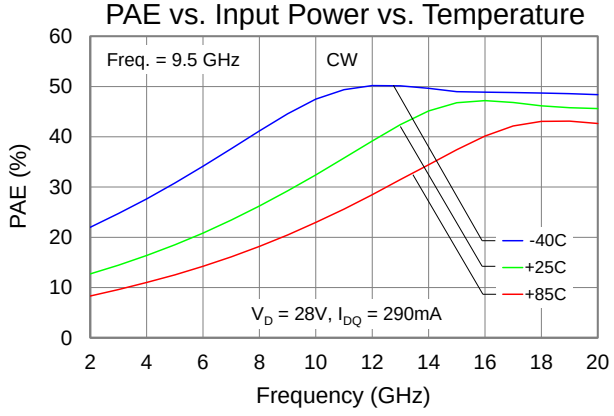
PAE vs. Frequency vs. Temperature



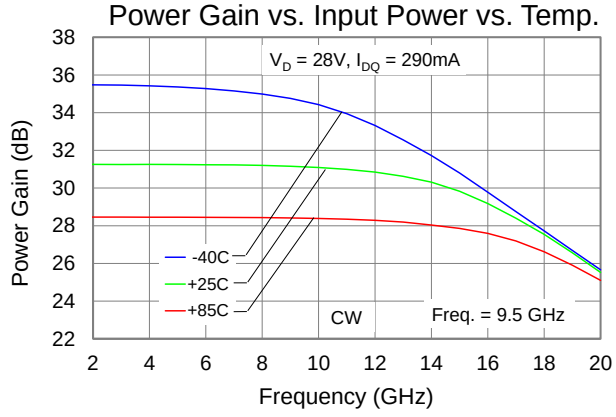
Power Gain vs. Frequency vs. Temp.



PAE vs. Input Power vs. Temperature

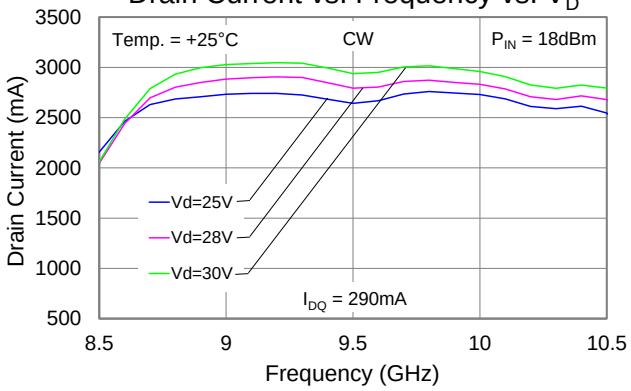


Power Gain vs. Input Power vs. Temp.

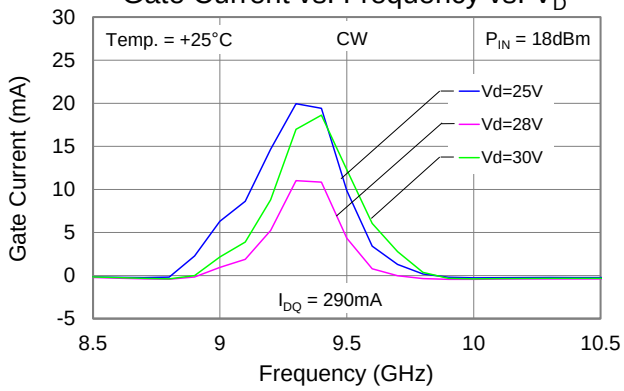


Typical Performance: Large Signal (CW Operation)

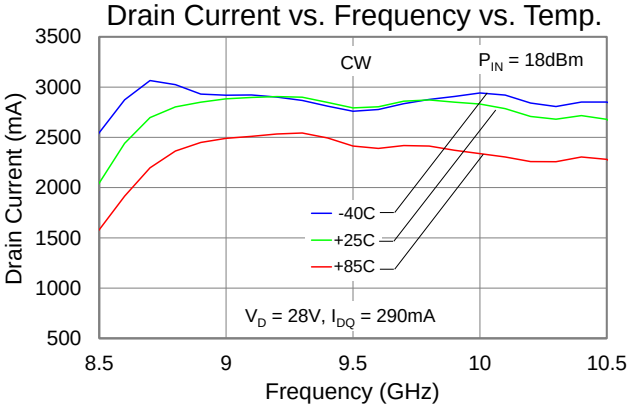
Drain Current vs. Frequency vs. V_D



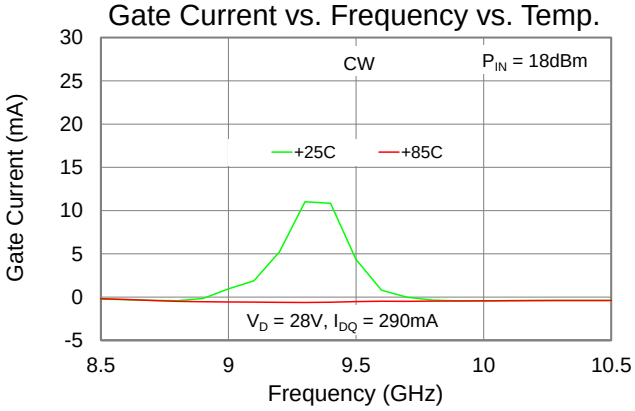
Gate Current vs. Frequency vs. V_D



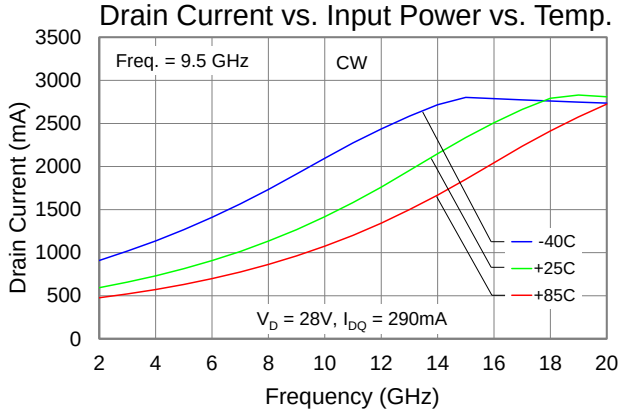
Drain Current vs. Frequency vs. Temp.



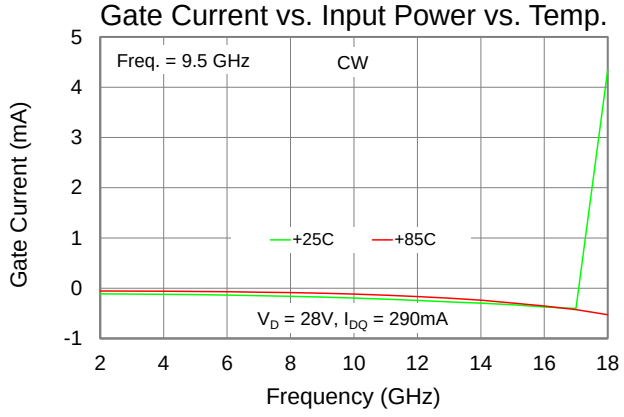
Gate Current vs. Frequency vs. Temp.



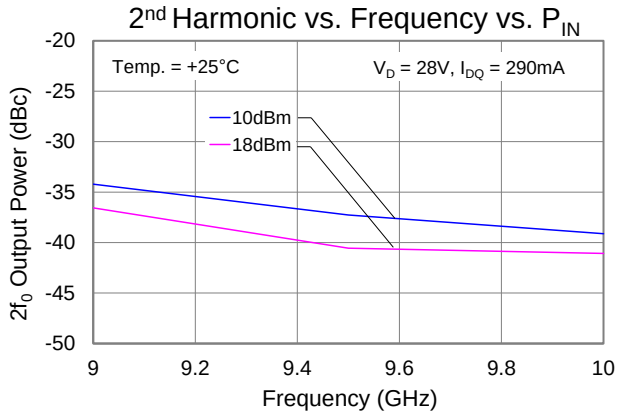
Drain Current vs. Input Power vs. Temp.

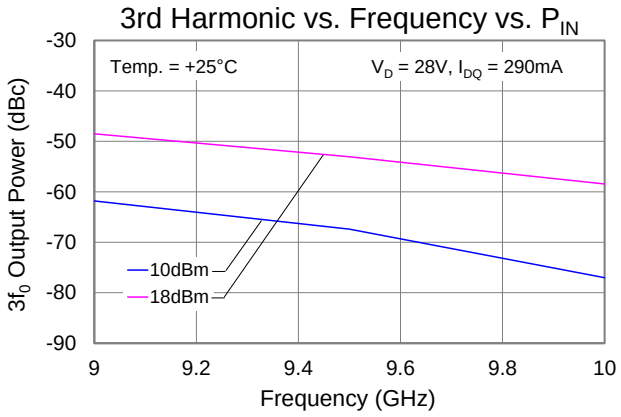


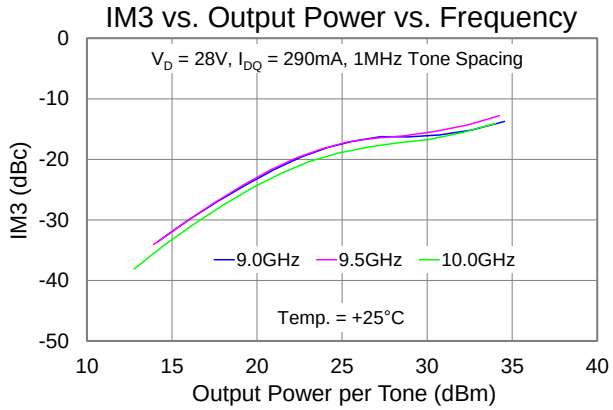
Gate Current vs. Input Power vs. Temp.

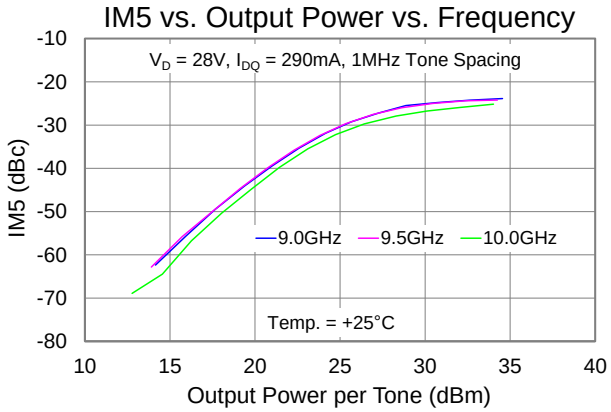


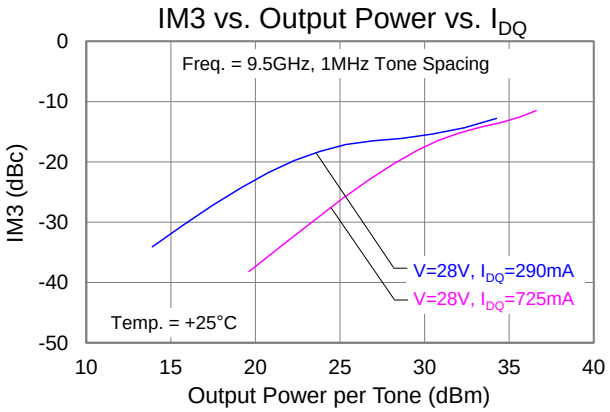
Typical Performance: Linearity

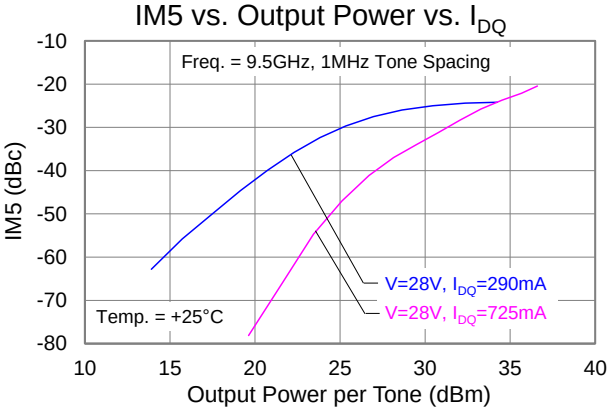






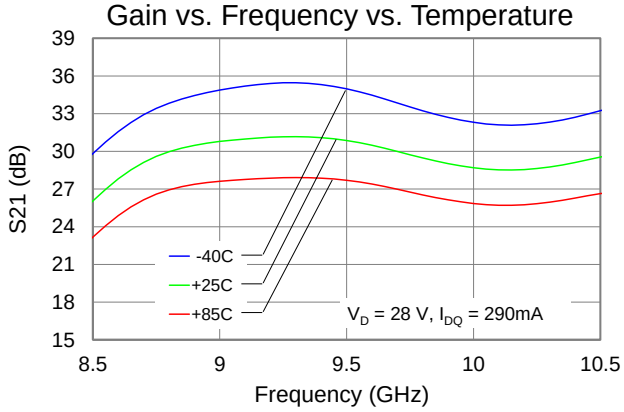




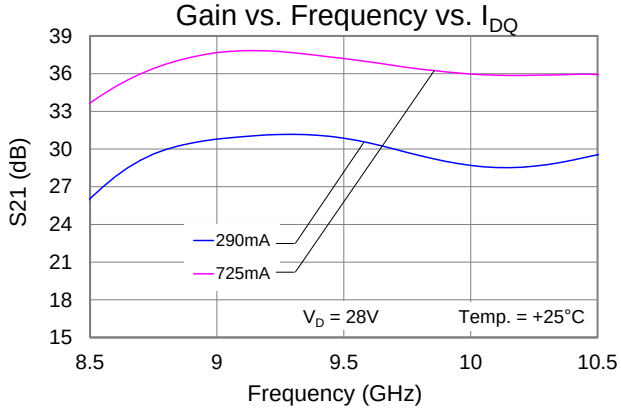


Typical Performance: Small Signal

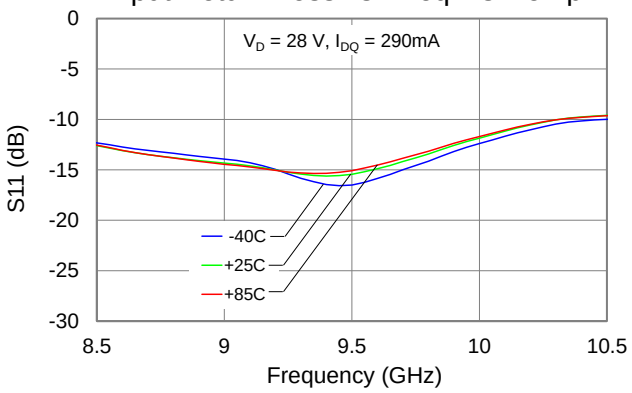
Gain vs. Frequency vs. Temperature



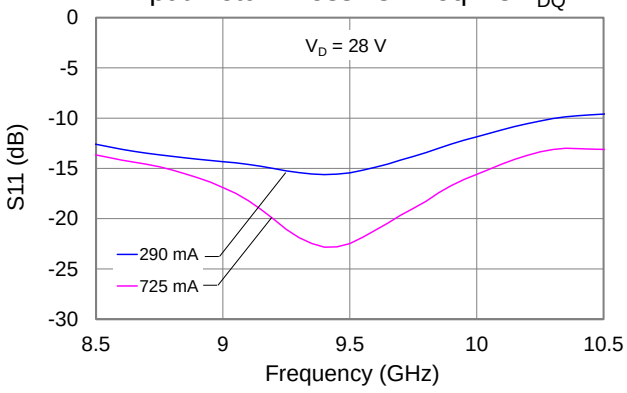
Gain vs. Frequency vs. I_{DQ}



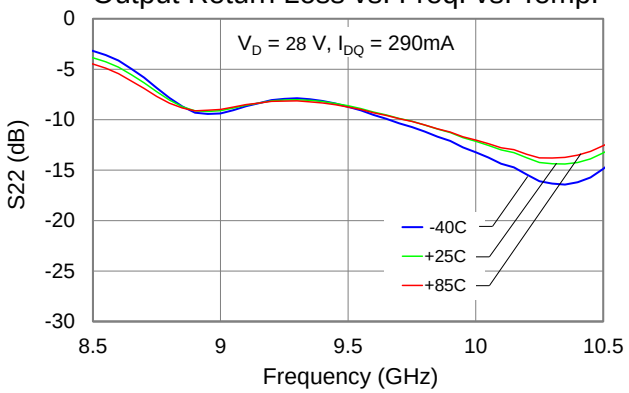
Input Return Loss vs. Freq. vs. Temp.



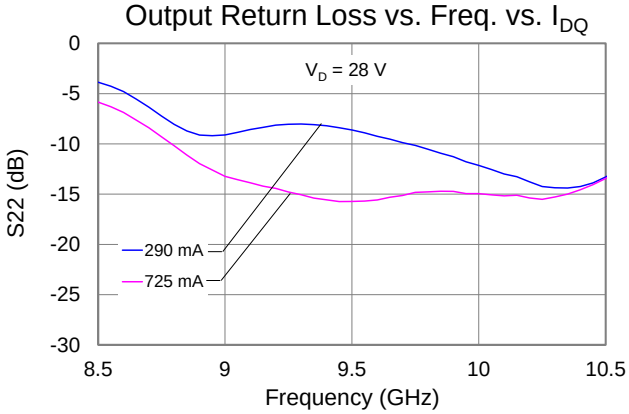
Input Return Loss vs. Freq. vs. I_{DQ}



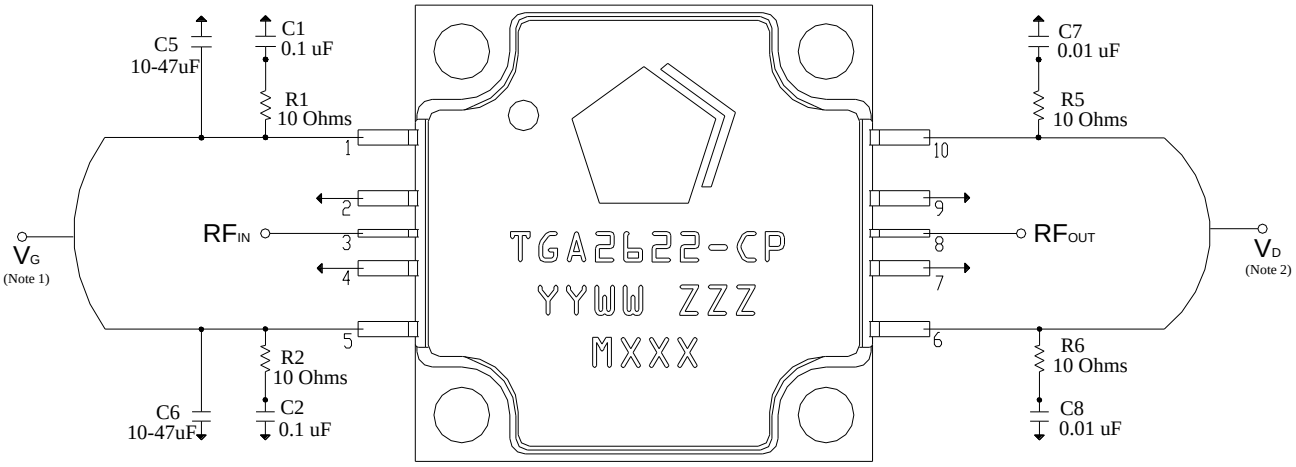
Output Return Loss vs. Freq. vs. Temp.



Output Return Loss vs. Freq. vs. I_{DQ}



Application Circuit



Notes:

1. V_G must be biased from both sides (Pins 1 and 5)
2. V_D must be biased from both sides (Pins 6 and 10)

Bias-up Procedure

1. Set power supply: I_D limit to 3.5 A, I_G limit to 25 mA
2. Apply -5.0 V to V_G (for pinch-off)
3. Increase V_D to +28 V; Ensure I_{DQ} is approx. 0 mA
4. Adjust V_G more positive until I_{DQ} = 290 mA V_G ~ -2.7 V typ
5. Apply RF signal

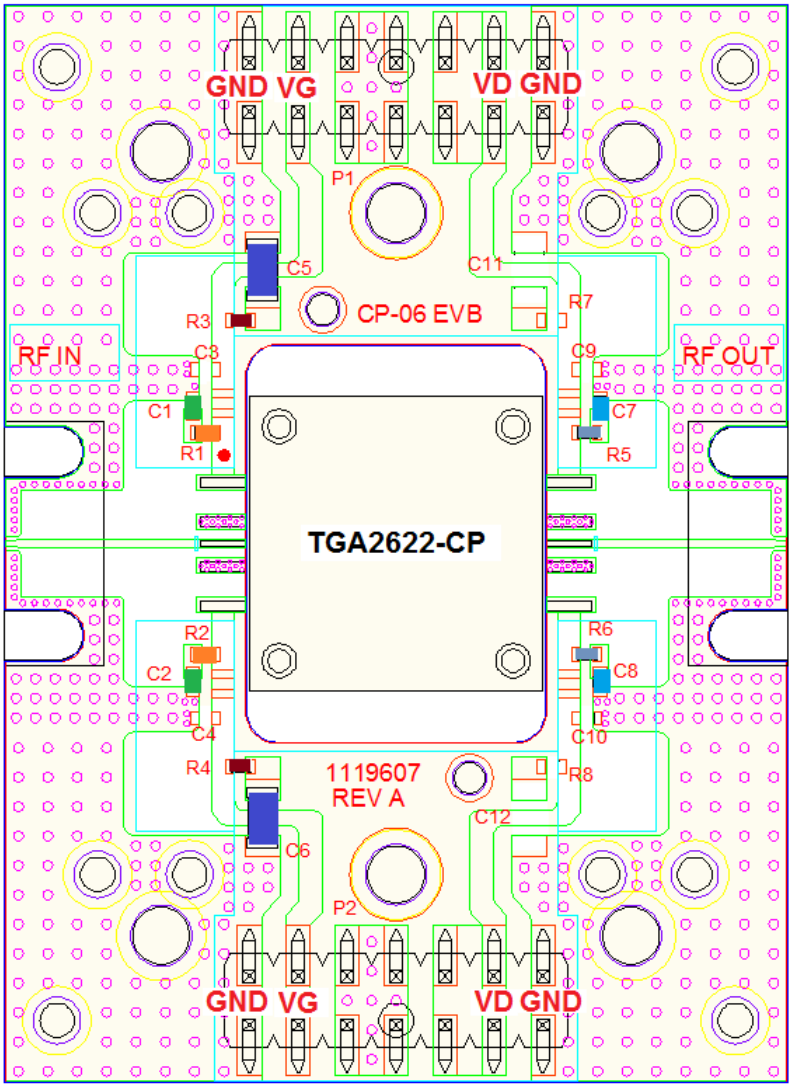
Bias-down Procedure

1. Turn off RF signal
2. Reduce V_G to −5.0 V; Ensure I_{DQ} ~ 0 mA
3. Reduce V_D to 0 V
4. Turn off V_D supply
5. Turn off V_G supply

Pin Description

Pin No.	Symbol	Description
1,5	V_G	Gate Voltage; Bias network is required; must be biased from both sides; see recommended Application Information above.
3	RF_{IN}	Output; matched to 50 Ω; DC blocked
2,4,7,9	GND	Must be grounded on the PCB.
6,10	V_D	Drain voltage; Bias network is required; must be biased from both sides; see recommended Application Information above.
8	RF_{OUT}	Input; matched to 50 Ω; DC blocked

Evaluation Board Layout



Notes: Both Top and Bottom V_D and V_G must be biased.

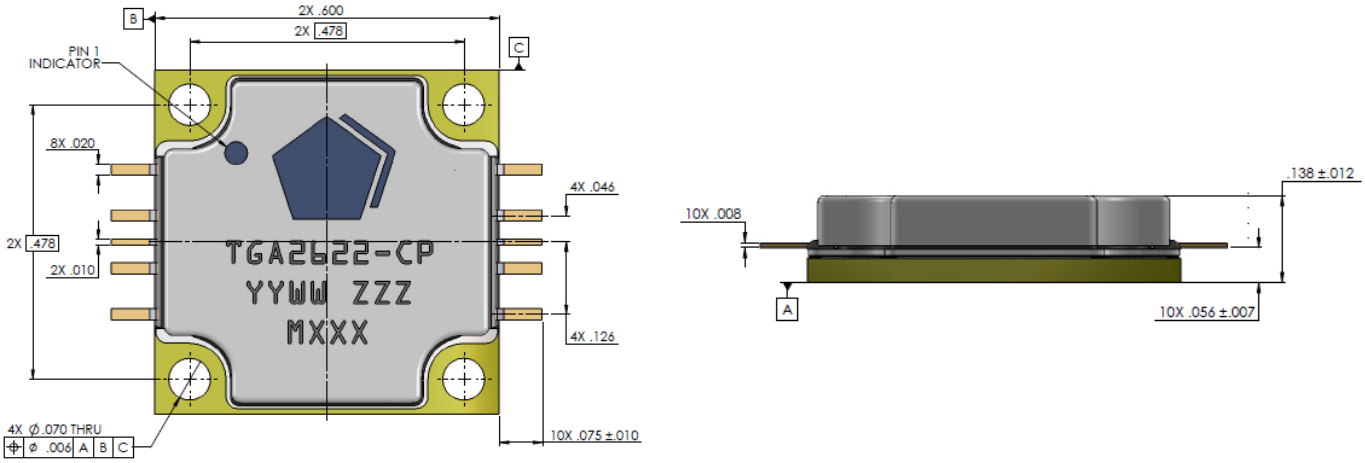
Bill of Material

Reference Des.	Value	Description	Manuf.	Part Number
C1, C2	0.1 µF	Cap, 0402, 50 V, 10%, X7R	Various	
C5, C6	10-47 µF	Cap, 1206, 50 V, 20%, X5R (10 V is OK)	Various	
C7, C8	0.01 µF	Cap, 0402, 50 V, 10%, X7R	Various	
R1, R2, R5, R6	10 ohms	Res, 0402, 50 V, 5%	Various	
R3, R4	0 ohms	Res, 0402, jumpers required for the above EVB	Various	

Assembly Notes

1. Clean the board or module with alcohol. Allow it to dry fully.
2. Nylock screws are recommended for mounting the TGA2622-CP to the board.
3. To improve the thermal and RF performance, we recommend the following:
   a. Apply thermal compound or 4 mils indium shim between the package and the board.
   b. Attach a heat sink to the bottom of the board and apply thermal compound or 4 mils indium shim between the heat sink and the board.
4. Apply solder to each pin of the TGA2622-CP.
5. Clean the assembly with alcohol.

Mechanical Information



Units: inches

Tolerances: unless specified

x.xx = ± 0.01; x.xxx = ± 0.005

Materials:

Base: Copper

Lead: Alloy 194

Lid: LCP (Liquid Crystal Polymer)

All metalized features are gold plated

Part is epoxy sealed

Marking:

2622: Part number

YY: Part Assembly year

WW: Part Assembly week

ZZZ: Serial Number

MXXX: Batch ID

Product Compliance Information

ESD Sensitivity Ratings

Caution! ESD-Sensitive Device

ESD Rating: TBD
Value: TBD
Test: Human Body Model (HBM)
Standard: JEDEC Standard JESD22-A114

MSL Rating

Level 5A at +260 °C convection reflow
The part is rated Moisture Sensitivity Level 5A at 260°C per JEDEC standard IPC/JEDEC J-STD-020

ECCN

US Department of Commerce: 3A001.b.2.b

Solderability

Compatible with the latest version of J-STD-020, Lead-free solder, 260°C

RoHS Compliance

This product also has the following attributes:

- Lead Free
- Halogen Free (Chlorine, Bromine)
- Antimony Free
- TBBP-A ($C_{15}H_{12}Br_4O_2$) Free
- PFOS Free
- SVHC Free

Contact Information

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