

CHERRY SEMICONDUCTOR

I²L DIGITAL and LINEAR ARRAY

GENESIS™ 1100

T-42-31

GENESIS™ 1100 SEMI-CUSTOM IC

The GENESIS 1100 is a powerful semicustom array for the simultaneous integration of standard linear and digital functions without performance compromises. It is a versatile bipolar linear array to which has been added a cluster of 64 Integrated-injection logic gates. The gates are speed-power programmable over a range of 100nA to 200 μ A, with corresponding propagation delays of 7 μ s to 50ns. Special components include Schottky-clamped NPN transistors for high speed logic interfaces, and power transistors with 400mA capability.

- CHIP SIZE: 98 x 124 mils
- BONDING PADS: 26
- MAX OPERATING VOLTAGE: 12V
- I²L GATES: 64
- MAX TOGGLE FREQUENCY: 2MHz
- NPN TRANSISTORS: 102
- PNP TRANSISTORS: 41
- RESISTORS — BASE: 339
— PINCH: 8

