



# 12N60-C

**Power MOSFET**

## 12A, 600V N-CHANNEL POWER MOSFET

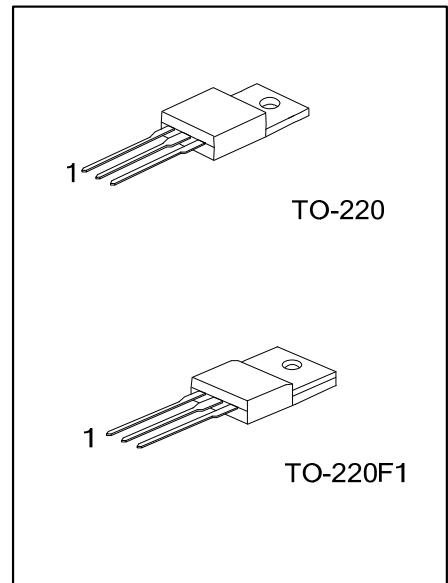
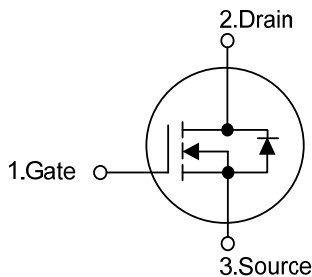
■ DESCRIPTION

The UTC **12N60-C** is a high voltage power MOSFET designed to have better characteristics, such as fast switching time, low gate charge, low on-state resistance and high rugged avalanche characteristics. This power MOSFET is usually used in high speed switching applications of switching power supplies and adaptors.

■ FEATURES

- \*  $R_{DS(ON)} < 0.7 \Omega @ V_{GS} = 10 V, I_D = 6.0 A$
- \* Fast switching capability
- \* Avalanche energy tested
- \* Improved dv/dt capability, high ruggedness

■ SYMBOL



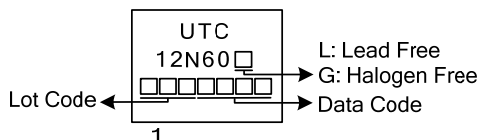
■ ORDERING INFORMATION

Ordering Number		Package	Pin Assignment			Packing
Lead Free	Halogen Free		1	2	3	
12N60L-TF1-T	12N60G-TF1-T	TO-220F1	G	D	S	Tube
12N60L-TF3-T	12N60G-TF3-T	TO-220F	G	D	S	Tube

Note: Pin Assignment: G: Gate D: Drain S: Source

<p>12N60G-TF1-T</p> <p>(1) Packing Type (2) Package Type (3) Green Package</p>	<p>(1) T: Tube (2) TF1: TO-220F1, TF3: TO-220F (3) G: Halogen Free and Lead Free, L: Lead Free</p>
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■ MARKING



■ ABSOLUTE MAXIMUM RATINGS ( $T_c = 25^\circ\text{C}$ , unless otherwise specified)

PARAMETER		SYMBOL	RATINGS	UNIT
Drain-Source Voltage		$V_{DSS}$	600	V
Gate-Source Voltage		$V_{GSS}$	$\pm 30$	V
Continuous Drain Current		$I_D$	12	A
Pulsed Drain Current (Note 2)		$I_{DM}$	48	A
Avalanche Energy	Single Pulsed (Note 3)	$E_{AS}$	109	mJ
Peak Diode Recovery dv/dt (Note 4)		dv/dt	1.9	ns
Power Dissipation	TO-220	$P_D$	156	W
	TO-220F1		51	W
Junction Temperature		$T_J$	+150	$^\circ\text{C}$
Storage Temperature		$T_{STG}$	-55 ~ +150	$^\circ\text{C}$

Notes: 1. Absolute maximum ratings are those values beyond which the device could be permanently damaged.

Absolute maximum ratings are stress ratings only and functional device operation is not implied.

2. Repetitive Rating: Pulse width limited by maximum junction temperature.

3.  $L = 10\text{mH}$ ,  $I_{AS} = 4.69\text{A}$ ,  $V_{DD} = 50\text{V}$ ,  $R_G = 25\Omega$ , Starting  $T_J = 25^\circ\text{C}$

4.  $I_{SD} \leq 12\text{A}$ ,  $di/dt \leq 200\text{A}/\mu\text{s}$ ,  $V_{DD} \leq BV_{DSS}$ , Starting  $T_J = 25^\circ\text{C}$

■ THERMAL DATA

PARAMETER		SYMBOL	RATING	UNIT
Junction to Ambient		$\theta_{JA}$	62.5	$^\circ\text{C}/\text{W}$
Junction to Case	TO-220	$\theta_{JC}$	0.56	$^\circ\text{C}/\text{W}$
	TO-220F1		2.43	$^\circ\text{C}/\text{W}$

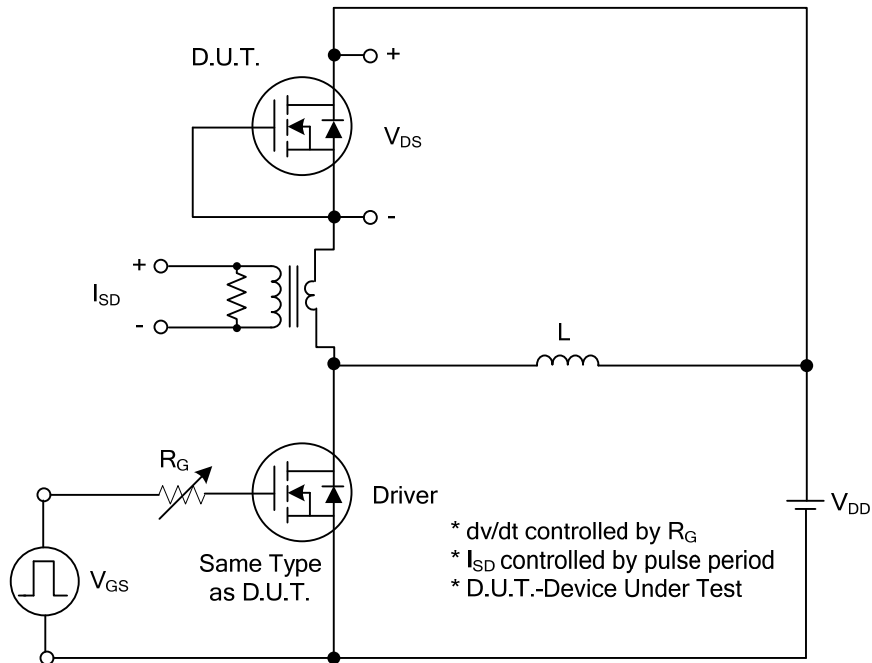
■ ELECTRICAL CHARACTERISTICS ( $T_J=25^\circ\text{C}$ , unless otherwise specified)

PARAMETER	SYMBOL	TEST CONDITIONS	MIN	TYP	MAX	UNIT
<b>OFF CHARACTERISTICS</b>						
Drain-Source Breakdown Voltage	$BV_{DSS}$	$V_{GS}=0V, I_D=250\mu A$	600			V
Drain-Source Leakage Current	$I_{DSS}$	$V_{DS}=600V, V_{GS}=0V$			1	$\mu A$
Gate- Source Leakage Current	Forward	$I_{GSS}$			100	nA
	Reverse				-100	nA
<b>ON CHARACTERISTICS</b>						
Gate Threshold Voltage	$V_{GS(TH)}$	$V_{DS}=V_{GS}, I_D=250\mu A$	2.0		4.0	V
Static Drain-Source On-State Resistance	$R_{DS(ON)}$	$V_{GS}=10V, I_D=6.0A$			0.7	$\Omega$
<b>DYNAMIC CHARACTERISTICS</b>						
Input Capacitance	$C_{ISS}$	$V_{GS}=0V, V_{DS}=25V, f=1.0\text{ MHz}$		1465		pF
Output Capacitance	$C_{OSS}$			245		pF
Reverse Transfer Capacitance	$C_{RSS}$			57		pF
<b>SWITCHING CHARACTERISTICS</b>						
Total Gate Charge (Note 1)	$Q_G$	$V_{DS}=50V, I_D=1.3A, I_G=100\mu A$ $V_{GS}=10V$ (Note 1,2)		144		nC
Gate-Source Charge	$Q_{GS}$			10		nC
Gate-Drain Charge	$Q_{GD}$			27		nC
Turn-On Delay Time (Note 1)	$t_{D(ON)}$	$V_{DD}=30V, I_D=0.5A,$ $R_G=25\Omega, V_{GS}=10V$ (Note 1,2)		81		ns
Turn-On Rise Time	$t_R$			152		ns
Turn-Off Delay Time	$t_{D(OFF)}$			430		ns
Turn-Off Fall Time	$t_F$			215		ns
<b>DRAIN-SOURCE DIODE CHARACTERISTICS AND MAXIMUM RATINGS</b>						
Maximum Continuous Drain-Source Diode Forward Current	$I_S$				12	A
Maximum Pulsed Drain-Source Diode Forward Current	$I_{SM}$				48	A
Drain-Source Diode Forward Voltage	$V_{SD}$	$V_{GS}=0V, I_S=6.0A$			1.4	V
Reverse Recovery Time	$t_{rr}$	$V_{GS}=0V, I_S=6.0A,$ $dI_F/dt=100\text{ A}/\mu\text{s}$ (Note 1)		336		ns
Reverse Recovery Charge	$Q_{rr}$			2.21		$\mu\text{C}$

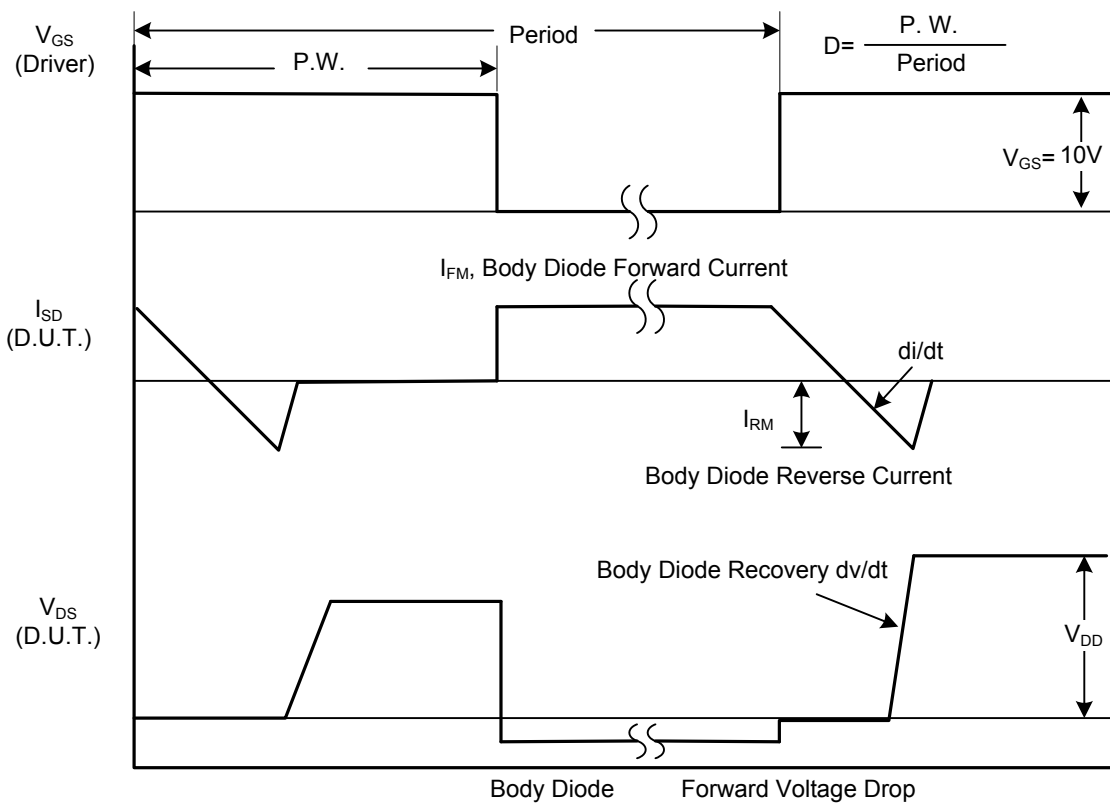
Notes: 1. Pulse Test: Pulse width  $\leq 300\mu\text{s}$ , Duty cycle  $\leq 2\%$ .

2. Essentially independent of operating temperature.

■ TEST CIRCUITS AND WAVEFORMS

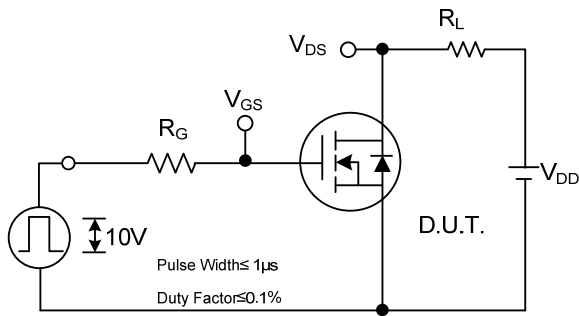


Peak Diode Recovery  $dv/dt$  Test Circuit

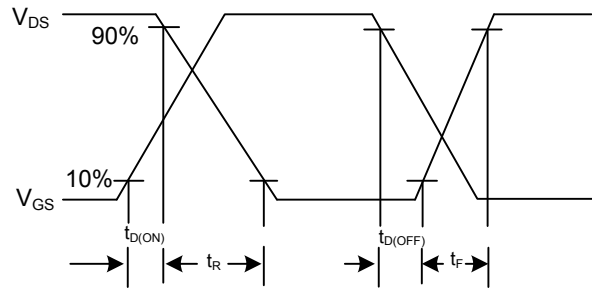


Peak Diode Recovery  $dv/dt$  Waveforms

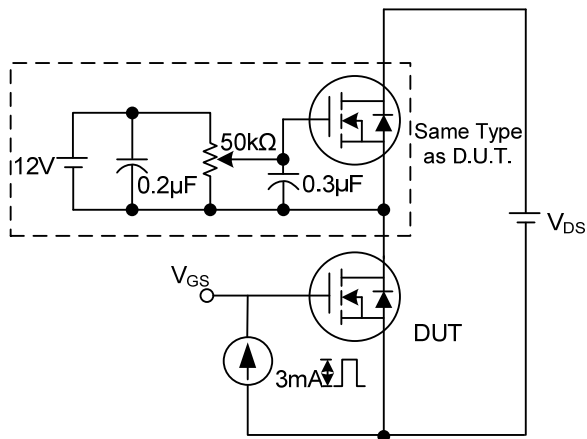
## TEST CIRCUITS AND WAVEFORMS (Cont.)



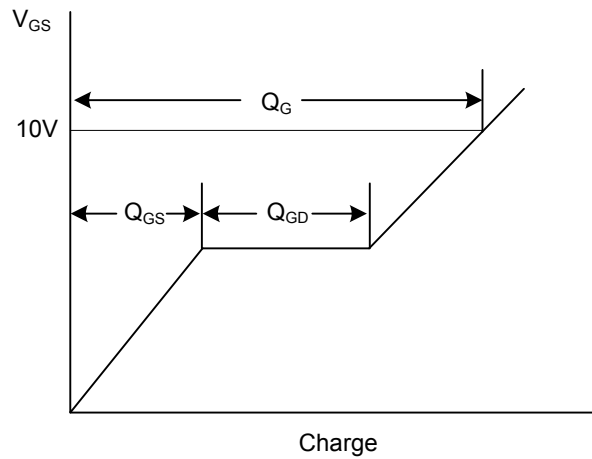
**Switching Test Circuit**



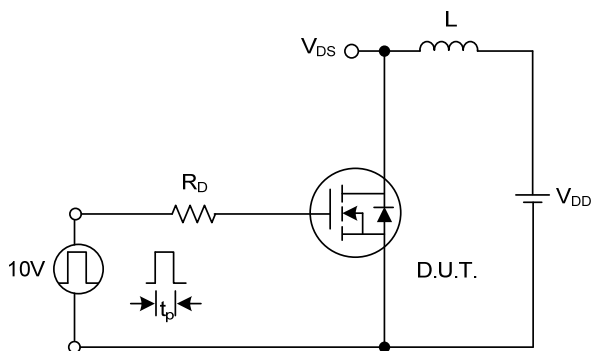
**Switching Waveforms**



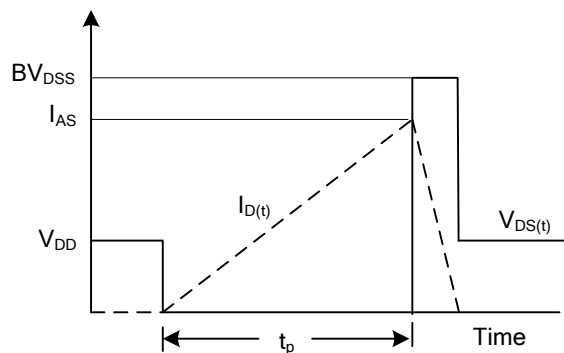
**Gate Charge Test Circuit**



**Gate Charge Waveform**



**Unclamped Inductive Switching Test Circuit**



**Unclamped Inductive Switching Waveforms**

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