



## 12N90-C

Power MOSFET

### 12A, 900V N-CHANNEL POWER MOSFET

#### DESCRIPTION

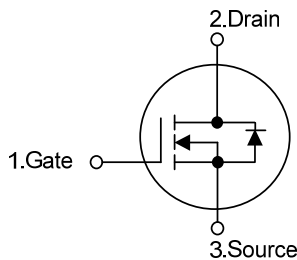
The UTC **12N90-C** is a N-channel mode power MOSFET using UTC's advanced technology to provide customers with planar stripe and DMOS technology. This technology allows a minimum on-state resistance and superior switching performance. It also can withstand high energy pulse in the avalanche and commutation mode.

This power MOSFET is usually used at high speed switching applications in power supplies, PWM motor controls, high efficient AC to DC converters and bridge circuits.

#### FEATURES

- \*  $R_{DS(ON)} \leq 1.2 \Omega @ V_{GS}=10V, I_D=6.0A$
- \* Fast switching
- \* 100% avalanche tested
- \* Improved dv/dt capability

#### SYMBOL

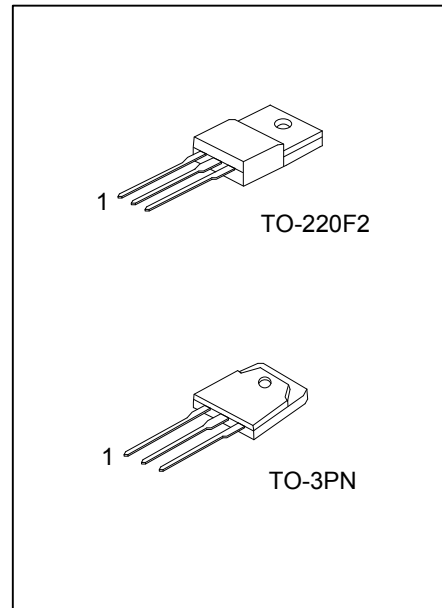


#### ORDERING INFORMATION

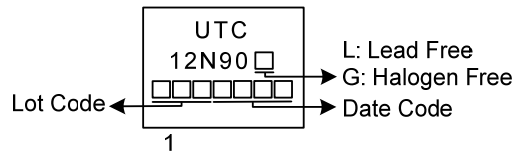
Ordering Number		Package	Pin Assignment			Packing
Lead Free	Halogen Free		1	2	3	
12N90L-TF2-T	12N90G-TF2-T	TO-220F2	G	D	S	Tube
12N90L-T3N-T	12N90G-T3N-T	TO-3PN	G	D	S	Tube

Note: Pin Assignment: G: Gate D: Drain S: Source

<p>12N90G-TF2-T</p>	<p>(1) T: Tube (2) TF2: TO-220F2, T3N: TO-3PN (3) G: Halogen Free and Lead Free, L: Lead Free</p>
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■ MARKING



■ ABSOLUTE MAXIMUM RATINGS ( $T_C = 25^\circ\text{C}$ , unless otherwise specified)

PARAMETER		SYMBOL	RATINGS	UNIT
Drain-Source Voltage		$V_{DSS}$	900	V
Gate-Source Voltage		$V_{GSS}$	$\pm 30$	V
Drain Current	Continuous	$I_D$	12	A
	Pulsed (Note 2)	$I_{DM}$	36	A
Avalanche Energy	Single Pulsed (Note 3)	$E_{AS}$	344	mJ
Peak Diode Recovery dv/dt (Note 4)		dv/dt	8.5	V/ns
Power Dissipation	TO-220F2	$P_D$	51	W
	TO-3PN		290	W
Junction Temperature		$T_J$	+150	$^\circ\text{C}$
Storage Temperature		$T_{STG}$	-55 ~ +150	$^\circ\text{C}$

Notes: 1. Absolute maximum ratings are those values beyond which the device could be permanently damaged.

Absolute maximum ratings are stress ratings only and functional device operation is not implied.

2. Repetitive Rating: Pulse width limited by maximum junction temperature.

3.  $L = 10\text{mH}$ ,  $I_{AS} = 8.3\text{A}$ ,  $V_{DD} = 50\text{V}$ ,  $R_G = 25\ \Omega$  Starting  $T_J = 25^\circ\text{C}$

4.  $I_{SD} \leq 12\text{A}$ ,  $di/dt \leq 200\text{A}/\mu\text{s}$ ,  $V_{DD} \leq BV_{DSS}$ , Starting  $T_J = 25^\circ\text{C}$

■ THERMAL DATA

PARAMETER		SYMBOL	RATING	UNIT
Junction to Ambient	TO-220F2	$\theta_{JA}$	62.5	$^\circ\text{C}/\text{W}$
	TO-3PN		40	$^\circ\text{C}/\text{W}$
Junction to Case	TO-220F2	$\theta_{JC}$	2.43	$^\circ\text{C}/\text{W}$
	TO-3PN		0.43	$^\circ\text{C}/\text{W}$

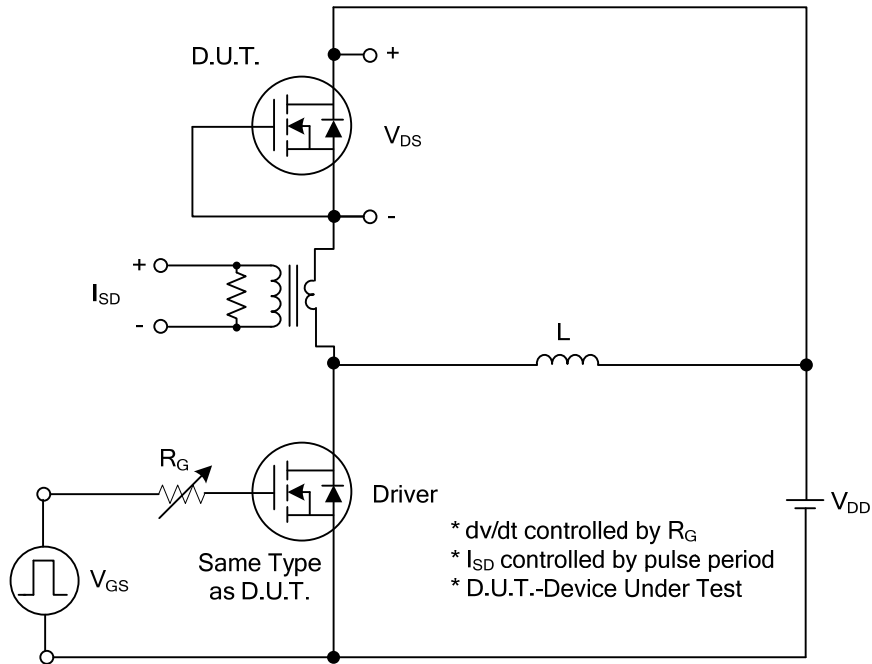
■ ELECTRICAL CHARACTERISTICS(  $T_J = 25^\circ\text{C}$ , unless otherwise specified)

PARAMETER	SYMBOL	TEST CONDITIONS	MIN	TYP	MAX	UNIT
<b>OFF CHARACTERISTICS</b>						
Drain-Source Breakdown Voltage	$BV_{DSS}$	$V_{GS}=0V, I_D=250\mu A$	900			V
Drain-Source Leakage Current	$I_{DSS}$	$V_{DS}=900V, V_{GS}=0V$			10	$\mu A$
Gate-Source Leakage Current	Forward	$I_{GSS}$			100	nA
	Reverse				$V_{GS}=-30V, V_{DS}=0V$	-100
<b>ON CHARACTERISTICS</b>						
Gate Threshold Voltage	$V_{GS(TH)}$	$V_{DS}=V_{GS}, I_D=250\mu A$	2.0		4.0	V
Static Drain-Source On-State Resistance	$R_{DS(ON)}$	$V_{GS}=10V, I_D=6.0A$			1.2	$\Omega$
<b>DYNAMIC CHARACTERISTICS</b>						
Input Capacitance	$C_{ISS}$	$V_{GS}=0V, V_{DS}=25V, f=1.0\text{ MHz}$		1912		pF
Output Capacitance	$C_{OSS}$			257		pF
Reverse Transfer Capacitance	$C_{RSS}$			50		pF
<b>SWITCHING CHARACTERISTICS</b>						
Total Gate Charge (Note 1)	$Q_G$	$V_{DS}=180V, V_{GS}=10V, I_D=12A,$ $I_G=10mA$ (Note 1, 2)		67.5		nC
Gate to Source Charge	$Q_{GS}$			19		nC
Gate to Drain Charge	$Q_{GD}$			29		nC
Turn-ON Delay Time (Note 1)	$t_{D(ON)}$	$V_{DD}=30V, V_{GS}=10V, I_D=12A,$ $R_G=25\Omega$ (Note 1, 2)		37		ns
Rise Time	$t_R$			97		ns
Turn-OFF Delay Time	$t_{D(OFF)}$			203		ns
Fall-Time	$t_F$			62		ns
<b>SOURCE- DRAIN DIODE RATINGS AND CHARACTERISTICS</b>						
Maximum Body-Diode Continuous Current	$I_S$				12	A
Maximum Body-Diode Pulsed Current	$I_{SM}$				36	A
Drain-Source Diode Forward Voltage (Note 1)	$V_{SD}$	$I_S=12A, V_{GS}=0V$			1.4	V
Body Diode Reverse Recovery Time (Note 1)	$t_{rr}$	$I_S=12A, V_{GS}=0V,$ $dI_F/dt = 100A/\mu s$		740		ns
Body Diode Reverse Recovery Charge	$Q_{rr}$			11		$\mu C$

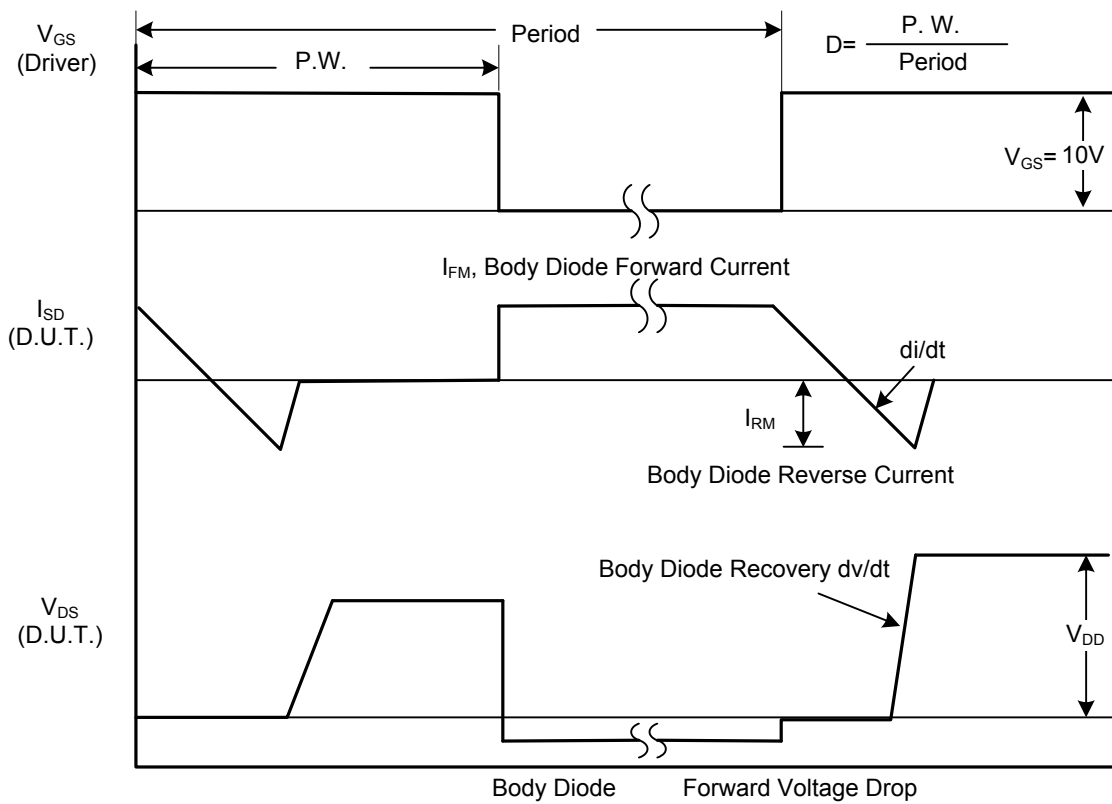
Notes: 1. Pulse Test : Pulse width  $\leq 300\mu s$ , Duty cycle  $\leq 2\%$ .

2. Essentially independent of operating temperature.

■ TEST CIRCUITS AND WAVEFORMS

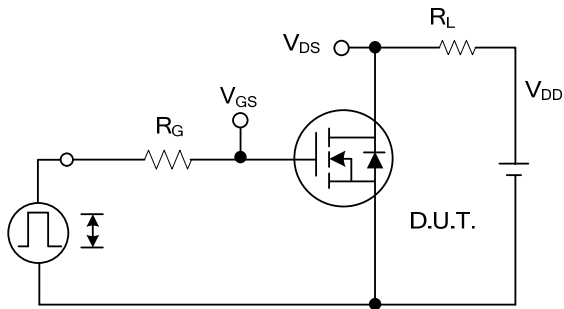


Peak Diode Recovery dv/dt Test Circuit

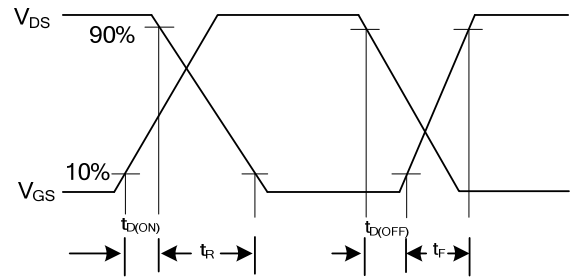


Peak Diode Recovery dv/dt Waveforms

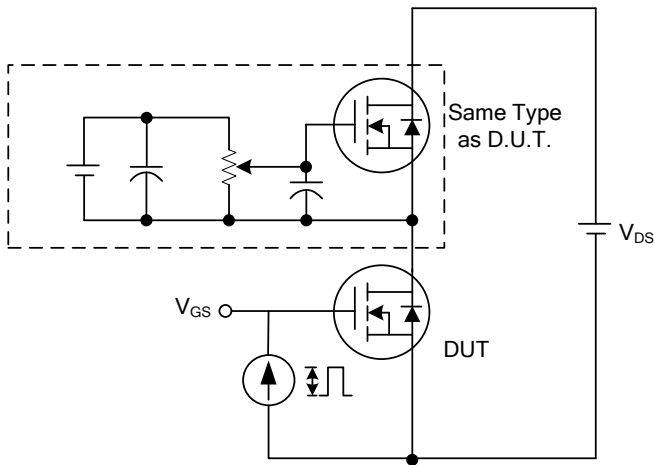
## TEST CIRCUITS AND WAVEFORMS



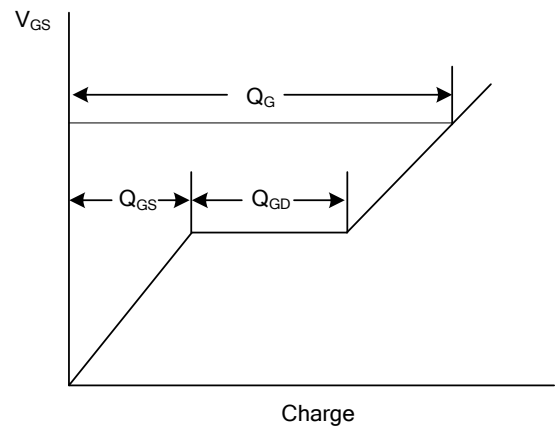
**Switching Test Circuit**



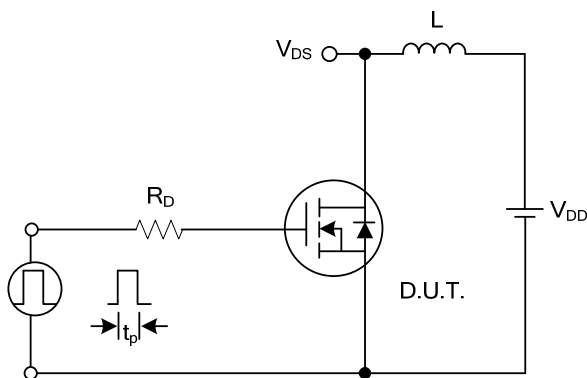
**Switching Waveforms**



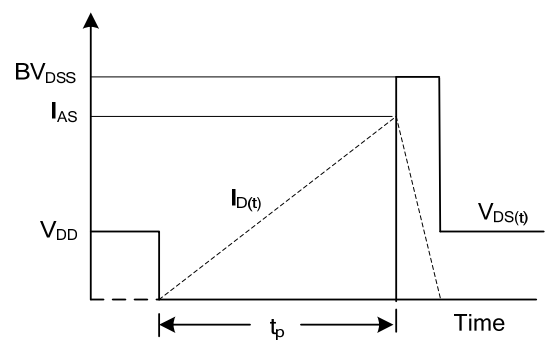
**Gate Charge Test Circuit**



**Gate Charge Waveform**

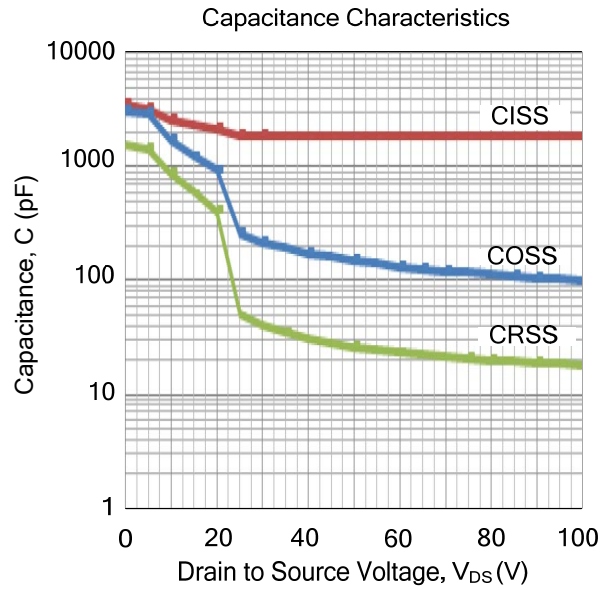
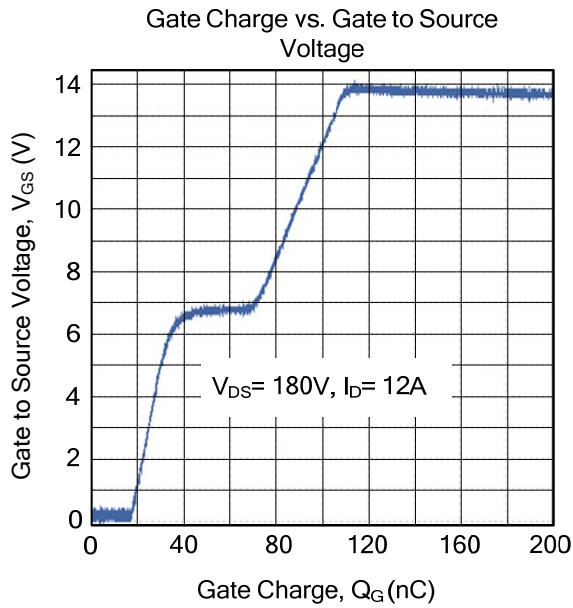


**Unclamped Inductive Switching Test Circuit**



**Unclamped Inductive Switching Waveforms**

■ TYPICAL CHARACTERISTICS



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