

## 13NM65

Power MOSFET

13A, 650V N-CHANNEL  
SUPER-JUNCTION MOSFET

## ■ DESCRIPTION

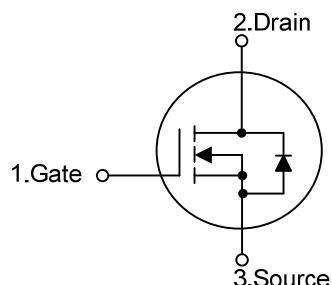
The UTC 13NM65 is a high voltage super junction MOSFET and is designed to have better characteristics.

The UTC 13NM65 Utilizing a advanced charge-balance technology, enhance system efficiency, improve EMI and reliability. such as low gate charge, low on-state resistance and have a high power density and high rugged avalanche characteristics. This super junction MOSFET usually used at AC/DC power conversion, and industrial power applications.

## ■ FEATURES

- \*  $R_{DS(ON)} \leq 0.5\Omega$  @  $V_{GS}=10V$ ,  $I_D=6.5A$
- \* Fast switching capability
- \* Avalanche energy tested
- \* Improved dv/dt capability, high ruggedness

## ■ SYMBOL



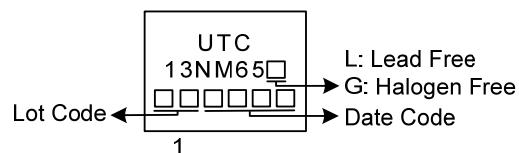
## ■ ORDERING INFORMATION

Ordering Number		Package	Pin Assignment			Packing
Lead Free	Halogen Free		1	2	3	
13NM65L-TA3-T	13NM65G-TA3-T	TO-220	G	D	S	Tube
13NM65L-TF3-T	13NM65G-TF3-T	TO-220F	G	D	S	Tube
13NM65L-TF1-T	13NM65G-TF1-T	TO-220F1	G	D	S	Tube
13NM65L-TM3-T	13NM65G-TM3-T	TO-251	G	D	S	Tube
13NM65L-TN3-R	13NM65G-TN3-R	TO-252	G	D	S	Tape Reel
13NM65L-T2Q-T	13NM65G-T2Q-T	TO-262	G	D	S	Tube
13NM65L-TQ2-T	13NM65G-TQ2-T	TO-263	G	D	S	Tube
13NM65L-TQ2-R	13NM65G-TQ2-R	TO-263	G	D	S	Tape Reel

Note: Pin Assignment: G: Gate D: Drain S: Source

13NM65G-TA3-T 	(1)Packing Type	(1) T: Tube, R: Tape Reel (2) TA3: TO-220, TF1: TO-220F1, TF3: TO-220F TM3: TO-251, TN3: TO-252, T2Q: TO-262 TQ2: TO-263 (3) G: Halogen Free and Lead Free, L: Lead Free
	(2)Package Type	
	(3)Green Package	

### ■ MARKING



■ ABSOLUTE MAXIMUM RATINGS ( $T_c = 25^\circ\text{C}$ , unless otherwise specified)

PARAMETER		SYMBOL	RATINGS	UNIT
Drain-Source Voltage		$V_{DSS}$	650	V
Gate-Source Voltage		$V_{GSS}$	$\pm 30$	V
Drain Current	Continuous	$I_D$	13	A
	Pulsed (Note 2)	$I_{DM}$	26	A
Avalanche Current (Note 2)		$I_{AR}$	2.4	A
Avalanche Energy	Single Pulsed (Note 3)	$E_{AS}$	452	mJ
Peak Diode Recovery $dv/dt$ (Note 4)		$dv/dt$	4.5	V/ns
Power Dissipation	TO-220/TO-262	$P_D$	118	W
	TO-263		30	W
	TO-220F/TO-220F1		60	W
	TO-251/TO-252		+150	°C
Junction Temperature		$T_J$	-55 ~ +150	°C
Storage Temperature		$T_{STG}$		

Notes: 1. Absolute maximum ratings are those values beyond which the device could be permanently damaged.

Absolute maximum ratings are stress ratings only and functional device operation is not implied.

2. Repetitive Rating : Pulse width limited by maximum junction temperature.

3.  $L = 157 \text{ mH}$ ,  $I_{AS} = 2.4\text{A}$ ,  $V_{DD} = 50\text{V}$ ,  $R_G = 25 \Omega$ , Starting  $T_J = 25^\circ\text{C}$

4.  $I_{SD} \leq 13\text{A}$ ,  $di/dt \leq 200\text{A}/\mu\text{s}$ ,  $V_{DD} \leq BV_{DSS}$ , Starting  $T_J = 25^\circ\text{C}$

■ THERMAL RESISTANCES CHARACTERISTICS

PARAMETER		SYMBOL	RATING	UNIT
Junction to Ambient	TO-220/TO-220F	$\theta_{JA}$	62.5	°C/W
	TO-220F1/TO-262		110	°C/W
Junction to Case	TO-263	$\theta_{JC}$	1.06	°C/W
	TO-251/TO-252		4.17	°C/W
	TO-220/TO-262		2.08	°C/W
	TO-263			
Junction to Case	TO-220F/TO-220F1			
	TO-251/TO-252			

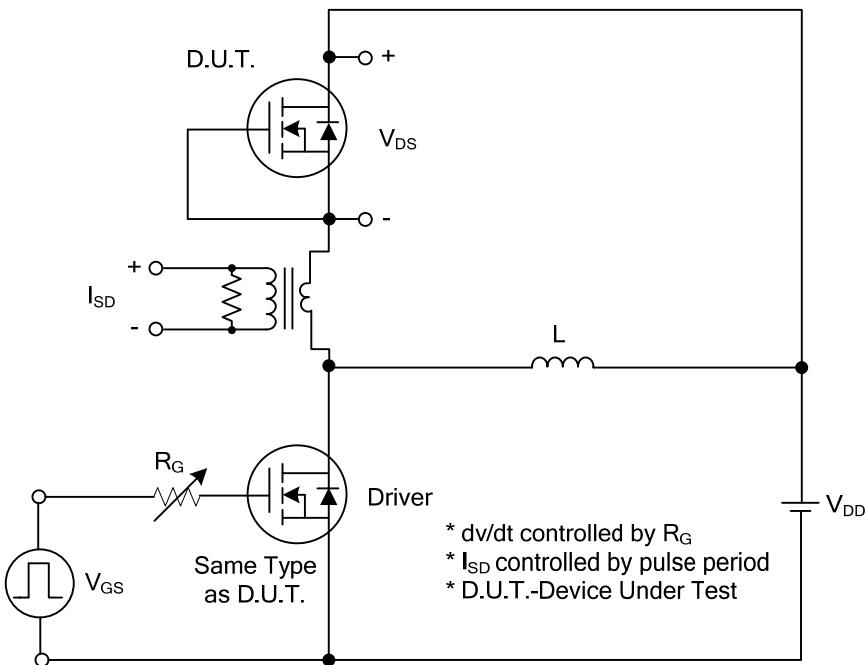
■ ELECTRICAL CHARACTERISTICS ( $T_J=25^\circ\text{C}$ , unless otherwise specified)

PARAMETER	SYMBOL	TEST CONDITIONS	MIN	TYP	MAX	UNIT
<b>OFF CHARACTERISTICS</b>						
Drain-Source Breakdown Voltage	$\text{BV}_{\text{DSS}}$	$\text{V}_{\text{GS}}=0\text{V}, \text{I}_D=250\mu\text{A}$	650			V
Drain-Source Leakage Current	$\text{I}_{\text{DSS}}$	$\text{V}_{\text{DS}}=650\text{V}, \text{V}_{\text{GS}}=0\text{V}$		10		$\mu\text{A}$
Gate-Source Leakage Current	Forward	$\text{V}_{\text{GS}}=30\text{V}, \text{V}_{\text{DS}}=0\text{V}$		100	nA	
	Reverse	$\text{V}_{\text{GS}}=-30\text{V}, \text{V}_{\text{DS}}=0\text{V}$		-100	nA	
<b>ON CHARACTERISTICS</b>						
Gate Threshold Voltage	$\text{V}_{\text{GS}(\text{TH})}$	$\text{V}_{\text{DS}}=\text{V}_{\text{GS}}, \text{I}_D=250\mu\text{A}$	2.5		4.5	V
Static Drain-Source On-State Resistance	$\text{R}_{\text{DS}(\text{ON})}$	$\text{V}_{\text{GS}}=10\text{V}, \text{I}_D=6.5\text{A}$			0.5	$\Omega$
<b>DYNAMIC CHARACTERISTICS</b>						
Input Capacitance	$\text{C}_{\text{ISS}}$	$\text{V}_{\text{GS}}=0\text{V}, \text{V}_{\text{DS}}=25\text{V}, f=1.0\text{MHz}$		780		pF
Output Capacitance	$\text{C}_{\text{OSS}}$			590		pF
Reverse Transfer Capacitance	$\text{C}_{\text{RSS}}$			64		pF
<b>SWITCHING CHARACTERISTICS</b>						
Total Gate Charge (Note 1)	$\text{Q}_G$	$\text{V}_{\text{DS}}=520\text{V}, \text{V}_{\text{GS}}=10\text{V}$ $\text{I}_D=13\text{A}, \text{I}_G=1\text{mA}$ (Note 1,2)		28		nC
Gate-Source Charge	$\text{Q}_{\text{GS}}$			5		nC
Gate-Drain Charge	$\text{Q}_{\text{GD}}$			11		nC
Turn-On Delay Time (Note 1)	$t_{\text{D}(\text{ON})}$	$\text{V}_{\text{DD}}=100\text{V}, \text{V}_{\text{GS}}=10\text{V}, \text{I}_D=13\text{A},$ $\text{R}_G=25\Omega$ (Note 1,2)		12		nS
Turn-On Rise Time	$t_R$			26		nS
Turn-Off Delay Time	$t_{\text{D}(\text{OFF})}$			82		nS
Turn-Off Fall Time	$t_F$			51		nS
<b>DRAIN-SOURCE DIODE CHARACTERISTICS AND MAXIMUM RATINGS</b>						
Maximum Continuous Drain-Source Diode Forward Current	$\text{I}_S$				13	A
Maximum Pulsed Drain-Source Diode Forward Current	$\text{I}_{\text{SM}}$				26	A
Drain-Source Diode Forward Voltage (Note 1)	$\text{V}_{\text{SD}}$	$\text{V}_{\text{GS}}=0\text{V}, \text{I}_S=13\text{A}$			1.4	V
Reverse Recovery Time (Note 1)	$t_{\text{rr}}$	$\text{V}_{\text{GS}}=0\text{V}, \text{I}_S=13\text{A},$ $d\text{I}_F/dt=100\text{A}/\mu\text{s}$		376		nS
Reverse Recovery Charge	$\text{Q}_{\text{rr}}$			5.3		$\mu\text{C}$

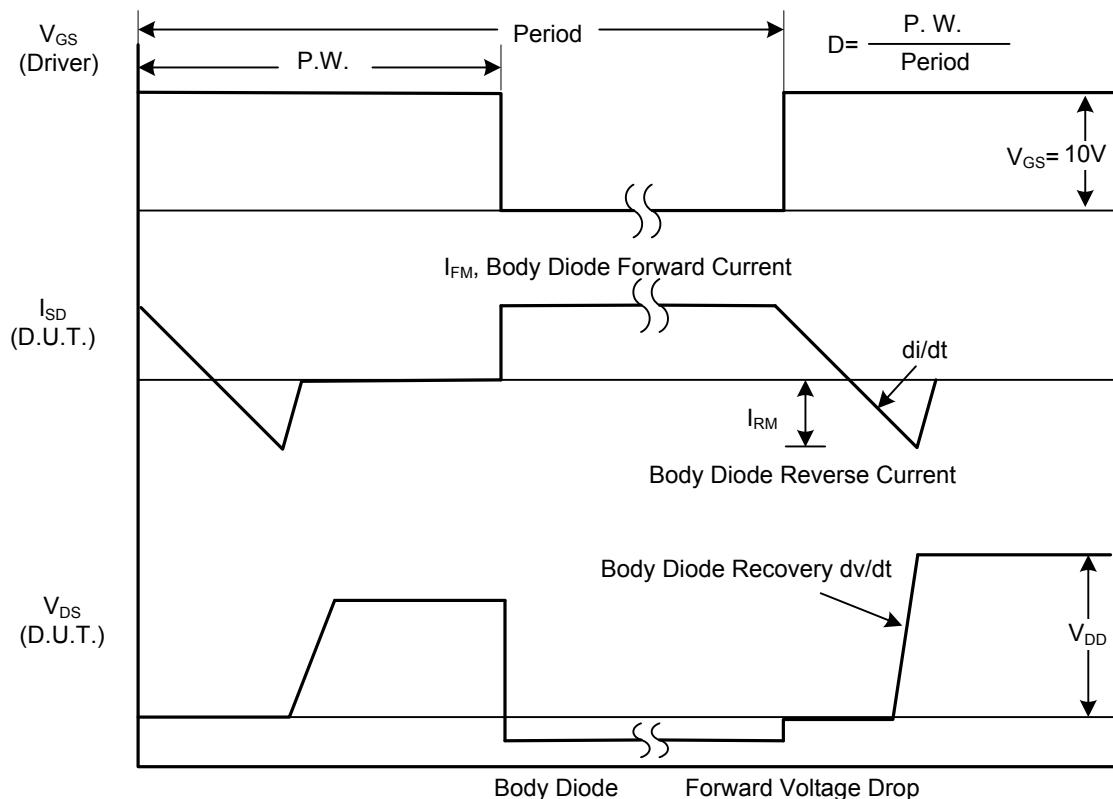
Notes: 1. Pulse Test : Pulse width  $\leq 300\mu\text{s}$ , Duty cycle  $\leq 2\%$ .

2. Essentially independent of operating ambient temperature.

■ TEST CIRCUITS AND WAVEFORMS

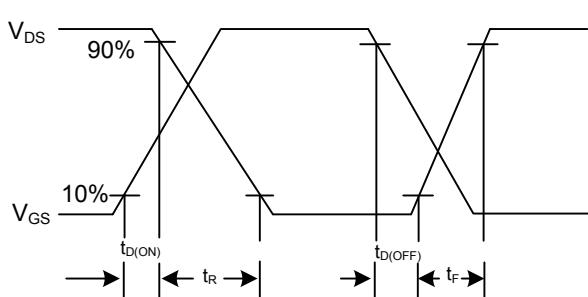
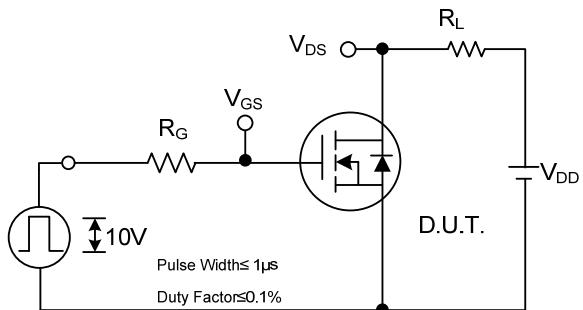


**Peak Diode Recovery dv/dt Test Circuit**



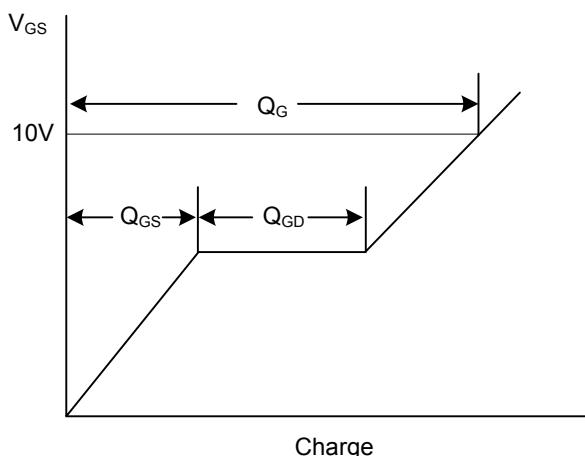
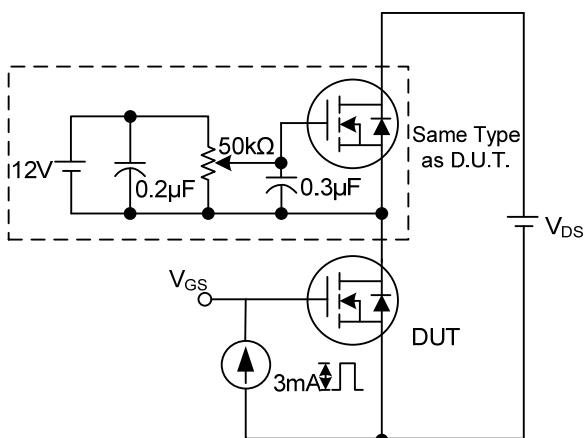
**Peak Diode Recovery dv/dt Waveforms**

### ■ TEST CIRCUITS AND WAVEFORMS



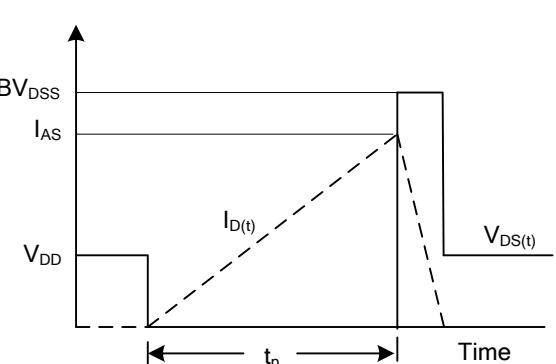
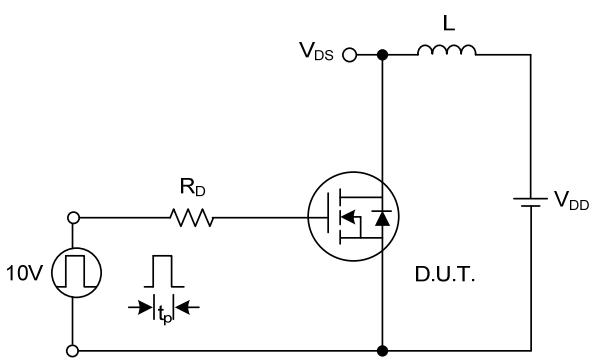
**Switching Test Circuit**

**Switching Waveforms**



**Gate Charge Test Circuit**

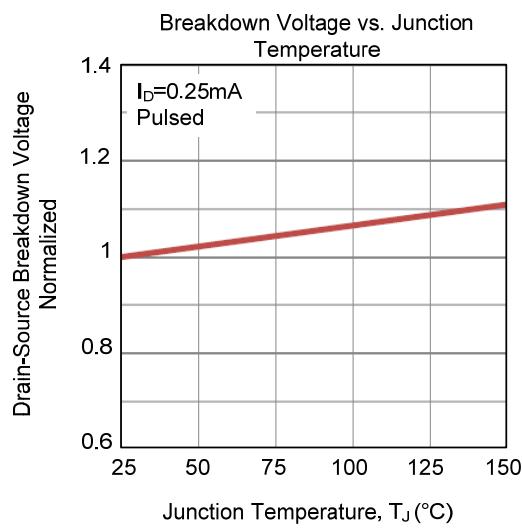
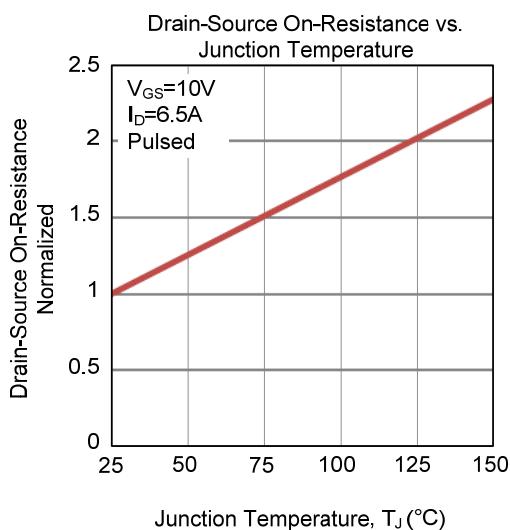
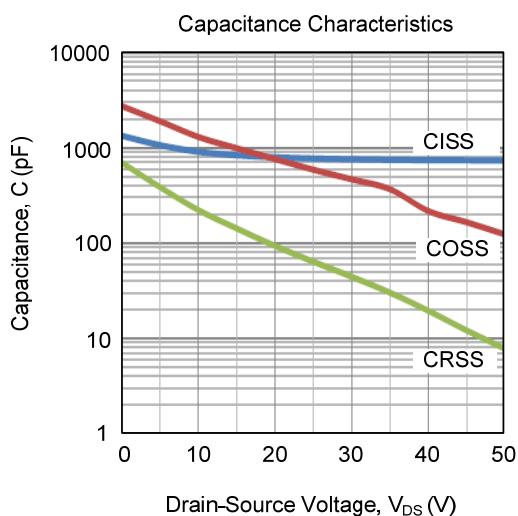
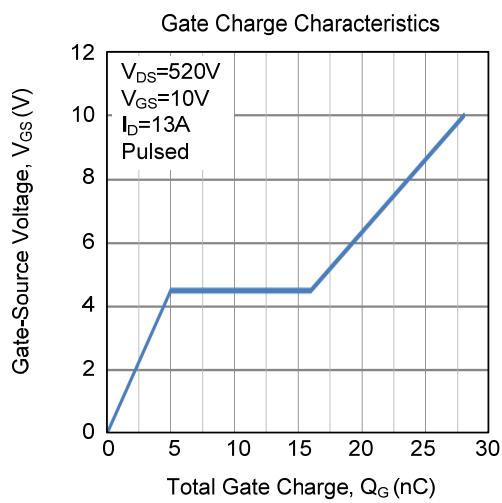
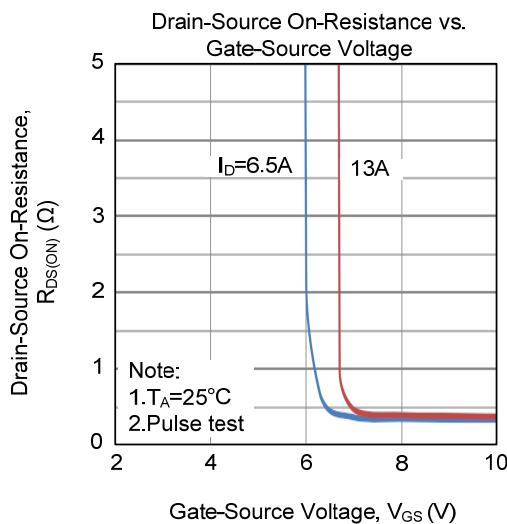
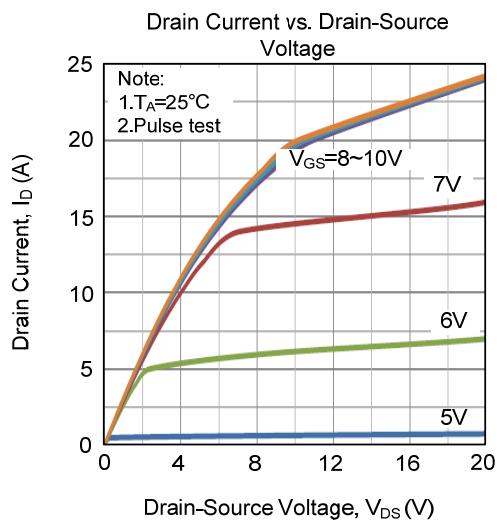
**Gate Charge Waveform**



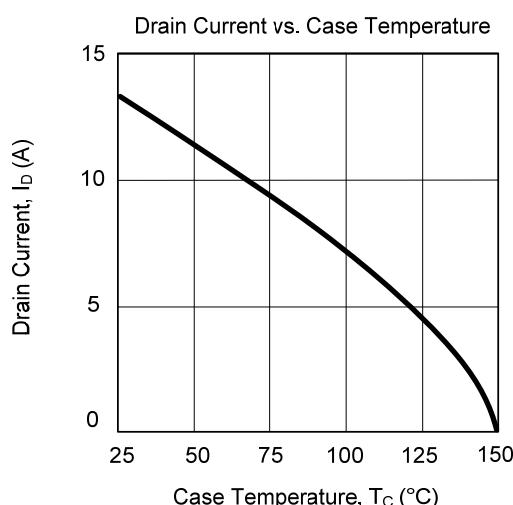
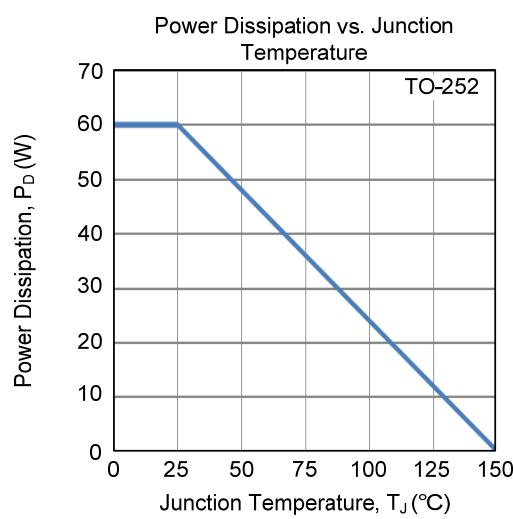
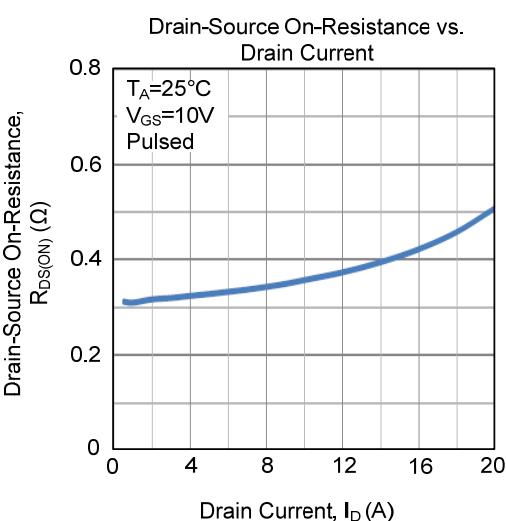
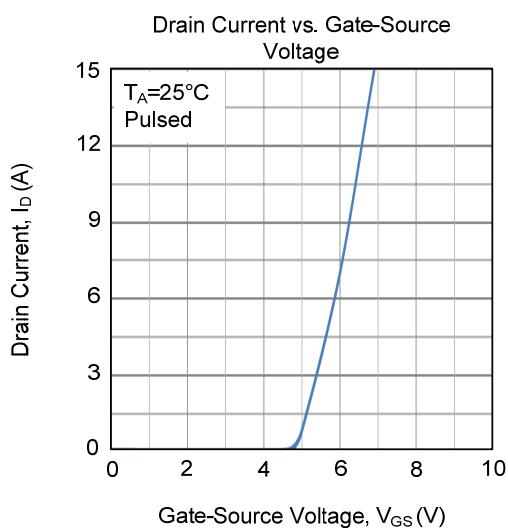
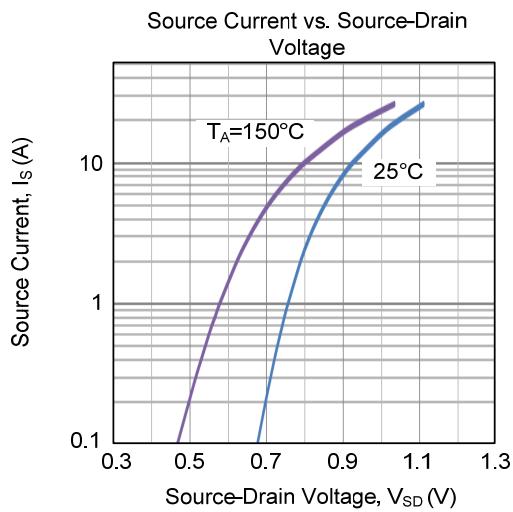
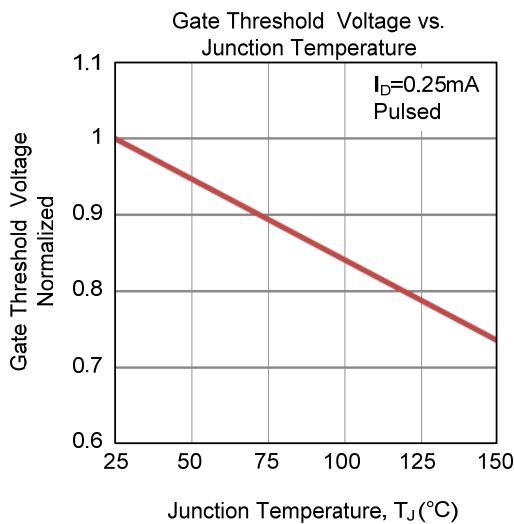
**Unclamped Inductive Switching Test Circuit**

**Unclamped Inductive Switching Waveforms**

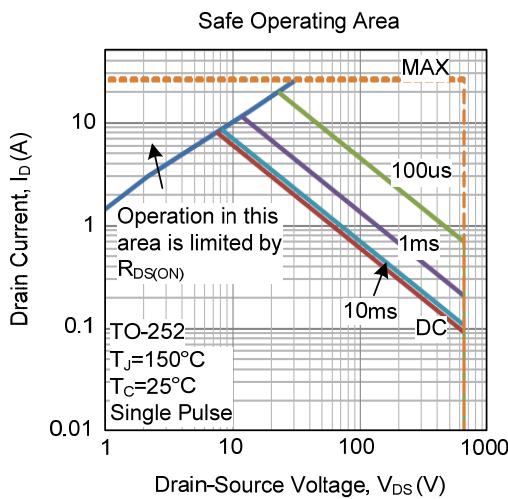
■ TYPICAL CHARACTERISTICS



■ TYPICAL CHARACTERISTICS (Cont.)



- TYPICAL CHARACTERISTICS (Cont.)



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