



## 15NM60-U3

Preliminary

Power MOSFET

### 15A, 600V N-CHANNEL SUPER-JUNCTION MOSFET

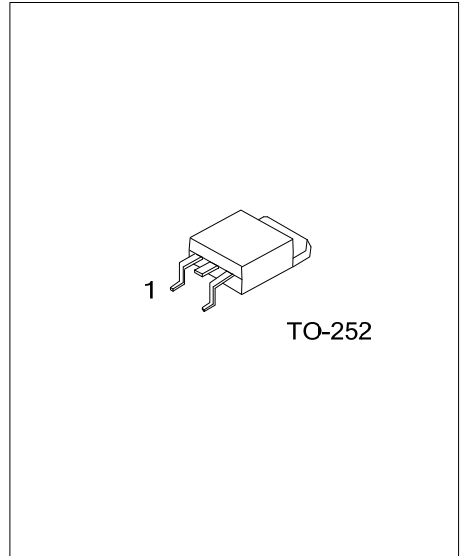
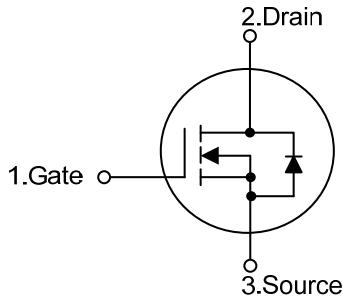
#### DESCRIPTION

The **UTC 15NM60-U3** is a Super Junction MOSFET Structure and is designed to have better characteristics, such as fast switching time, low gate charge, low on-state resistance and a high rugged avalanche characteristics. This power MOSFET is usually used at AC-DC converters for power applications.

#### FEATURES

- \*  $R_{DS(ON)} \leq 0.36 \Omega @ V_{GS}=10V, I_D=7.5A$
- \* Fast switching capability
- \* Avalanche energy tested
- \* Improved dv/dt capability, high ruggedness

#### SYMBOL



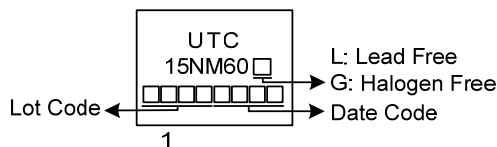
#### ORDERING INFORMATION

Ordering Number		Package	Pin Assignment			Packing
Lead Free	Halogen Free		1	2	3	
15NM60L-TN3-R	15NM60G-TN3-R	TO-252	G	D	S	Tape Reel

Note: Pin Assignment: G: Gate D: Drain S: Source

<p>15NM60G-TN3-R</p> <p>(1)Packing Type</p> <p>(2)Package Type</p> <p>(3)Green Package</p>	<p>(1) R: Tape Reel</p> <p>(2) TN3: TO-252</p> <p>(3) G: Halogen Free and Lead Free, L: Lead Free</p>
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#### MARKING



■ ABSOLUTE MAXIMUM RATINGS ( $T_C = 25^\circ\text{C}$ , unless otherwise specified)

PARAMETER		SYMBOL	RATINGS	UNIT	
Drain-Source Voltage		$V_{DSS}$	600	V	
Gate-Source Voltage		$V_{GSS}$	$\pm 30$	V	
Drain Current	Continuous	$T_C=25^\circ\text{C}$	15	A	
		$T_C=100^\circ\text{C}$	9.7	A	
	Pulsed (Note 2)		$I_{DM}$	45	A
Avalanche Energy	Single Pulsed (Note 3)		$E_{AS}$	338	mJ
Peak Diode Recovery $dv/dt$ (Note 4)		$dv/dt$	3.8	V/ns	
Power Dissipation		$P_D$	60	W	
Junction Temperature		$T_J$	+150	$^\circ\text{C}$	
Storage Temperature		$T_{STG}$	-55 ~ +150	$^\circ\text{C}$	

Notes: 1. Absolute maximum ratings are those values beyond which the device could be permanently damaged.

Absolute maximum ratings are stress ratings only and functional device operation is not implied.

2. Repetitive Rating: Pulse width limited by maximum junction temperature.

3.  $L=100\text{mH}$ ,  $I_{AS}=2.6\text{A}$ ,  $V_{DD}=90\text{V}$ ,  $R_G=25\Omega$ , Starting  $T_J = 25^\circ\text{C}$

4.  $I_{SD} \leq 30\text{A}$ ,  $di/dt \leq 200\text{A}/\mu\text{s}$ ,  $V_{DD} \leq BV_{DSS}$ , Starting  $T_J = 25^\circ\text{C}$

■ THERMAL DATA

PARAMETER	SYMBOL	RATING	UNIT
Junction to Ambient	$\theta_{JA}$	110	$^\circ\text{C}/\text{W}$
Junction to Case	$\theta_{JC}$	2.08	$^\circ\text{C}/\text{W}$

Note: Device mounted on FR-4 substrate PC board, 2oz copper, with 1inch square copper plate.

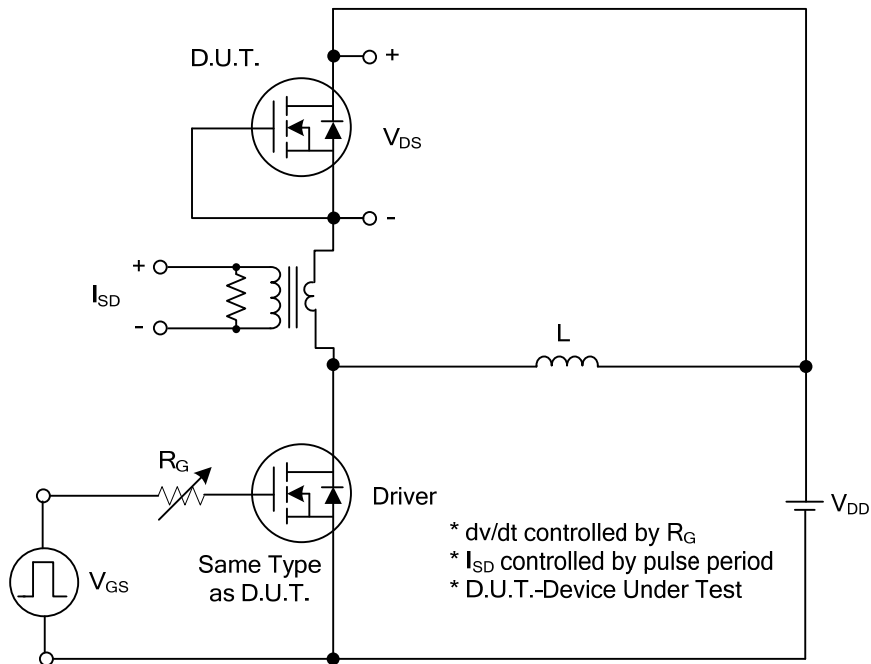
■ ELECTRICAL CHARACTERISTICS ( $T_J = 25^\circ\text{C}$ , unless otherwise specified)

PARAMETER	SYMBOL	TEST CONDITIONS	MIN	TYP	MAX	UNIT
<b>OFF CHARACTERISTICS</b>						
Drain-Source Breakdown Voltage	$BV_{DSS}$	$V_{GS}=0V, I_D=250\mu A$	600			V
Drain-Source Leakage Current	$I_{DSS}$	$V_{DS}=600V, V_{GS}=0V$			10	$\mu A$
Gate-Source Leakage Current	$I_{GSS}$	$V_{GS}=\pm 30V, V_{DS}=0V$			$\pm 100$	nA
<b>ON CHARACTERISTICS</b>						
Gate Threshold Voltage	$V_{GS(TH)}$	$V_{DS}=V_{GS}, I_D=250\mu A$	2.5		4.5	V
Static Drain-Source On-State Resistance	$R_{DS(ON)}$	$V_{GS}=10V, I_D=7.5A$			0.36	$\Omega$
<b>DYNAMIC CHARACTERISTICS</b>						
Input Capacitance	$C_{ISS}$	$V_{GS}=0V, V_{DS}=50V, f=1MHz$		870		pF
Output Capacitance	$C_{OSS}$			90		pF
Reverse Transfer Capacitance	$C_{RSS}$			5		pF
<b>SWITCHING CHARACTERISTICS</b>						
Total Gate Charge	$Q_G$	$V_{DS}=480V, V_{GS}=10V, I_D=15A$ (Note 1, 2)		45		nC
Gate-Source Charge	$Q_{GS}$			13		nC
Gate-Drain Charge	$Q_{DD}$			16		nC
Turn-On Delay Time	$t_{D(ON)}$	$V_{DD}=100V, V_{GS}=10V, I_D=15A,$ $R_G=25\Omega$ (Note 1, 2)		8		ns
Turn-On Rise Time	$t_R$			24		ns
Turn-Off Delay Time	$t_{D(OFF)}$			100		ns
Turn-Off Fall Time	$t_F$			44		ns
<b>SOURCE- DRAIN DIODE RATINGS AND CHARACTERISTICS</b>						
Maximum Continuous Drain-Source Diode Forward Current	$I_S$				15	A
Maximum Pulsed Drain-Source Diode Forward Current	$I_{SM}$				45	A
Drain-Source Diode Forward Voltage	$V_{SD}$	$I_S=15A, V_{GS}=0V$			1.4	V
Body Diode Reverse Recovery Time	$t_{rr}$	$I_S=15A, V_{GS}=0V,$ $dI_F/dt=100A/\mu s$		352		ns
Body Diode Reverse Recovery Charge	$Q_{rr}$			4.4		$\mu C$

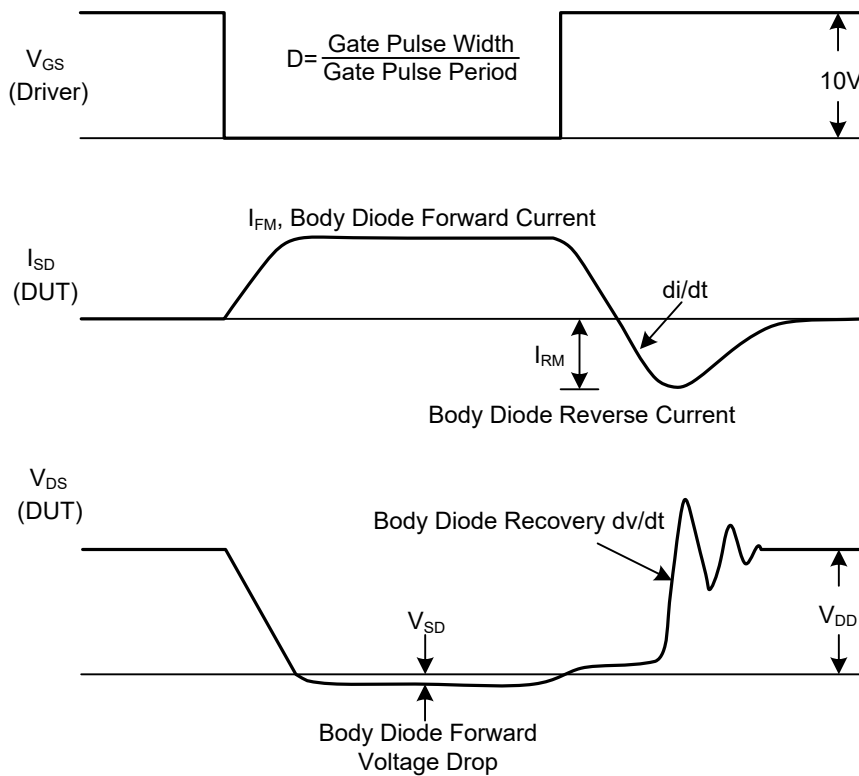
Notes: 1. Pulse Test: Pulse width  $\leq 300\mu s$ , Duty cycle  $\leq 2\%$ .

2. Essentially independent of operating temperature.

■ TEST CIRCUITS AND WAVEFORMS

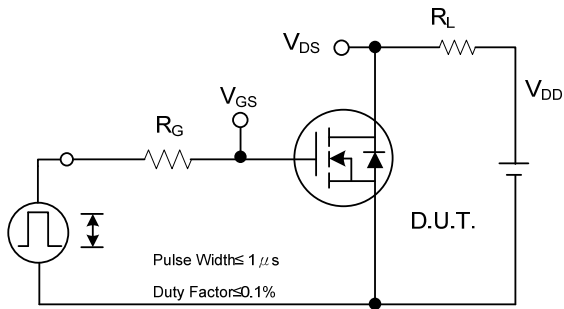


Peak Diode Recovery dv/dt Test Circuit

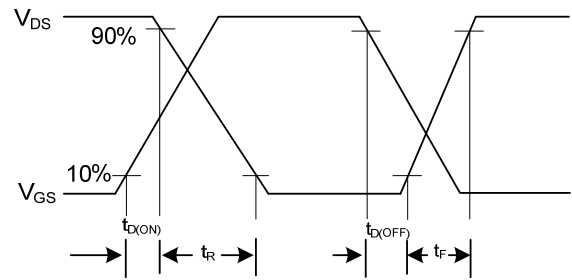


Peak Diode Recovery dv/dt Waveforms

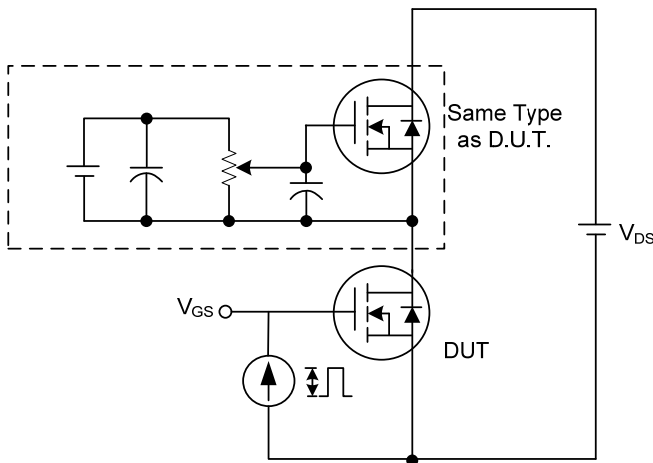
■ TEST CIRCUITS AND WAVEFORMS



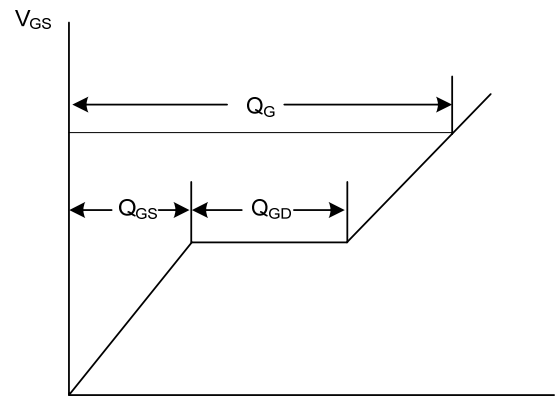
Switching Test Circuit



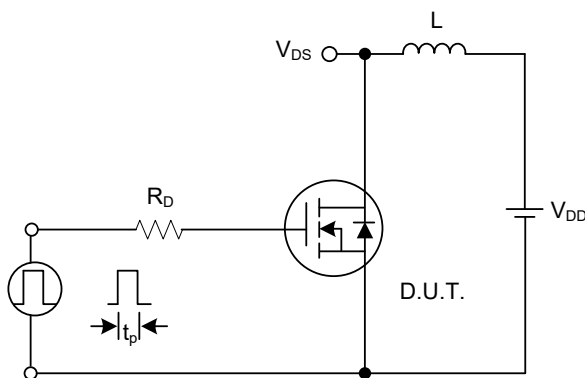
Switching Waveforms



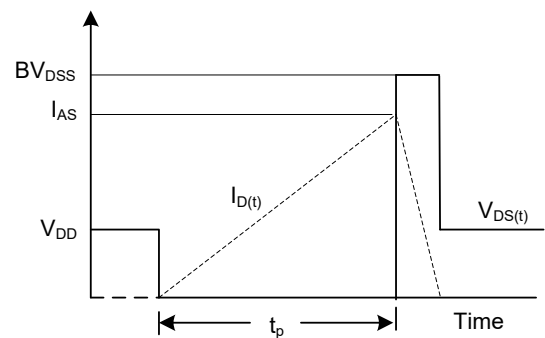
Gate Charge Test Circuit



Charge  
Gate Charge Waveform



Unclamped Inductive Switching Test Circuit



Unclamped Inductive Switching Waveforms

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