



16NM65

Power MOSFET

16A, 650V N-CHANNEL SUPER-JUNCTION MOSFET

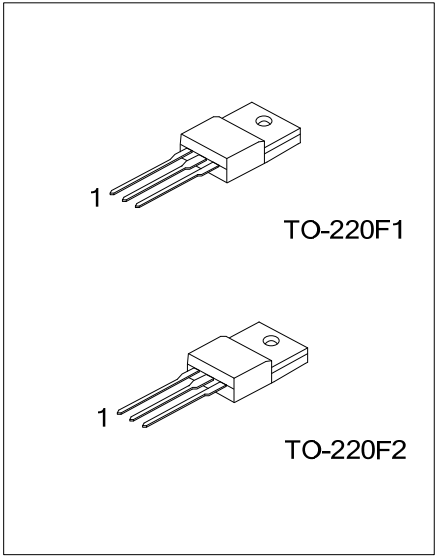
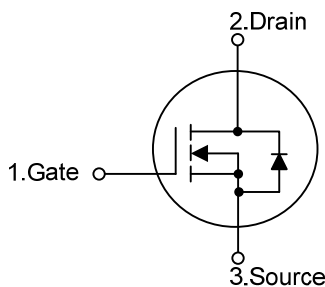
■ DESCRIPTION

The UTC **16NM65** is a Super Junction MOSFET Structure and is designed to have better characteristics, such as fast switching time, low gate charge, low on-state resistance and a high rugged avalanche characteristics. This power MOSFET is usually used at AC-DC converters for power applications.

■ FEATURES

- * $R_{DS(ON)} \leq 0.35 \Omega @ V_{GS}=10V, I_D=8.0A$
- * High Switching Speed

■ SYMBOL



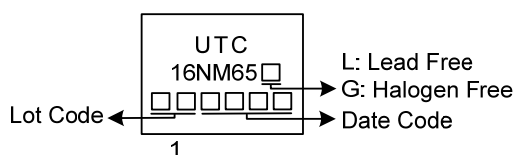
■ ORDERING INFORMATION

Ordering Number		Package	Pin Assignment			Packing
Lead Free	Halogen Free		1	2	3	
16NM65L-TF1-T	16NM65G-TF1-T	TO-220F1	G	D	S	Tube
16NM65L-TF2-T	16NM65G-TF2-T	TO-220F2	G	D	S	Tube

Note: Pin Assignment: G: Gate D: Drain S: Source

<p>16NM65G-TF1-T</p> <p>(1)Packing Type</p> <p>(2)Package Type</p> <p>(3)Green Package</p>	<p>(1) T: Tube</p> <p>(2) TF1: TO-220F1, TF2: TO-220F2</p> <p>(3) G: Halogen Free and Lead Free, L: Lead Free</p>
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■ MARKING



■ ABSOLUTE MAXIMUM RATINGS (unless otherwise specified)

PARAMETER	SYMBOL	RATINGS	UNIT
Drain-Source Voltage	V_{DSS}	650	V
Gate-Source Voltage	V_{GSS}	± 30	V
Continuous Drain Current	Continuous	I_D	16
	Pulsed	I_{DM}	48
Single Pulsed Avalanche Energy	E_{AS}	480	mJ
Peak Diode Recovery dv/dt (Note 4)	dv/dt	1.6	V/ns
Power Dissipation	P_D	31	W
Junction Temperature	T_J	+150	$^{\circ}C$
Storage Temperature	T_{STG}	-55 ~ +150	$^{\circ}C$

Notes: 1. Absolute maximum ratings are those values beyond which the device could be permanently damaged.

Absolute maximum ratings are stress ratings only and functional device operation is not implied.

2. Repetitive Rating: Pulse width limited by maximum junction temperature.

3. $L = 100mH$, $I_{AS} = 3.1A$, $V_{DD} = 50V$, $R_G = 25\Omega$, Starting $T_J = 25^{\circ}C$

4. $I_{SD} \leq 16A$, $di/dt \leq 200A/\mu s$, $V_{DD} \leq BV_{DSS}$, Starting $T_J = 25^{\circ}C$

■ THERMAL DATA

PARAMETER	SYMBOL	RATING	UNIT
Junction to Ambient	θ_{JA}	62.5	$^{\circ}C/W$
Junction to Case	θ_{JC}	4.03	$^{\circ}C/W$

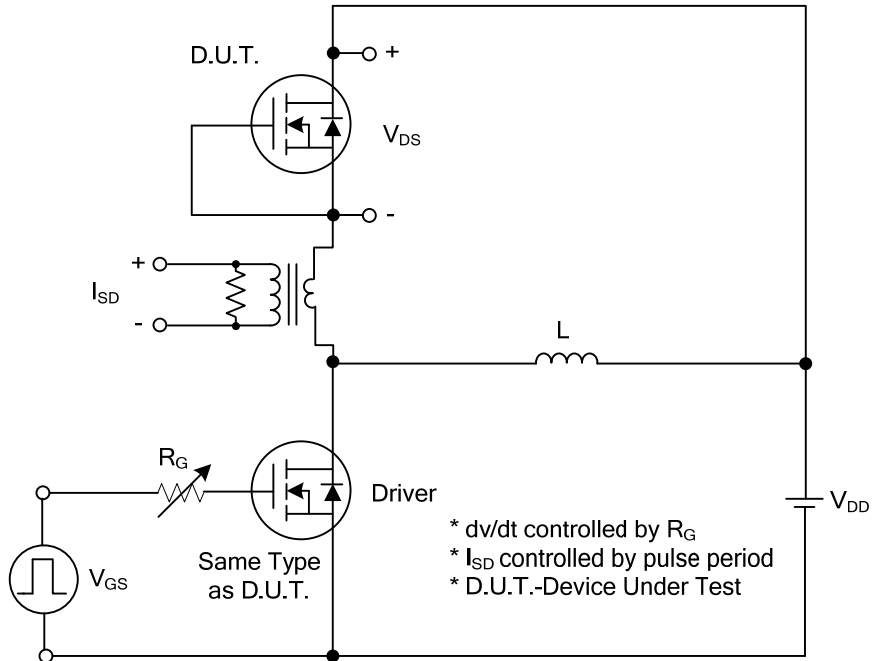
■ ELECTRICAL CHARACTERISTICS

PARAMETER	SYMBOL	TEST CONDITIONS	MIN	TYP	MAX	UNIT	
OFF CHARACTERISTICS							
Drain-Source Breakdown Voltage	BV_{DSS}	$I_D=250\mu A$, $V_{GS}=0V$	650			V	
Drain-Source Leakage Current	I_{DSS}	$V_{DS}=650V$, $V_{GS}=0V$			10	μA	
Gate-Source Leakage Current	Forward	I_{GSS}			+100	nA	
	Reverse				-100	nA	
ON CHARACTERISTICS							
Gate Threshold Voltage	$V_{GS(TH)}$	$V_{DS}=V_{GS}$, $I_D=250\mu A$	2.5		4.5	V	
Static Drain-Source On-State Resistance	$R_{DS(ON)}$	$V_{GS}=10V$, $I_D=8.0A$			0.35	Ω	
DYNAMIC PARAMETERS							
Input Capacitance	C_{ISS}	$V_{GS}=0V$, $V_{DS}=50V$, $f=1.0MHz$		940		pF	
Output Capacitance	C_{OSS}				150		pF
Reverse Transfer Capacitance	C_{RSS}				6.6		pF
SWITCHING PARAMETERS							
Total Gate Charge	Q_G	$V_{DS}=520V$, $V_{GS}=10V$, $I_D=16A$ (Note 1, 2)		50		nC	
Gate to Source Charge	Q_{GS}			13		nC	
Gate to Drain Charge	Q_{GD}			20		nC	
Turn-ON Delay Time	$t_{D(ON)}$	$V_{DD}=100V$, $V_{GS}=10V$, $I_D=16A$, $R_G=25\Omega$ (Note 1, 2)		12		ns	
Rise Time	t_R			28		ns	
Turn-OFF Delay Time	$t_{D(OFF)}$			110		ns	
Fall-Time	t_F			40		ns	
SOURCE- DRAIN DIODE RATINGS AND CHARACTERISTICS							
Maximum Body-Diode Continuous Current	I_S				16	A	
Maximum Body-Diode Pulsed Current	I_{SM}				48	A	
Drain-Source Diode Forward Voltage	V_{SD}	$I_S=16A$, $V_{GS}=0V$			1.4	V	
Body Diode Reverse Recovery Time	t_{rr}	$I_S=16A$, $V_{GS}=0V$, $dI_F/dt=100A/\mu s$ (Note 1)		420		ns	
Reverse Recovery Charge	Q_{rr}				7.1		μC

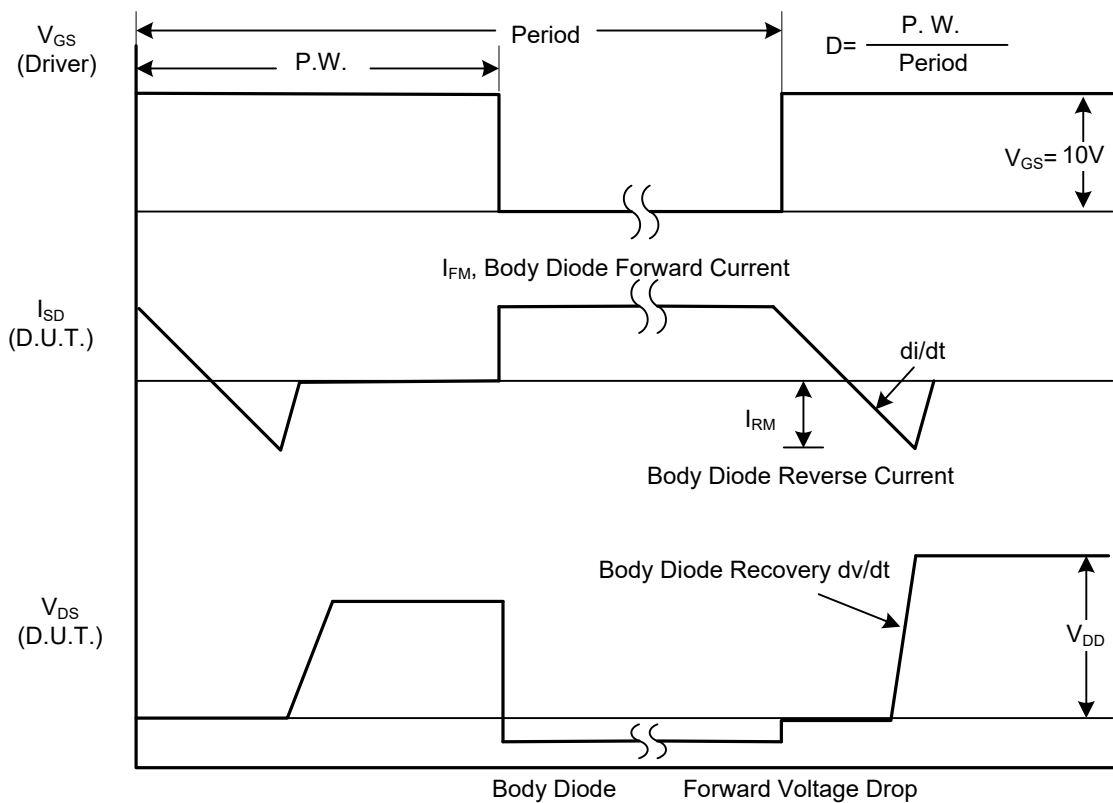
Notes: 1. Pulse Test: Pulse width $\leq 650\mu s$, Duty cycle $\leq 2\%$.

2. Essentially independent of operating temperature.

TEST CIRCUITS AND WAVEFORMS

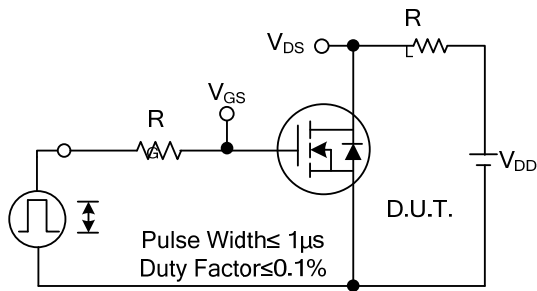


Peak Diode Recovery dv/dt Test Circuit

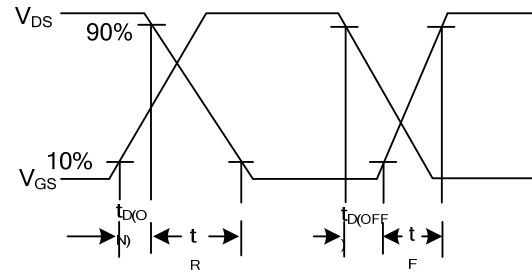


Peak Diode Recovery dv/dt Waveforms

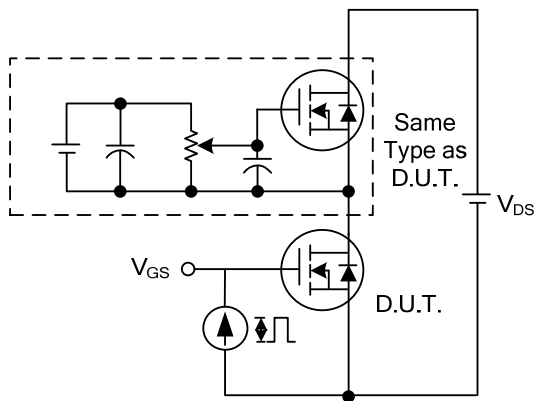
TEST CIRCUITS AND WAVEFORMS



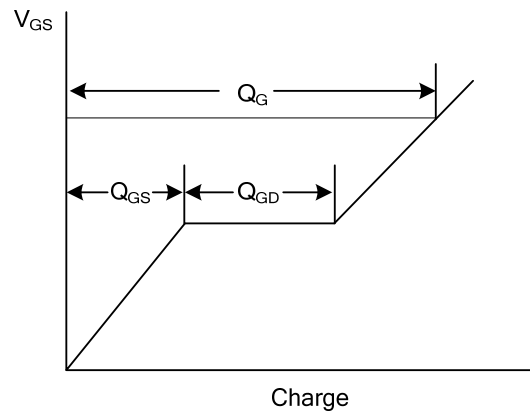
Switching Test Circuit



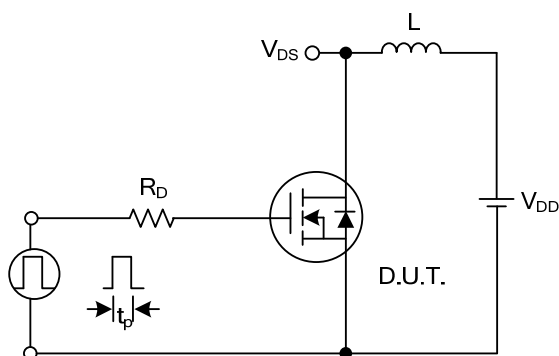
Switching Waveforms



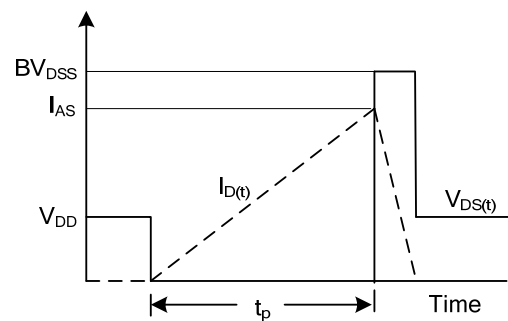
Gate Charge Test Circuit



Gate Charge Waveform

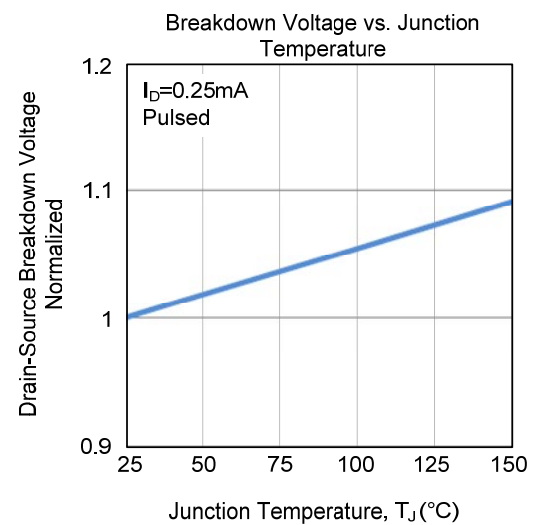
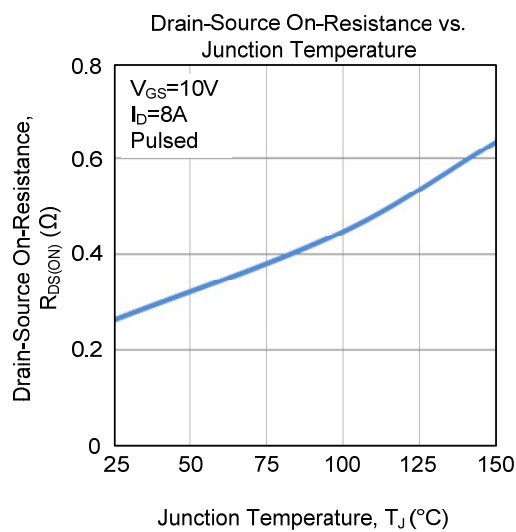
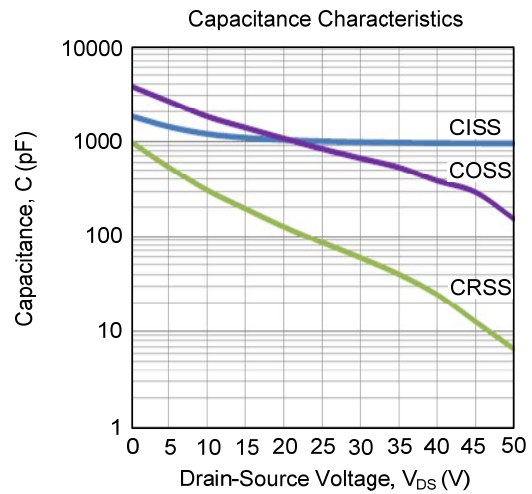
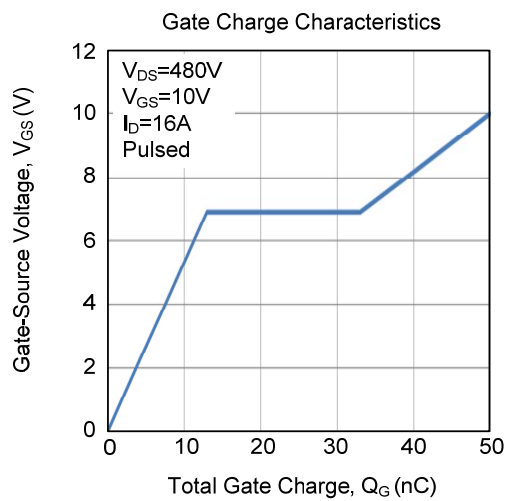
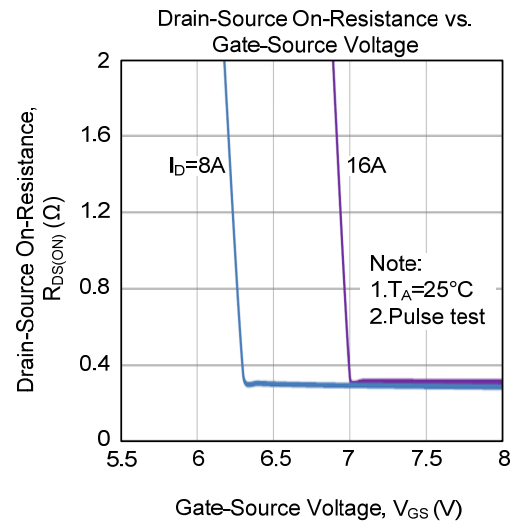
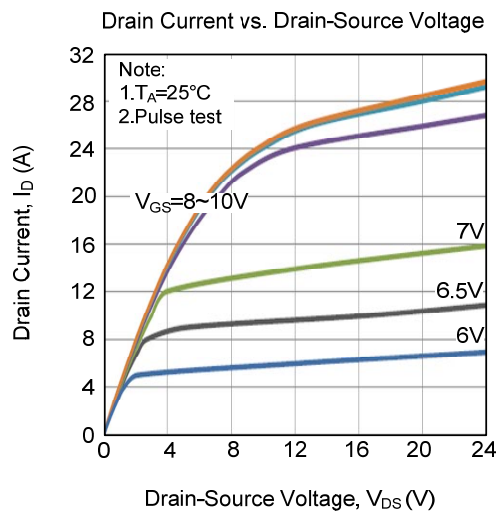


Unclamped Inductive Switching Test Circuit

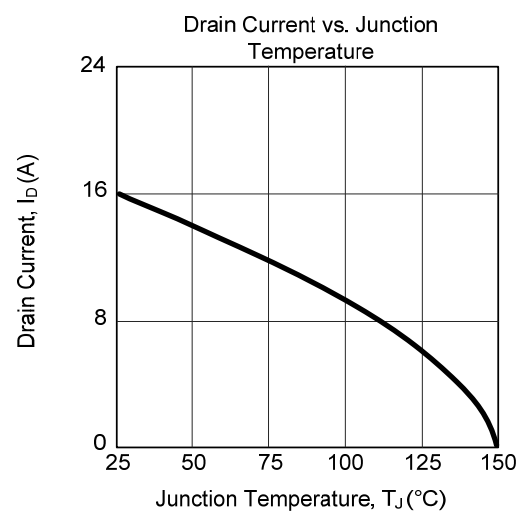
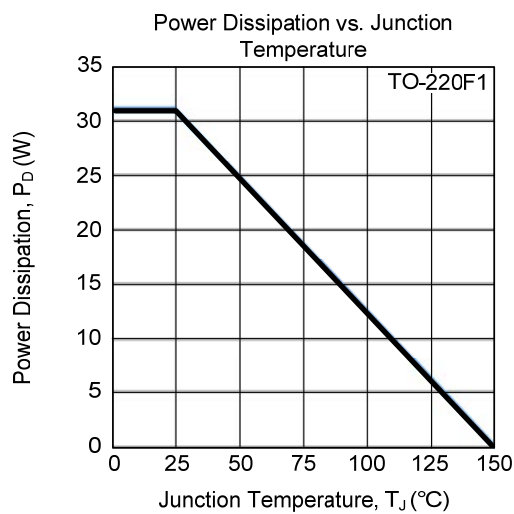
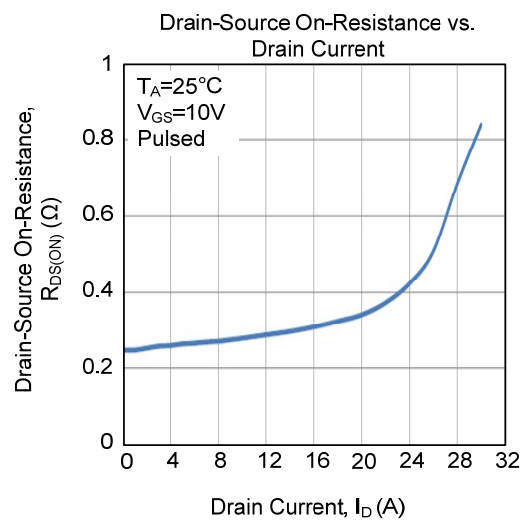
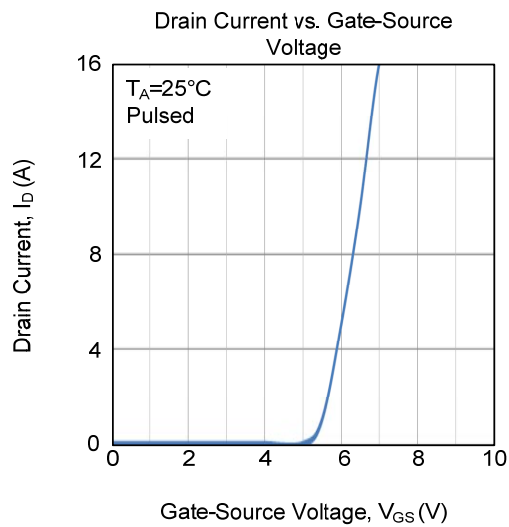
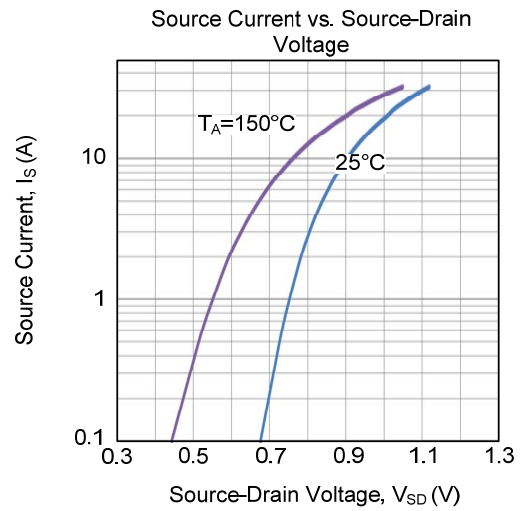
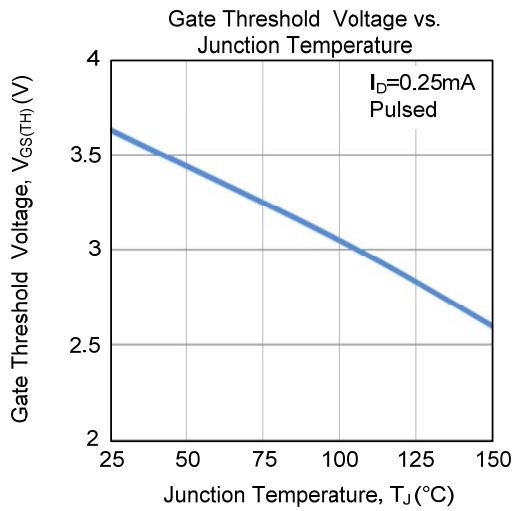


Unclamped Inductive Switching Waveforms

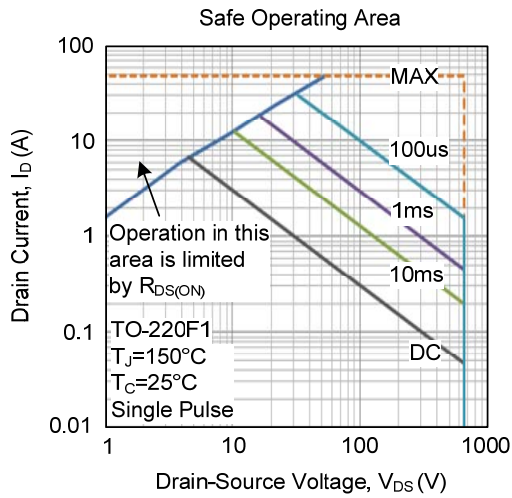
TYPICAL CHARACTERISTICS



■ TYPICAL CHARACTERISTICS (Cont.)



■ TYPICAL CHARACTERISTICS (Cont.)



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