

## 1000W, 10V - 100V Surface Mount Transient Voltage Suppressor

### FEATURES

- Low profile package
- Ideal for automated placement
- Glass passivated junction
- Built-in strain relief
- Excellent clamping capability
- Fast response time: Typically less than 1.0ps from 0 volt to BV min
- Compliant to RoHS Directive 2011/65/EU and in accordance to WEEE 2002/96/EC
- Halogen-free according to IEC 61249-2-21 definition
- AEC-Q101 qualified



**DO-214AA (SMB)**

### MECHANICAL DATA

**Case:** DO-214AA (SMB)

Molding compound, UL flammability classification rating 94V-0

Moisture sensitivity level: level 1, per J-STD-020

Packing code with suffix "G" means green compound (halogen-free)

**Terminal:** Matte tin plated leads, solderable per JESD22-B102

Meet JESD 201 class 2 whisker test

**Polarity:** Indicated by cathode band

**Weight:** 0.11 g (approximately)

MAXIMUM RATINGS AND ELECTRICAL CHARACTERISTICS (T <sub>A</sub> =25°C unless otherwise noted)			
PARAMETER	SYMBOL	VALUE	UNIT
Peak power dissipation at T <sub>A</sub> =25°C, tp=1ms (Note 1)	P <sub>PK</sub>	1000	Watts
Steady state power dissipation	P <sub>D</sub>	5	Watts
Peak forward surge current, 8.3 ms single half sine-wave superimposed on rated load	I <sub>FSM</sub>	100	A
Maximum instantaneous forward voltage at 50 A for Unidirectional only (Note 2)	V <sub>F</sub>	3.5 / 5.0	Volts
Typical thermal resistance	R <sub>θJL</sub> R <sub>θJA</sub>	20 100	°C/W
Operating junction temperature range	T <sub>J</sub>	- 55 to +175	°C
Storage temperature range	T <sub>STG</sub>	- 55 to +175	°C

Note 1: Non-repetitive Current Pulse Per Fig. 3 and Derated above T<sub>A</sub>=25°C Per Fig. 2

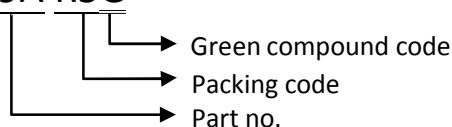
Note 2: V<sub>F</sub>=3.5V for Devices of V<sub>BR</sub> ≤ 50V and V<sub>F</sub>=5.0V Max. for Devices V<sub>BR</sub> > 50V

### Devices for Bipolar Applications

1. For Bidirectional use CA suffix

### ORDER INFORMATION (EXAMPLE)

**1KSMB39A R5G**



RATINGS AND CHARACTERISTICS CURVES

( $T_A=25^\circ\text{C}$  unless otherwise noted)

FIG. 1 PEAK PULSE POWER RATING CURVE

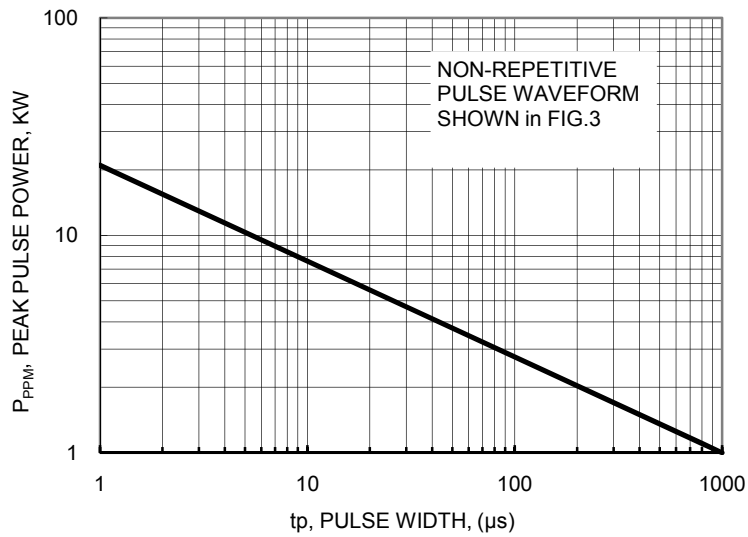


FIG.2 PULSE DERATING CURVE

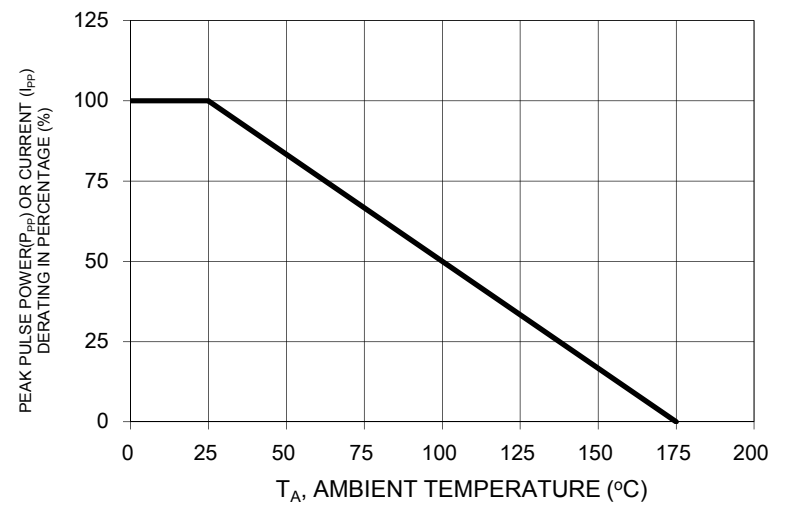


FIG. 3 CLAMPING POWER PULSE WAVEFORM

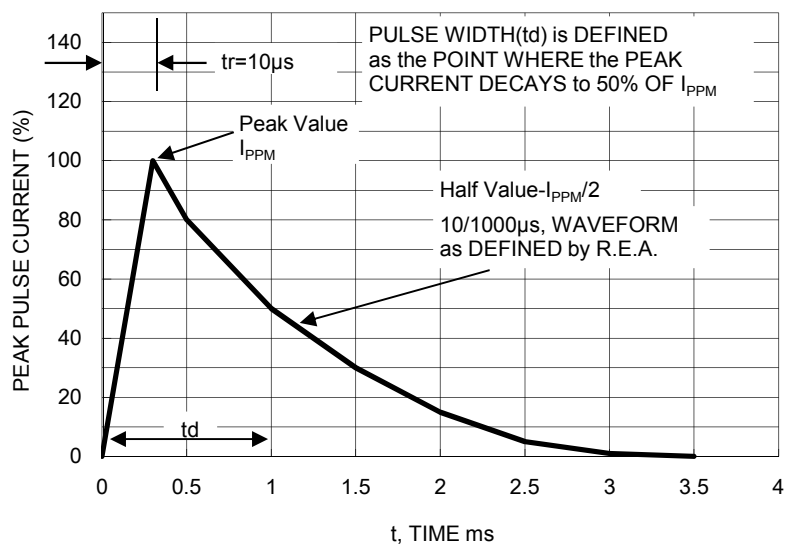


FIG. 4 MAXIMUM NON-REPETITIVE FORWARD SURGE CURRENT UNIDIRECTIONAL ONLY

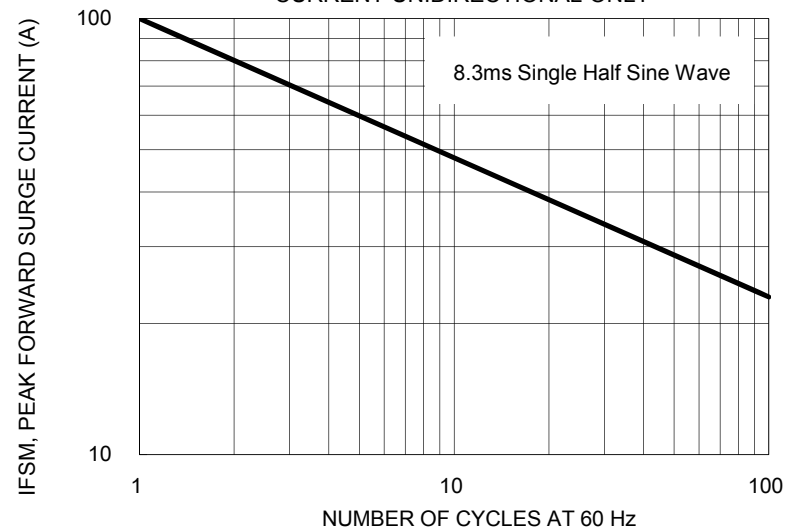
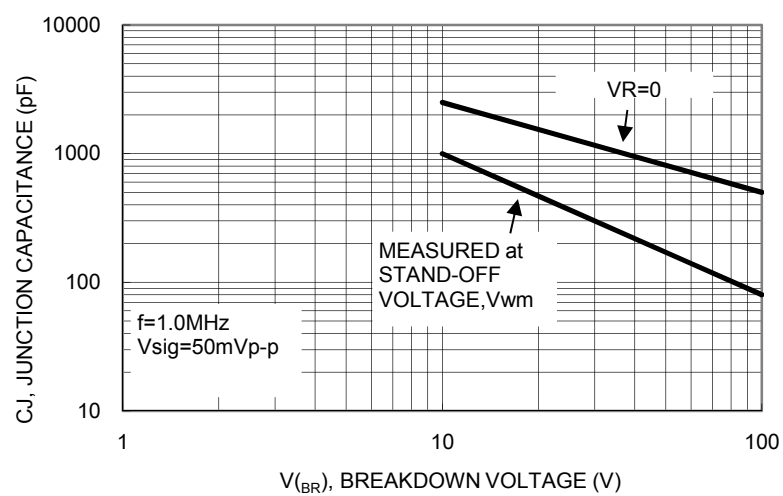


FIG. 5 TYPICAL JUNCTION CAPACITANCE



Device	Device Marking Code	Breakdown Voltage $V_{BR}$ (V) (Note 1)		Test Current $I_T$ (mA)	Stand-Off Voltage $V_{WM}$ (V)	Maximum Reverse Leakage @ $V_{WM}$ $I_D$ ( $\mu$ A)	Maximum Peak Pulse Current $I_{PPM}$ (A) (Note 2)	Maximum Clamping Voltage @ $I_{PPM}$ $V_C$ (V)
		Min	Max					
1KSMB10A	A10E	9.5	10.5	1.0	8.55	10.0	69.0	14.5
1KSMB10CA	N10E							
1KSMB11A	A10F	10.5	11.6	1.0	9.40	5.0	64.1	15.6
1KSMB11CA	N10F							
1KSMB12A	A10G	11.4	12.6	1.0	10.2	5.0	59.9	16.7
1KSMB12CA	N10G							
1KSMB13A	A10H	12.4	13.7	1.0	11.1	5.0	54.9	18.2
1KSMB13CA	N10H							
1KSMB15A	A10I	14.3	15.8	1.0	12.8	5.0	47.2	21.2
1KSMB15CA	N10I							
1KSMB16A	A10J	15.2	16.8	1.0	13.6	1.0	44.4	22.5
1KSMB16CA	N10J							
1KSMB18A	A10K	17.1	18.9	1.0	15.3	1.0	39.2	25.5
1KSMB18CA	N10K							
1KSMB20A	A10L	19.0	21.0	1.0	17.1	1.0	36.1	27.7
1KSMB20CA	N10L							
1KSMB22A	A10M	20.9	23.1	1.0	18.8	1.0	32.7	30.6
1KSMB22CA	N10M							
1KSMB24A	A10N	22.8	25.2	1.0	20.5	1.0	30.1	33.2
1KSMB24CA	N10N							
1KSMB27A	A10O	25.7	28.4	1.0	23.1	1.0	26.7	37.5
1KSMB27CA	N10O							
1KSMB30A	A10P	28.5	31.5	1.0	25.6	1.0	24.2	41.4
1KSMB30CA	N10P							
1KSMB33A	A10Q	31.4	34.7	1.0	28.2	1.0	21.9	45.7
1KSMB33CA	N10Q							
1KSMB36A	A10R	34.2	37.8	1.0	30.8	1.0	20.0	49.9
1KSMB36CA	N10R							
1KSMB39A	A10S	37.1	41.0	1.0	33.3	1.0	18.6	53.9
1KSMB39CA	N10S							
1KSMB43A	A10T	40.9	45.2	1.0	36.8	1.0	16.9	59.3
1KSMB43CA	N10T							
1KSMB47A	A10U	44.7	49.4	1.0	40.2	1.0	15.4	64.8
1KSMB47CA	N10U							
1KSMB51A	A10V	48.5	53.6	1.0	43.6	1.0	14.3	70.1
1KSMB51CA	N10V							
1KSMB56A	A10W	53.2	58.8	1.0	47.8	1.0	13.0	77.0
1KSMB56CA	N10W							
1KSMB62A	A10X	58.9	65.1	1.0	53.0	1.0	11.8	85.0
1KSMB62CA	N10X							
1KSMB68A	A10Y	64.6	71.4	1.0	58.1	1.0	10.9	92.0
1KSMB68CA	N10Y							
1KSMB75A	A10Z	71.3	78.8	1.0	64.1	1.0	9.7	103
1KSMB75CA	N10Z							
1KSMB82A	B10A	77.9	86.1	1.0	70.1	1.0	8.8	113
1KSMB82CA	O10A							

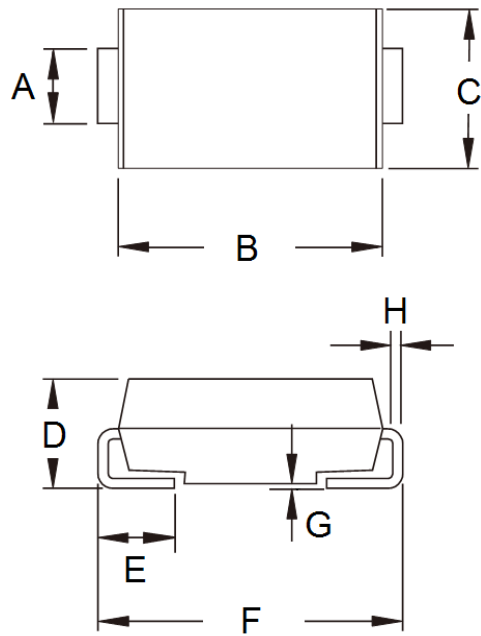
Device	Device Marking Code	Breakdown Voltage $V_{BR}$ (V) (Note 1)		Test Current $I_T$ (mA)	Stand-Off Voltage $V_{WM}$ (V)	Maximum Reverse Leakage @ $V_{WM}$ $I_D$ ( $\mu$ A)	Maximum Peak Pulse Current $I_{PPM}$ (A) (Note 2)	Maximum Clamping Voltage @ $I_{PPM}$ $V_C$ (V)
		Min	Max					
1KSMB91A	B10B	86.5	95.5	1.0	77.8	1.0	8.0	125
1KSMB91CA	O10B							
1KSMB100A	B10C	95	105	1.0	85.5	1.0	7.3	137
1KSMB100CA	O10C							

**Notes:**

1.  $V_{BR}$  measure after  $I_T$  applied for 300 $\mu$ s,  $I_T$ =square wave pulse or equivalent.
2. All terms and symbols are consistent with ANSI/IEEE C62.35.
3. For Bidirectional use CA suffix

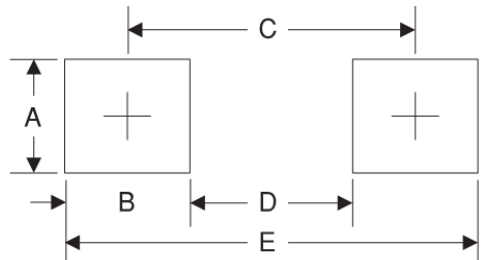
PACKAGE OUTLINE DIMENSIONS

**DO-214AA (SMB)**



DIM.	Unit (mm)		Unit (inch)	
	Min	Max	Min	Max
A	1.95	2.10	0.077	0.083
B	4.25	4.75	0.167	0.187
C	3.48	3.73	0.137	0.147
D	1.99	2.61	0.078	0.103
E	0.90	1.41	0.035	0.056
F	5.10	5.30	0.201	0.209
G	0.10	0.20	0.004	0.008
H	0.15	0.31	0.006	0.012

SUGGESTED PAD LAYOUT



Symbol	Unit (mm)	Unit (inch)
A	2.3	0.091
B	2.5	0.098
C	4.3	0.169
D	1.8	0.071
E	6.8	0.268

MARKING DIAGRAM



- P/N = Specific Device Code
- G = Green Compound
- YW = Date Code
- F = Factory Code

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