

## 1MBI400VF-120-50

**IGBT Modules** 

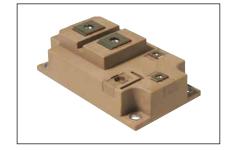
# IGBT MODULE (V series) 1200V / 400A / 1 in one package

#### Features

High speed switching Voltage drive Low Inductance module structure

#### Applications

Inverter for Motor Drive AC and DC Servo Drive Amplifier Uninterruptible Power Supply Industrial machines, such as Welding machines



#### **■** Maximum Ratings and Characteristics

#### ● Absolute Maximum Ratings (at T<sub>c</sub>=25°C unless otherwise specified)

Items	ems		Conditions		Maximum ratings	Units	
Collector-Emitter voltage		Vces			1200	V	
Gate-Emitter voltage		V <sub>GES</sub>			±20		
Collector current		Ic	Continuous	Tc=100°C	400		
				Tc=25°C	480		
		C pulse	1ms		800	Α	
		-lc			400		
		-Ic pulse	1ms		800		
Collector power dissipation		Pc	1 device		3330	W	
Junction temperature		Tj			175		
Operating junction temperature (under switching conditions)		Тјор			150	°C	
Case temperature		Tc		125			
Storage temperature		T <sub>stg</sub>	-40~+125				
Isolation voltage	Between terminal and copper base (*1)	Viso	AC: 1min.	·	2500	VAC	
Screw torque	Mounting (*2)	-			6.0		
	Terminals (*3)	M4			2.0	N m	
		M6			5.0		

Note \*1: All terminals should be connected together during the test.

Note \*2: Recommendable Value : 1.96-6.0 Nm (M5, M6) Grease type : Shin-Etsu Chemical Co.,Ltd "G-747" Note \*3: Recommendable Value : 0.98-2.0 Nm (M4) Recommendable Value : 1.96-5.0 Nm (M6)

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#### ● Electrical characteristics (at T<sub>i</sub>= 25°C unless otherwise specified)

Itama	Cymphala	Conditions		Characteristics			Units
Items	Symbols			min.	typ.	max.	Units
Zero gate voltage collector current	o gate voltage collector current Ices VGE = 0V, VCE = 1		= 1200V		-	4.0	mA
Gate-Emitter leakage current	Iges	V <sub>CE</sub> = 0V, V <sub>GE</sub> = ±20\	V <sub>CE</sub> = 0V, V <sub>GE</sub> = ±20V		-	400	nA
Gate-Emitter threshold voltage	V <sub>GE (th)</sub>	V <sub>CE</sub> = 20V, I <sub>C</sub> = 400mA		6.0	6.5	7.0	V
	.,		T <sub>j</sub> =25°C	-	1.95	2.40	V
	V <sub>CE (sat)</sub> (terminal)		T <sub>j</sub> =125°C	-	2.25	-	
Callagton Emitter activistics valtage	(terminal)	V <sub>GE</sub> = 15V	T <sub>j</sub> =150°C		2.30		
Collector-Emitter saturation voltage	V <sub>CE</sub> (sat)	Ic = 400A	T <sub>j</sub> =25°C	-	1.75	2.15	
			T <sub>j</sub> =125°C	-	2.05	-	
	(chip)		T <sub>j</sub> =150°C		2.10		
Internal gate resistance	gate resistance R <sub>G(int)</sub> -		<u>'</u>	-	1.9	-	Ω
Input capacitance	Cies	V <sub>GE</sub> = 0V, V <sub>CE</sub> = 10V, f = 1MHz		-	36	-	nF
	ton			-	600	-	
Turn-on time	tr	Vcc = 600V, Ic = 400	-	200	-	nsec	
	t <sub>r(i)</sub>	V <sub>GE</sub> = ±15V, R <sub>G</sub> = 1.8	-	80	-		
T off time :	toff	T <sub>j</sub> =150°C, L <sub>s</sub> =35nH	T <sub>j</sub> =150°C, L <sub>s</sub> =35nH			-	1
Turn-off time	t <sub>f</sub>		-	140	-	1	
	VF		T <sub>j</sub> =25°C	-	1.85	2.35	V
	(terminal)	V <sub>GE</sub> = 0V I <sub>F</sub> = 400A	T <sub>j</sub> =125°C	-	2.00	-	
P			T <sub>j</sub> =150°C		1.95		
Forward on voltage	VF		T <sub>j</sub> =25°C	-	1.70	2.15	
	(chip)		T <sub>j</sub> =125°C	-	1.85	-	
			T <sub>j</sub> =150°C		1.80		
Reverse recovery time	trr	I <sub>F</sub> = 400A		-	200	-	nsec

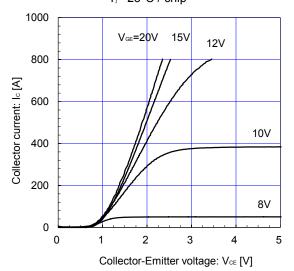
#### ● Thermal resistance characteristics

Items	Symbols	Conditions	Characteristics			Units	
items		Conditions	min.	typ.	max.	Ullits	
Thermal registeres (4 device)	R <sub>th(j-c)</sub>	IGBT	-	-	0.045		
Thermal resistance (1device)		FWD	-	-	0.077	°C/W	
Contact thermal resistance (1device) (*4)	R <sub>th(c-f)</sub>	with Thermal Compound	-	0.0125	-		

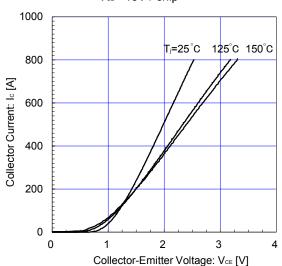
Note \*4: This is the value which is defined mounting on the additional cooling fin with thermal compound.

#### **■** Characteristics (Representative)

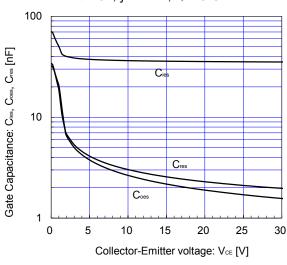
Collector current vs. Collector-Emitter voltage (typ.)  $T_i$ = 25°C / chip



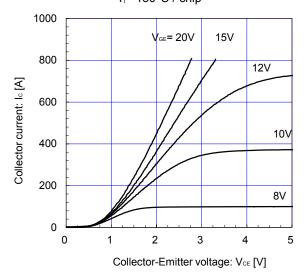
Collector current vs. Collector-Emitter voltage (typ.)  $V_{\text{GE}}$ = 15V / chip



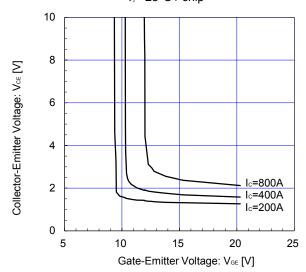
Gate Capacitance vs. Collector-Emitter Voltage  $V_{GE}$  = 0V, f = 1MHz,  $T_i$  = 25°C



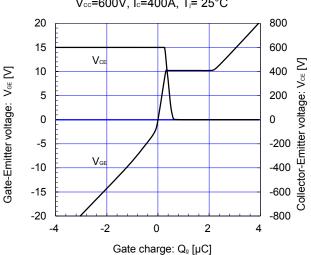
Collector current vs. Collector-Emitter voltage (typ.) T<sub>,</sub>= 150°C / chip

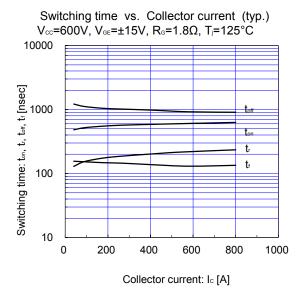


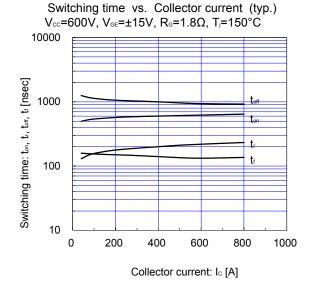
Collector-Emitter voltage vs. Gate-Emitter voltage  $T_i$ = 25°C / chip

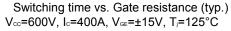


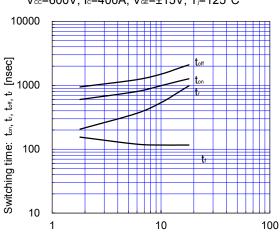
Dynamic Gate Charge (typ.) V<sub>cc</sub>=600V, I<sub>c</sub>=400A, T<sub>i</sub>= 25°C



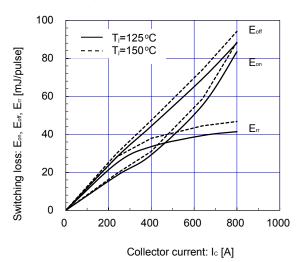






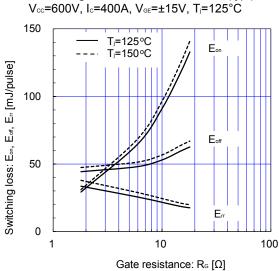


Switching loss vs. Collector current (typ.)  $V_{cc}$ =600V,  $V_{ce}$ =±15V,  $R_{c}$ =1.8 $\Omega$ ,  $T_{j}$ =125°C

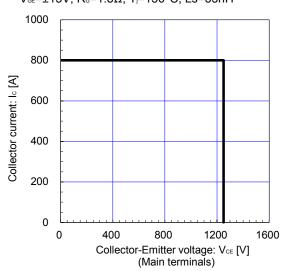


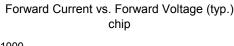
Switching loss vs. Gate resistance (typ.) Vcc=600V, Ic=400A, Vce=±15V, T=125°C

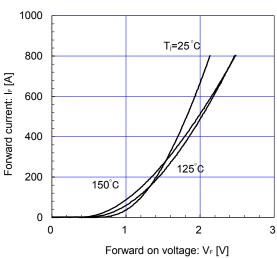
Gate resistance: R<sub>G</sub> [Ω]



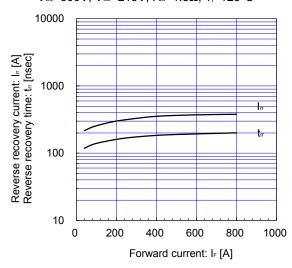
Reverse bias safe operating area (max.)  $V_{\text{GE}}$ =±15V,  $R_{\text{G}}$ =1.8 $\Omega$ ,  $T_{\text{J}}$ =150°C, Ls=35nH



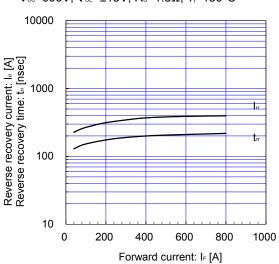




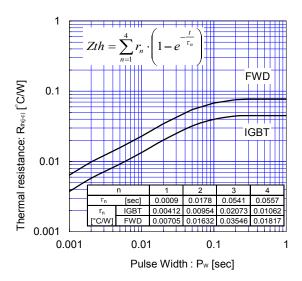
### Reverse Recovery Characteristics (typ.) $V_{\text{CC}}$ =600V, $V_{\text{CE}}$ =±15V, $R_{\text{C}}$ =1.8 $\Omega$ , $T_{\text{I}}$ =125°C



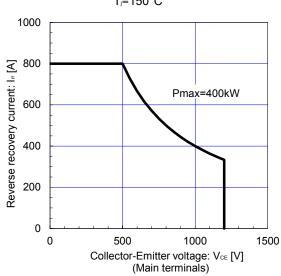
## Reverse Recovery Characteristics (typ.) $V_{cc}$ =600V, $V_{ce}$ =±15V, $R_s$ =1.8 $\Omega$ , $T_j$ =150°C



Transient Thermal Resistance (max.)

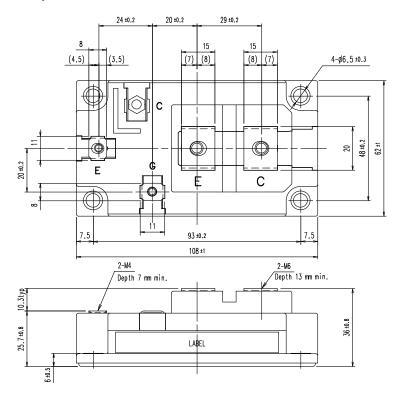


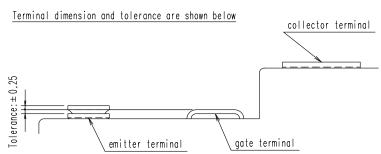
## FWD safe operating area (max.) $T_i$ =150°C



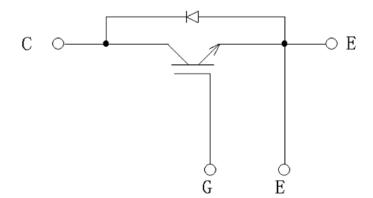
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#### ■ Outline Drawings(Unit:mm)





#### **■** Equivalent Circuit



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- Measurement equipment

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