

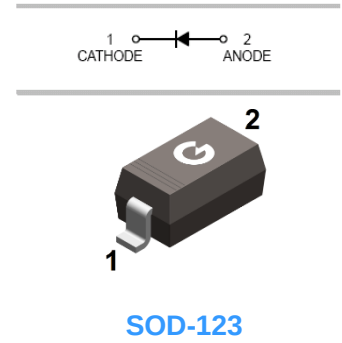
Features

- Silicon epitaxial planar diode
- For general purpose switching applications

HF

Mechanical Data

- Case: SOD-123
- Molding Compound: UL Flammability Classification Rating 94V-0
- Terminals: Matte tin-plated leads; solderability-per MIL-STD-202, Method 208



Ordering Information

Part Number	Package	Shipping Quantity	Marking Code
1N4150W	SOD-123	3000 pcs / Tape & Reel	50W

Maximum Ratings (@ T_A = 25°C unless otherwise specified)

Parameter	Symbol	Value	Unit
Peak Repetitive Peak Reverse Voltage	V _{RRM}	50	V
Working Peak Reverse Voltage	V _{RWM}	50	V
DC Blocking Voltage	V _R	50	V
Average Rectified Output Current	I _F	200	mA
Non-repetitive Peak Forward Surge Current, t = 1ms	I _{FSM}	1	A

Thermal Characteristics

Parameter	Symbol	Value	Unit
Power Dissipation	P _D	500	mW
Thermal Resistance Junction-to-Air *1	R _{θJA}	250	°C/W
Thermal Resistance Junction-to-Case *1	R _{θJC}	180	°C/W
Thermal Resistance Junction-to-Lead *1	R _{θJL}	200	°C/W
Operating Junction Temperature Range	T _J	-55 ~ +150	°C
Storage Temperature Range	T _{STG}	-55 ~ +150	°C

Note 1: The data tested by surface mounted on a 1 inch² FR-4 board with 20Z copper

Electrical Characteristics (@ $T_A = 25^\circ\text{C}$ unless otherwise specified)

Parameter	Symbol	Test Condition	Min.	Typ.	Max.	Unit
Reverse Breakdown Voltage	$V_{(BR)R}$	$I_R = 100\mu\text{A}$	50	-	-	V
Forward Voltage	V_F	$I_F = 200\text{mA}$	-	-	1	V
Maximum Peak Reverse Current	I_R	$V_R = 50\text{V}$	-	-	0.1	μA
Total Capacitance	C_J	$V_R = 0, f = 1.0\text{MHz}$	-	-	4	pF
Reverse Recovery Time	t_{rr}	$I_F = I_R = 10\text{mA}$, $I_{rr} = 0.1 \times I_R, R_L = 100\Omega$	-	-	4	ns

Ratings and Characteristic Curves (@ $T_A = 25^\circ\text{C}$ unless otherwise specified)

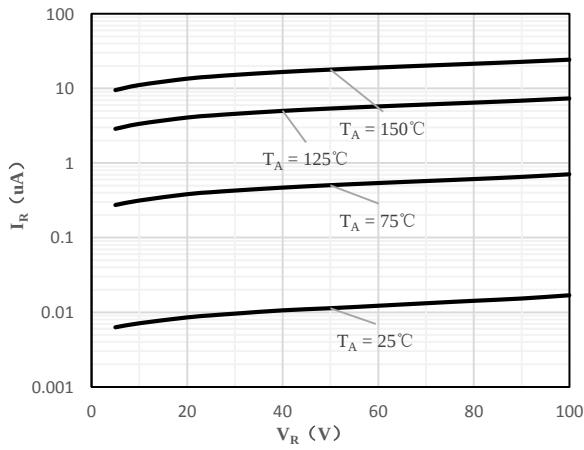


Fig 1 Typical Reverse Characteristic

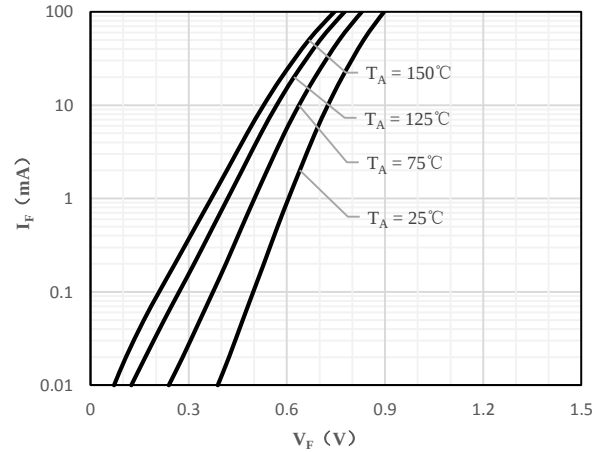


Fig 2 Typical Forward Characteristics

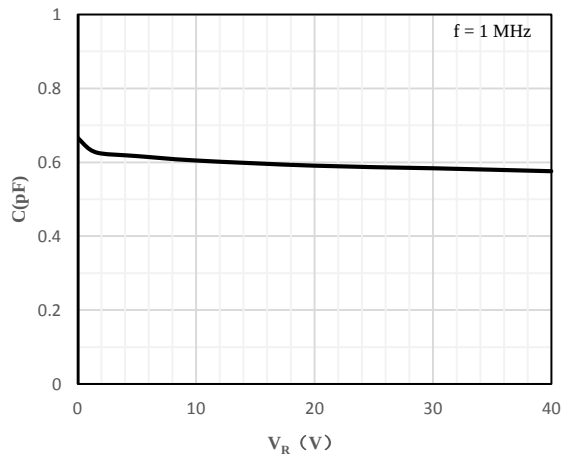
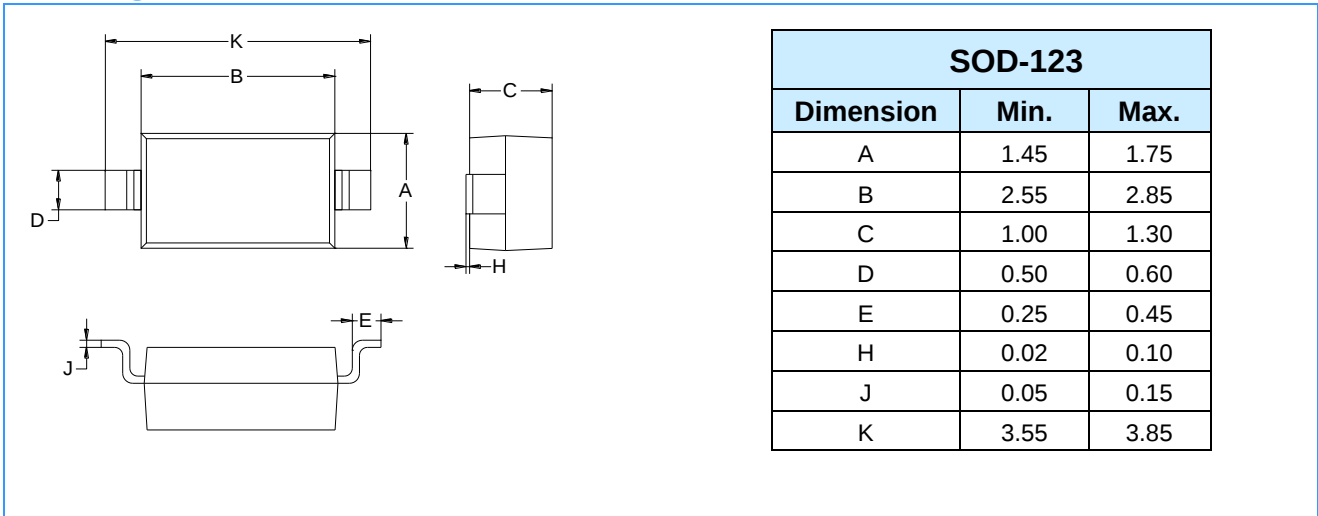


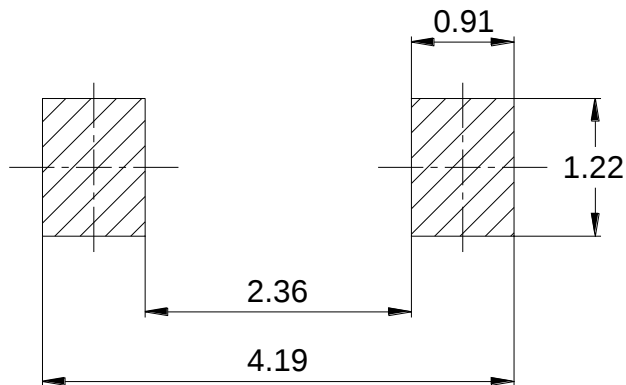
Fig 3 Capacitance vs. Reverse Voltage

Package Outline Dimensions (Unit: mm)



Package Outline Dimensions (Unit: mm)

SOD-123



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