



1N50K-TA

Preliminary

Power MOSFET

1.3A, 500V N-CHANNEL POWER MOSFET

DESCRIPTION

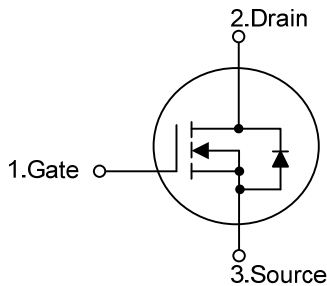
The UTC **1N50K-TA** is an N-channel mode power MOSFET using UTC's advanced technology to provide customers with planar stripe and DMOS technology. This technology allows a minimum on-state resistance and superior switching performance. It also can withstand high energy pulse in the avalanche and commutation mode.

The UTC **1N50K-TA** is generally applied in high efficiency switch mode power supplies, active power factor correction and electronic lamp ballasts based on half bridge topology.

FEATURES

- * $R_{DS(ON)} < 6.0\Omega$ @ $V_{GS}=10V, I_D=0.65A$
- * High Switching Speed
- * 100% Avalanche Tested

SYMBOL

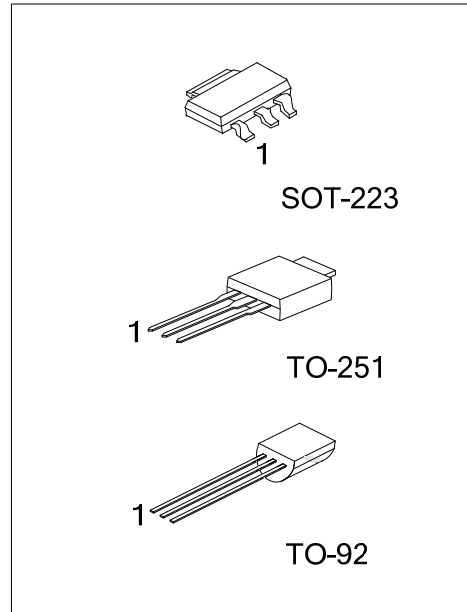


ORDERING INFORMATION

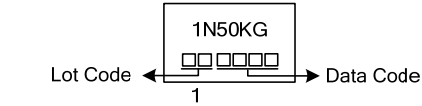
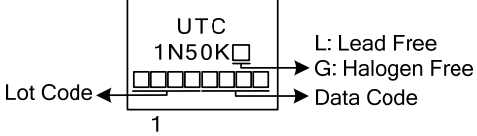
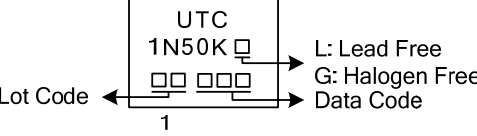
Ordering Number		Package	Pin Assignment			Packing
Lead Free	Halogen Free		1	2	3	
-	1N50KG-AA3-T	SOT-223	G	D	S	Tape Reel
1N50KL-TM3-T	1N50KG-TM3-T	TO-251	G	D	S	Tube
1N50KL-x-T92-B	1N50KG-x-T92-B	TO-92	G	D	S	Tape Box
1N50KL-x-T92-K	1N50KG-x-T92-K	TO-92	G	D	S	Bulk

Note: Pin Assignment: G: Gate D: Drain S: Source

<p>1N50KG-AA3-R</p> <p>(1) Packing Type (2) Package Type (3) Green Package</p>	<p>(1) T: Tube, R: Tape Reel, B: Tape Box, K: Bulk (2) AA3: SOT-223, TM3: TO-251, T92: TO-92 (3) L: Lead Free, G: Halogen Free and Lead Free</p>
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MARKING

PACKAGE	MARKING
SOT-223	
TO-251	
TO-92	

■ ABSOLUTE MAXIMUM RATINGS ($T_c=25^\circ\text{C}$, unless otherwise specified)

PARAMETER		SYMBOL	RATINGS	UNIT	
Drain-Source Voltage		V_{DSS}	500	V	
Gate-Source Voltage		V_{GSS}	± 30	V	
Drain Current	Continuous ($T_c=25^\circ\text{C}$)	I_D	1.3 (Note 2)	A	
	Pulsed (Note 3)	I_{DM}	5 (Note 2)	A	
Avalanche Current (Note 3)		I_{AR}	1.3	A	
Avalanche Energy	Single Pulsed (Note 4)	E_{AS}	35	mJ	
	Repetitive (Note 5)	E_{AR}	2.6	mJ	
Power Dissipation	SOT-223	P_D	1	W	
	TO-251		25	W	
	TO-92		1.56	W	
Derate above 25°C	SOT-223		125	$\text{W}/^\circ\text{C}$	
	TO-251		0.2	$\text{W}/^\circ\text{C}$	
	TO-92		0.0125	$\text{W}/^\circ\text{C}$	
Junction Temperature			T_J	+150	$^\circ\text{C}$
Storage Temperature			T_{STG}	-55~+150	$^\circ\text{C}$

Notes: 1. Absolute maximum ratings are those values beyond which the device could be permanently damaged.

Absolute maximum ratings are stress ratings only and functional device operation is not implied.

2. Drain current limited by maximum junction temperature
3. Repetitive Rating: Pulse width limited by maximum junction temperature
4. $L = 41\text{mH}$, $I_{AS} = 1.3\text{A}$, $V_{DD} = 50\text{V}$, $R_G = 27\Omega$, Starting $T_J = 25^\circ\text{C}$
5. $I_{SD} \leq 1.5\text{A}$, $di/dt \leq 200\text{A}/\mu\text{s}$, $V_{DD} \leq BV_{DSS}$, Starting $T_J = 25^\circ\text{C}$

■ THERMAL RESISTANCES CHARACTERISTICS

PARAMETER		SYMBOL	RATINGS	UNIT
Junction to Ambient	SOT-223	θ_{JA}	150	$^\circ\text{C}/\text{W}$
	TO-251		110	$^\circ\text{C}/\text{W}$
	TO-92		140	$^\circ\text{C}/\text{W}$
Junction to Case	SOT-223	θ_{JC}	125	$^\circ\text{C}/\text{W}$
	TO-251		5	$^\circ\text{C}/\text{W}$
	TO-92		80	$^\circ\text{C}/\text{W}$

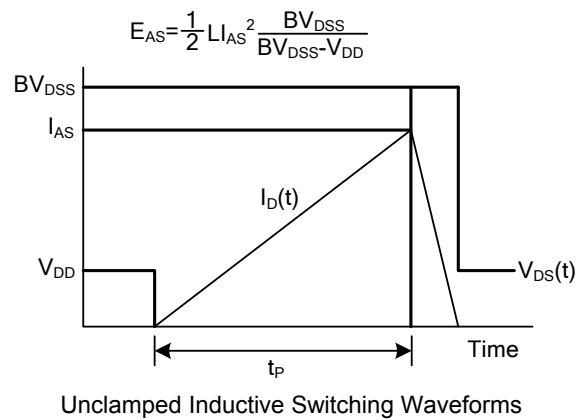
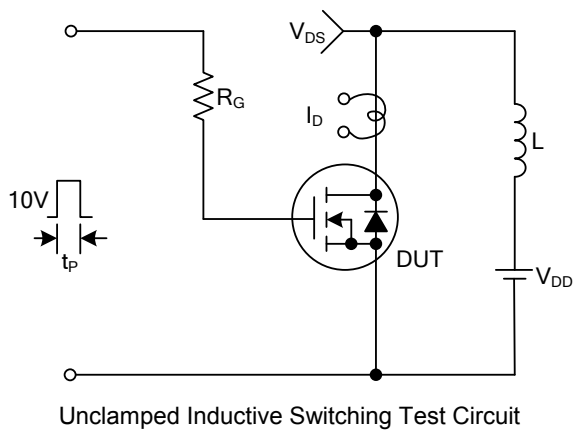
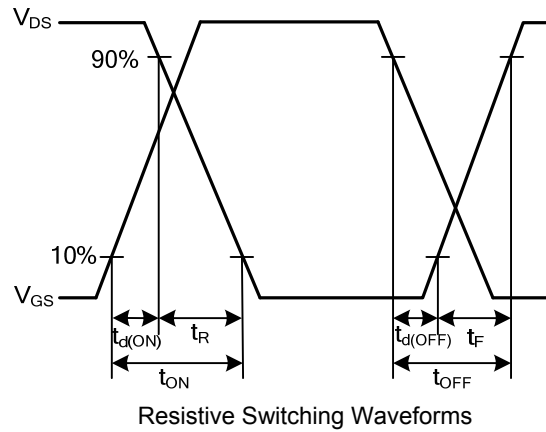
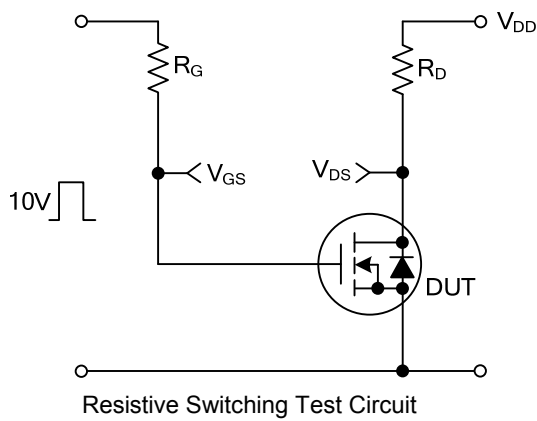
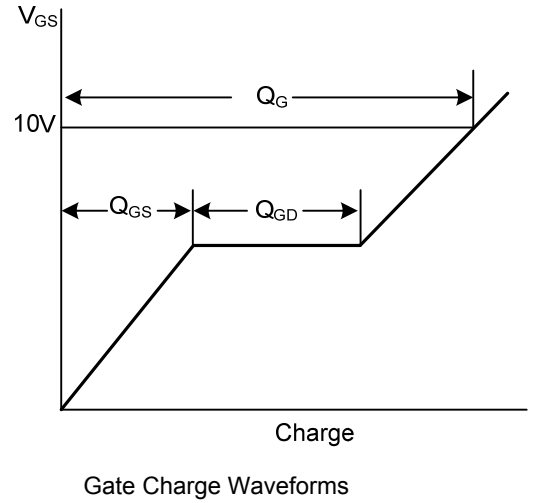
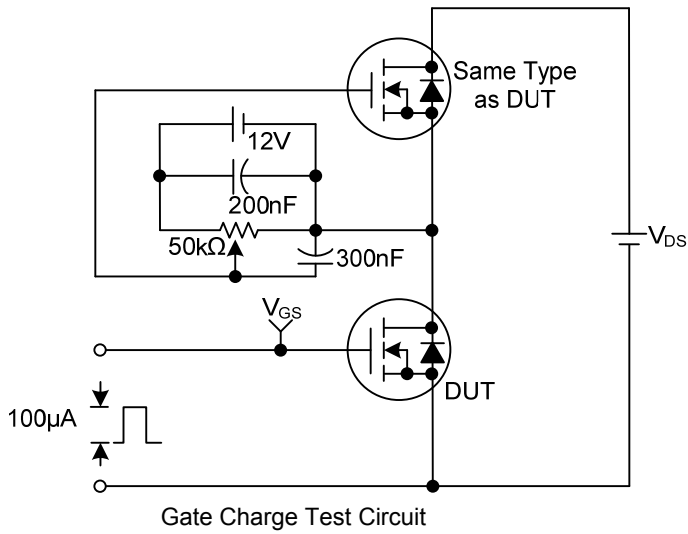
■ ELECTRICAL CHARACTERISTICS ($T_c=25^\circ\text{C}$, unless otherwise specified)

PARAMETER	SYMBOL	TEST CONDITIONS	MIN	TYP	MAX	UNIT
OFF CHARACTERISTICS						
Drain-Source Breakdown Voltage	BV_{DSS}	$I_D=250\mu\text{A}$, $V_{GS}=0\text{V}$	500			V
Drain-Source Leakage Current	I_{DSS}	$V_{DS}=500\text{V}$, $V_{GS}=0\text{V}$			1	μA
Gate- Source Leakage Current	Forward	$V_{GS}=+30\text{V}$, $V_{DS}=0\text{V}$			+100	nA
	Reverse	$V_{GS}=-30\text{V}$, $V_{DS}=0\text{V}$			-100	nA
ON CHARACTERISTICS						
Gate Threshold Voltage	$V_{GS(TH)}$	$V_{DS}=V_{GS}$, $I_D=250\mu\text{A}$	3.0		5.0	V
Static Drain-Source On-State Resistance	$R_{DS(ON)}$	$V_{GS}=10\text{V}$, $I_D=0.65\text{A}$		5	6	Ω
DYNAMIC PARAMETERS						
Input Capacitance	C_{ISS}	$V_{GS}=0\text{V}$, $V_{DS}=25\text{V}$, $f=1.0\text{MHz}$		180		pF
Output Capacitance	C_{OSS}			30		pF
Reverse Transfer Capacitance	C_{RSS}			12.5		pF
SWITCHING PARAMETERS						
Total Gate Charge	Q_G	$V_{GS}=10\text{V}$, $V_{DS}=50\text{V}$, $I_D=1.3\text{A}$ (Note 1, 2)		11	15	nC
Gate to Source Charge	Q_{GS}			2.5		nC
Gate to Drain Charge	Q_{GD}			1		nC
Turn-ON Delay Time	$t_{D(ON)}$	$V_{DD}=30\text{V}$, $I_D=0.5\text{A}$, $R_G=25\Omega$ (Note 1, 2)		37.5		ns
Rise Time	t_R			20		ns
Turn-OFF Delay Time	$t_{D(OFF)}$			53		ns
Fall-Time	t_F			12		ns
SOURCE- DRAIN DIODE RATINGS AND CHARACTERISTICS						
Maximum Body-Diode Continuous Current	I_S				1.3	A
Maximum Body-Diode Pulsed Current	I_{SM}				5.2	A
Drain-Source Diode Forward Voltage	V_{SD}	$I_S=1.3\text{A}$, $V_{GS}=0\text{V}$			1.2	V

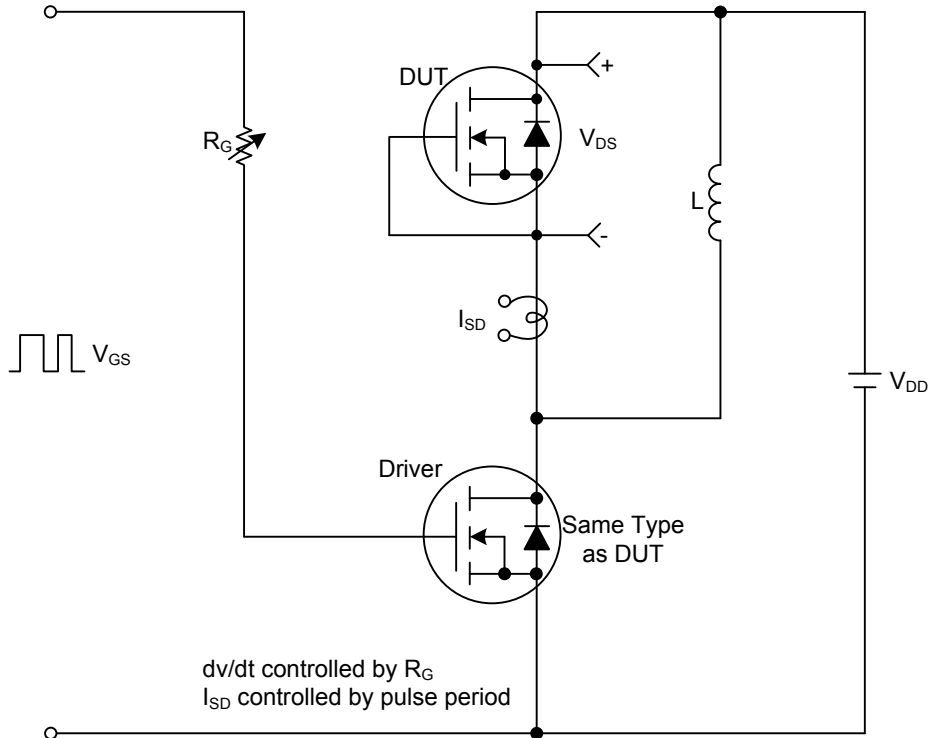
Notes: 1. Pulse Test: Pulse width $\leq 300\mu\text{s}$, Duty cycle $\leq 2\%$.

2. Essentially independent of operating temperature.

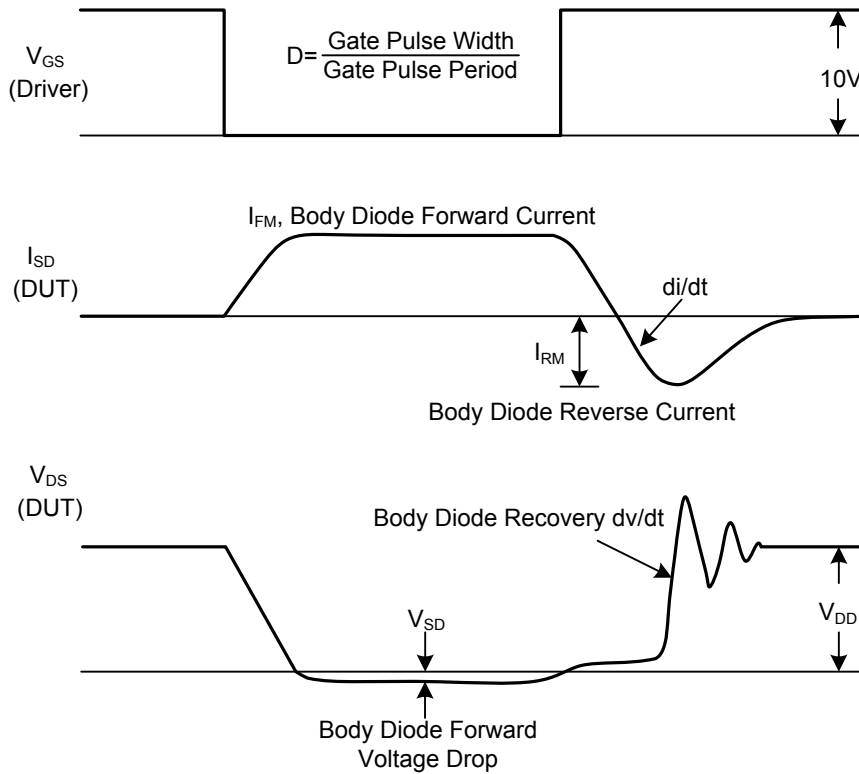
■ TEST CIRCUITS AND WAVEFORMS



■ TEST CIRCUITS AND WAVEFORMS(Cont.)



Peak Diode Recovery dv/dt Test Circuit & Waveforms



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