

1N6263W

SURFACE MOUNT SCHOTTKY BARRIER DIODE

Features:

- Low Forward Voltage Drop
- Guard Ring Construction for Transient Protection
- Fast Switching Time
- Low Reverse Capacitance
- Surface Mount Package Ideally Suited for Automatic Insertion

Mechanical Data:

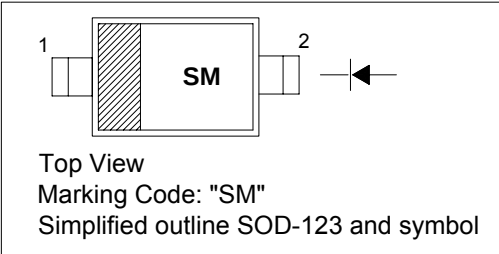
Case: SOD-123, Plastic

Terminals: Solderable per MIL-STD-202, Method 208

Polarity: Cathode Band

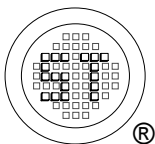
PINNING

PIN	DESCRIPTION
1	Cathode
2	Anode



Absolute Maximum Ratings ($T_a = 25^{\circ}\text{C}$)

	Symbol	Value	Unit
Peak Repetitive Reverse Voltage	V_{RRM}	60	V
Working Peak Reverse Voltage	V_{RWM}	60	V
DC Blocking Voltage	V_R	60	V
RMS Reverse Voltage	$V_{R(RMS)}$	42	V
Forward Continuous Current	I_F	15	mA
Power Dissipation	P_{tot}	333	mW
Non-Repetitive Peak Forward Surge Current @ $t \leq 1.0s$ @ $t = 10ms$	I_{FSM}	50 2.0	mA A
Thermal Resistance Junction to Ambient Air	$R_{\theta JA}$	300	$^{\circ}\text{C/W}$
Operating Temperature Range	T_j	-55 to +125	$^{\circ}\text{C}$
Storage Temperature Range	T_s	-55 to +150	$^{\circ}\text{C}$



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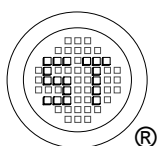


Dated : 12/08/2004

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Characteristics at $T_{amb} = 25^{\circ}\text{C}$

		Symbol	Min.	Typ.	Max.	Unit
Reverse Breakdown Voltage at $I_R = 10\mu\text{A}$		$V_{(BR)R}$	60	-	-	V
Forward Voltage Drop	at $I_F = 1\text{mA}$	V_{FM}	-	-	0.41	V
	at $I_F = 15\text{mA}$	V_{FM}	-	-	1.0	V
Reverse Leakage Current at $V_R = 50\text{V}$		I_{RM}	-	-	200	nA
Total Capacitance at $V_R = 0\text{V}$, $f = 1\text{MHz}$		C_{tot}	-	-	2.2	pF
Reverse Recovery Time at $I_F = I_R = 5\text{mA}$, $I_{rr} = 0.1 \times I_R$, $R_L = 100\Omega$		t_{rr}	-	-	1	ns



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ISO/TS 16949 : 2002
Certificate No. 05103



ISO 14001:2004
Certificate No. 7116



ISO 9001:2000
Certificate No. 0506098

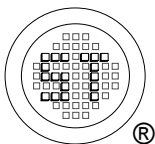
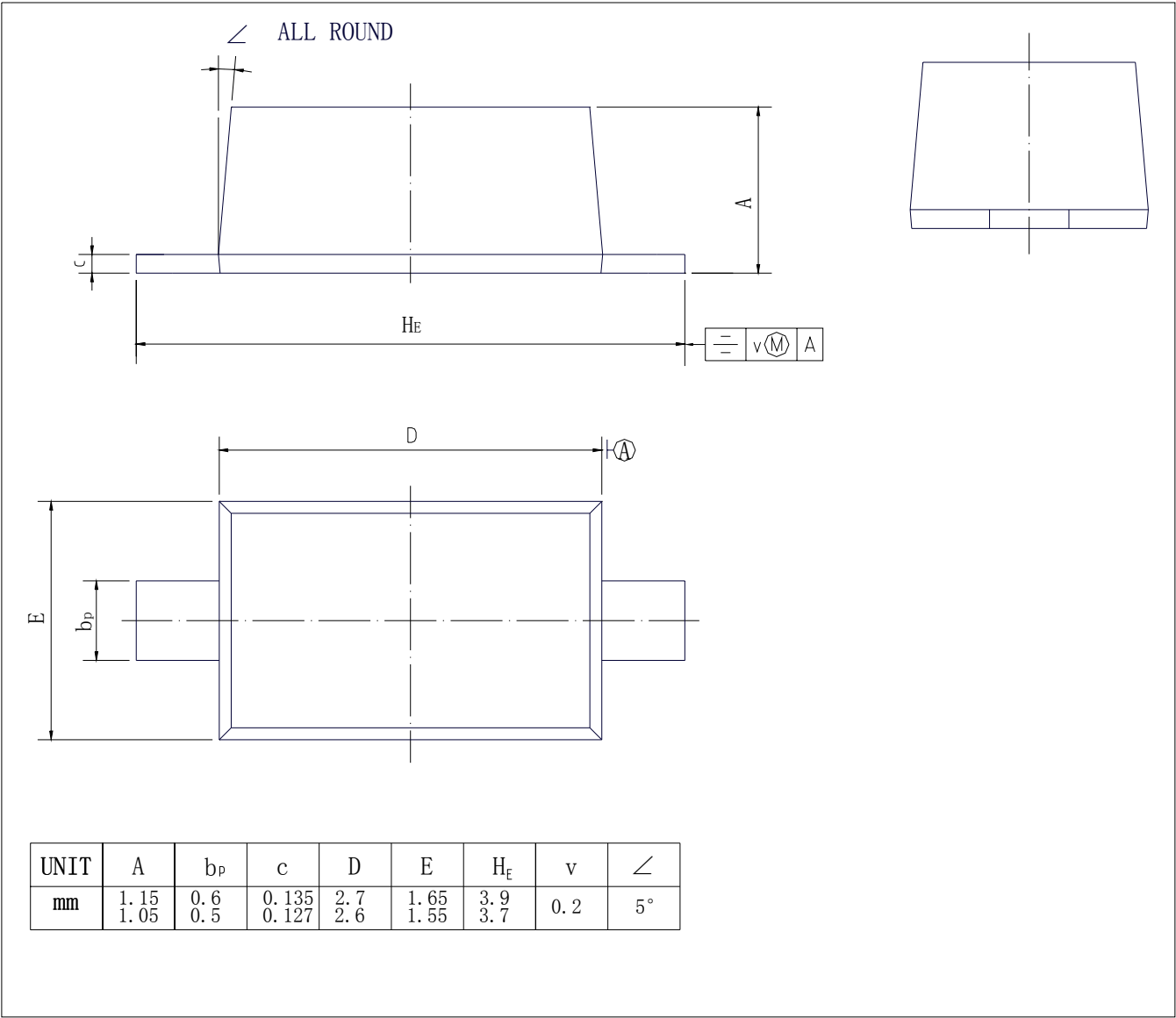
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PACKAGE OUTLINE

Plastic surface mounted package; 2 leads

SOD-123



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