



1N70-LC1

Power MOSFET

1.0A, 700V N-CHANNEL POWER MOSFET

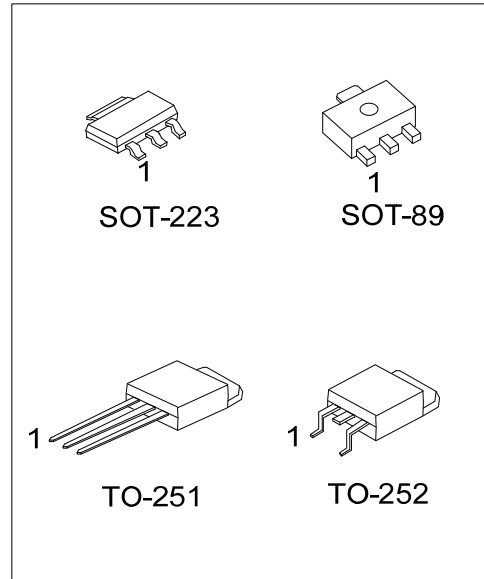
DESCRIPTION

The UTC **1N70-LC1** is an N-channel power MOSFET using UTC's advanced technology to provide customers with a minimum on-state resistance and superior switching performance.

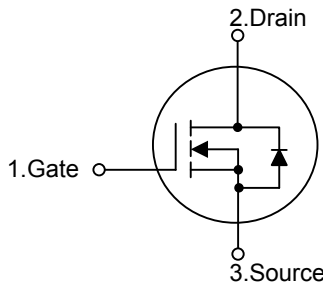
The UTC **1N70-LC1** is generally applied in low power switching mode power appliances and electronic ballast.

FEATURES

- * $R_{DS(ON)} \leq 15 \Omega @ V_{GS}=10V, I_D=0.5A$
- * High Switching Speed
- * 100% Avalanche Tested



SYMBOL



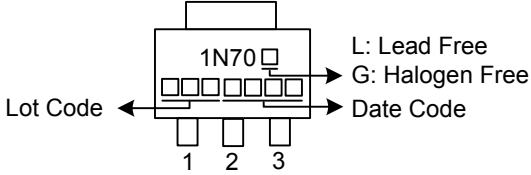
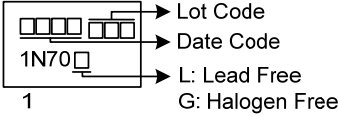
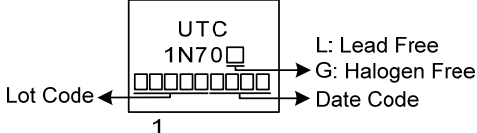
ORDERING INFORMATION

Ordering Number		Package	Pin Assignment			Packing
Lead Free	Halogen Free		1	2	3	
1N70L-AA3-R	1N70G-AA3-R	SOT-223	G	D	S	Tape Reel
1N70L-AB3-R	1N70G-AB3-R	SOT-89	G	D	S	Tape Reel
1N70L-TM3-T	1N70G-TM3-T	TO-251	G	D	S	Tube
1N70L-TN3-R	1N70G-TN3-R	TO-252	G	D	S	Tape Reel

Note: Pin Assignment: G: Gate D: Drain S: Source

<p>1N70G-AA3-R</p> <p>(1) Packing Type</p> <p>(2) Package Type</p> <p>(3) Green Package</p>	<p>(1) R: Tape Reel, T: Tube</p> <p>(2) AA3: SOT-223, AB3: SOT-89, TM3: TO-251, TN3: TO-252</p> <p>(3) G: Halogen Free and Lead Free, L: Lead Free</p>
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■ MARKING

SOT-223	SOT-89
 <p>Diagram of SOT-223 marking: The package is a small rectangle with three leads at the bottom labeled 1, 2, and 3. The top surface has the part number '1N70' and a small square symbol. Below the part number is a row of eight small squares representing the Lot Code. To the right of the Lot Code is another row of eight small squares representing the Date Code. To the right of the Date Code are the labels 'L: Lead Free' and 'G: Halogen Free'.</p>	 <p>Diagram of SOT-89 marking: The package is a small rectangle with one lead at the bottom labeled 1. The top surface has the part number '1N70' and a small square symbol. Above the part number is a row of four small squares representing the Lot Code, and below it is a row of four small squares representing the Date Code. To the right of the Date Code are the labels 'L: Lead Free' and 'G: Halogen Free'.</p>
TO-251 / TO-252	-
 <p>Diagram of TO-251 / TO-252 marking: The package is a square with one lead at the bottom labeled 1. The top surface has the manufacturer code 'UTC' and the part number '1N70' with a small square symbol. Below the part number is a row of eight small squares representing the Lot Code. To the right of the Lot Code is another row of eight small squares representing the Date Code. To the right of the Date Code are the labels 'L: Lead Free' and 'G: Halogen Free'.</p>	-

■ ABSOLUTE MAXIMUM RATINGS ($T_c=25^\circ\text{C}$, unless otherwise specified)

PARAMETER		SYMBOL	RATINGS	UNIT
Drain-Source Voltage		V_{DSS}	700	V
Gate-Source Voltage		V_{GSS}	± 30	V
Drain Current	Continuous	I_D	1	A
	Pulsed (Note 2)	I_{DM}	2	A
Avalanche Energy	Single Pulsed (Note 3)	E_{AS}	22	mJ
Peak Diode Recovery dv/dt (Note 4)		dv/dt	4.4	V/ns
Power Dissipation	SOT-223	P_D	1.8	W
	SOT-89		3.5	W
	TO-251/TO-252		39	W
Junction Temperature		T_J	+150	$^\circ\text{C}$
Storage Temperature		T_{STG}	-55 ~ +150	$^\circ\text{C}$

Notes: 1. Absolute maximum ratings are those values beyond which the device could be permanently damaged.

Absolute maximum ratings are stress ratings only and functional device operation is not implied.

2. Repetitive Rating: Pulse width limited by maximum junction temperature.

3. $L = 30\text{mH}$, $I_{AS} = 1.2\text{A}$, $V_{DD} = 50\text{V}$, $R_G = 25\Omega$, Starting $T_J = 25^\circ\text{C}$

4. $I_{SD} \leq 1.0\text{A}$, $di/dt \leq 200\text{A}/\mu\text{s}$, $V_{DD} \leq BV_{DSS}$, Starting $T_J = 25^\circ\text{C}$

■ THERMAL DATA

PARAMETER		SYMBOL	RATINGS	UNIT
Junction to Ambient	SOT-223	θ_{JA}	150	$^\circ\text{C}/\text{W}$
	SOT-89		200	$^\circ\text{C}/\text{W}$
	TO-251/TO-252		110	$^\circ\text{C}/\text{W}$
Junction to Case	SOT-223	θ_{JC}	69.4	$^\circ\text{C}/\text{W}$
	SOT-89		35.7	$^\circ\text{C}/\text{W}$
	TO-251/TO-252		3.2	$^\circ\text{C}/\text{W}$

Note: Device mounted on FR-4 substrate PC board, 2oz copper, with 1inch square copper plate.

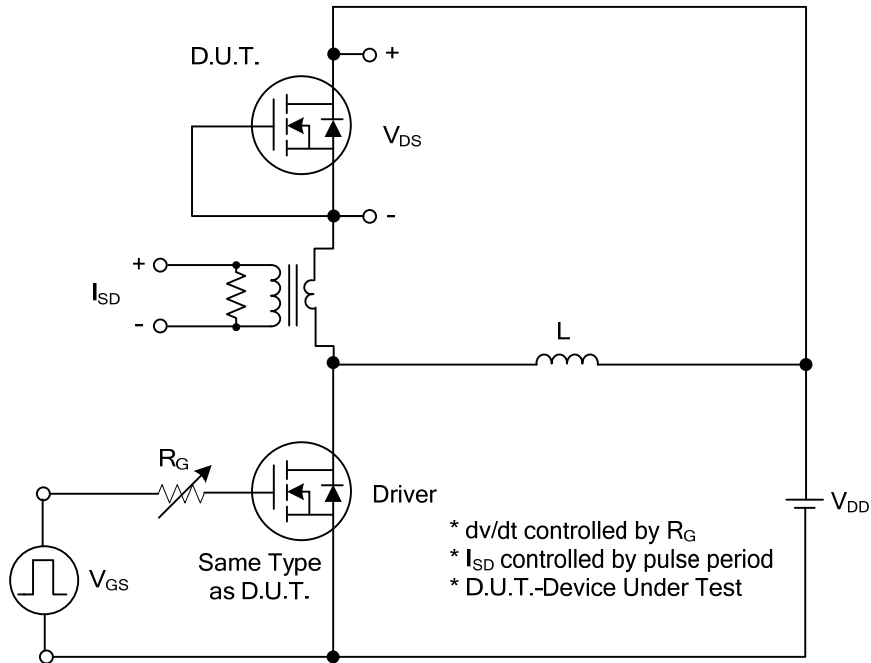
■ ELECTRICAL CHARACTERISTICS ($T_J=25^\circ\text{C}$, unless otherwise specified)

PARAMETER	SYMBOL	TEST CONDITIONS	MIN	TYP	MAX	UNIT
OFF CHARACTERISTICS						
Drain-Source Breakdown Voltage	BV_{DSS}	$I_D=250\mu\text{A}$, $V_{GS}=0\text{V}$	700			V
Drain-Source Leakage Current	I_{DSS}	$V_{DS}=700\text{V}$, $V_{GS}=0\text{V}$			10	μA
Gate- Source Leakage Current	Forward	$V_{GS}=+30\text{V}$, $V_{DS}=0\text{V}$			+100	nA
	Reverse		$V_{GS}=-30\text{V}$, $V_{DS}=0\text{V}$			-100
ON CHARACTERISTICS						
Gate Threshold Voltage	$V_{GS(TH)}$	$V_{DS}=V_{GS}$, $I_D=250\mu\text{A}$	2.0		4.0	V
Static Drain-Source On-State Resistance	$R_{DS(ON)}$	$V_{GS}=10\text{V}$, $I_D=0.5\text{A}$			15	Ω
DYNAMIC PARAMETERS						
Input Capacitance	C_{ISS}	$V_{GS}=0\text{V}$, $V_{DS}=25\text{V}$, $f=1.0\text{MHz}$		127		pF
Output Capacitance	C_{OSS}			18		pF
Reverse Transfer Capacitance	C_{RSS}			2.4		pF
SWITCHING PARAMETERS						
Total Gate Charge (Note 1)	Q_G	$V_{DS}=560\text{V}$, $V_{GS}=10\text{V}$, $I_D=1.0\text{A}$ $I_G=1\text{mA}$ (Note 1, 2)		8.6		nC
Gate to Source Charge	Q_{GS}			3.7		nC
Gate to Drain Charge	Q_{GD}			1.6		nC
Turn-ON Delay Time (Note 1)	$t_{D(ON)}$	$V_{DD}=100\text{V}$, $V_{GS}=10\text{V}$, $I_D=1.0\text{A}$, $R_G=25\Omega$ (Note 1, 2)		4		ns
Rise Time	t_R			15		ns
Turn-OFF Delay Time	$t_{D(OFF)}$			14		ns
Fall-Time	t_F			39		ns
SOURCE- DRAIN DIODE RATINGS AND CHARACTERISTICS						
Maximum Body-Diode Continuous Current	I_S				1	A
Maximum Body-Diode Pulsed Current (Note 1)	I_{SM}				2	A
Drain-Source Diode Forward Voltage (Note 1)	V_{SD}	$I_S=1.0\text{A}$, $V_{GS}=0\text{V}$			1.4	V
Body Diode Reverse Recovery Time	t_{rr}	$I_S=1.0\text{A}$, $V_{GS}=0\text{V}$,		190		ns
Body Diode Reverse Recovery Charge	Q_{rr}		$dI_F/dt=100\text{A}/\mu\text{s}$		1.3	

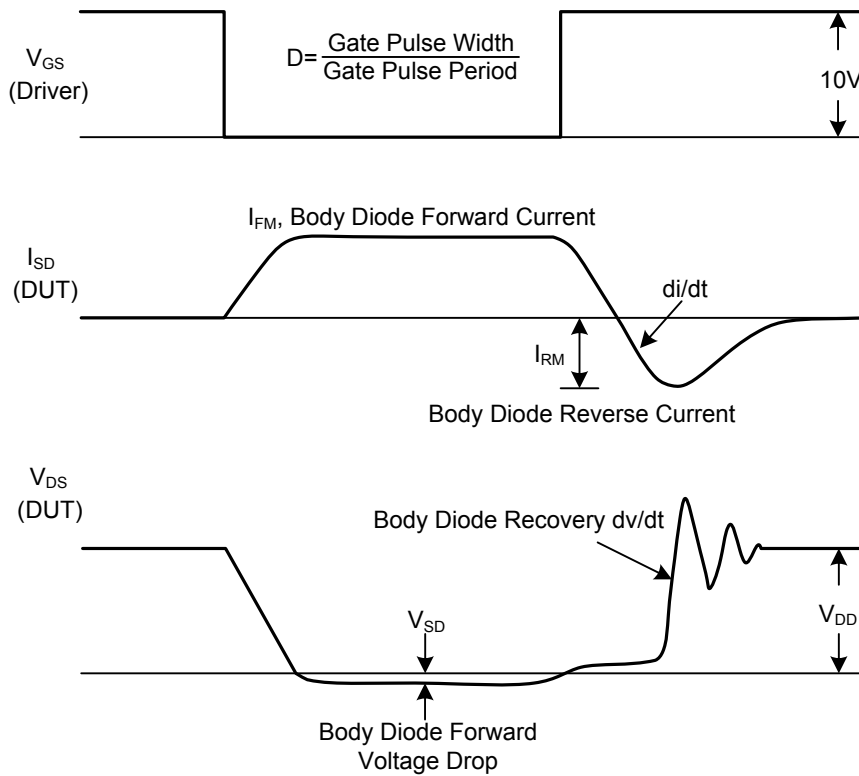
Notes: 1. Pulse Test: Pulse width $\leq 300\mu\text{s}$, Duty cycle $\leq 2\%$.

2. Essentially independent of operating temperature.

TEST CIRCUITS AND WAVEFORMS

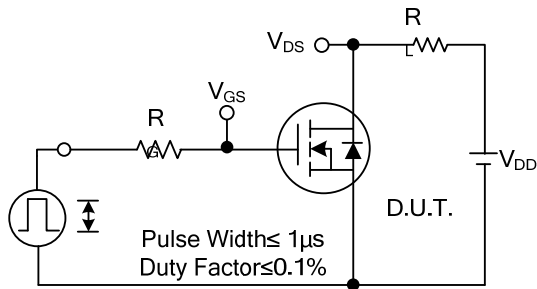


Peak Diode Recovery dv/dt Test Circuit

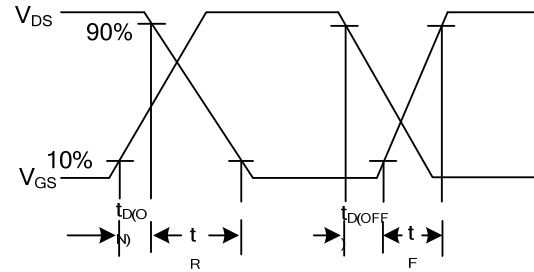


Peak Diode Recovery dv/dt Waveforms

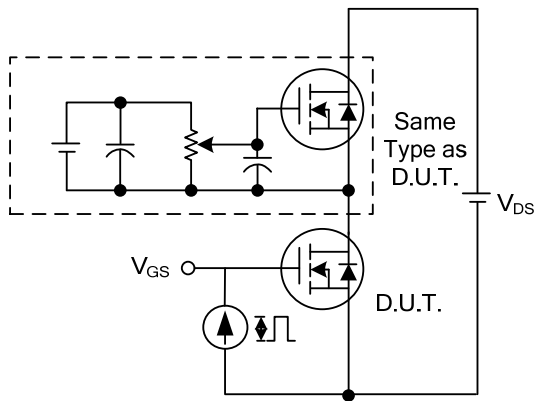
TEST CIRCUITS AND WAVEFORMS



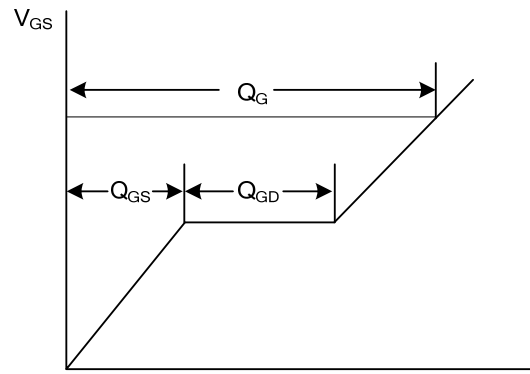
Switching Test Circuit



Switching Waveforms

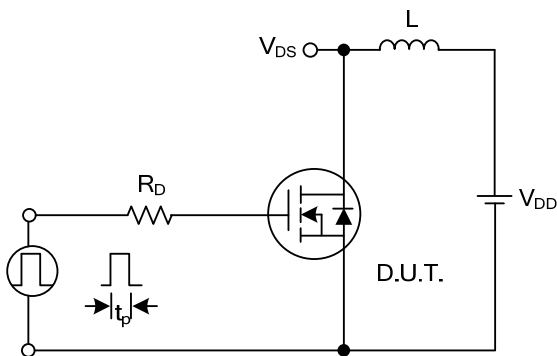


Gate Charge Test Circuit

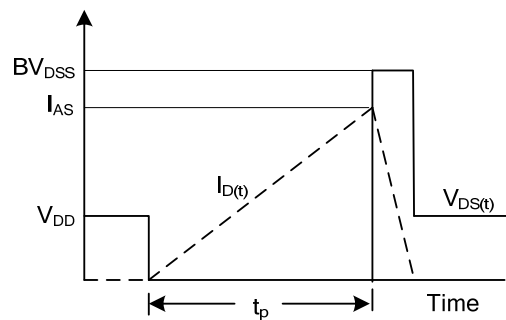


Charge

Gate Charge Waveform

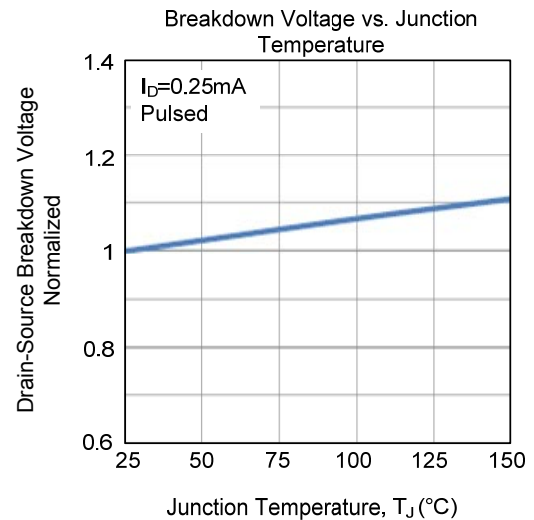
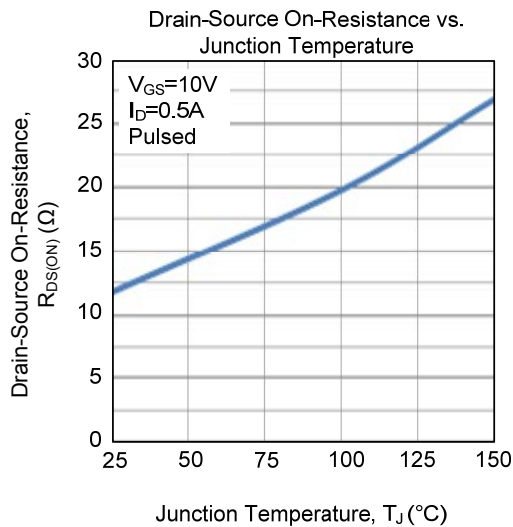
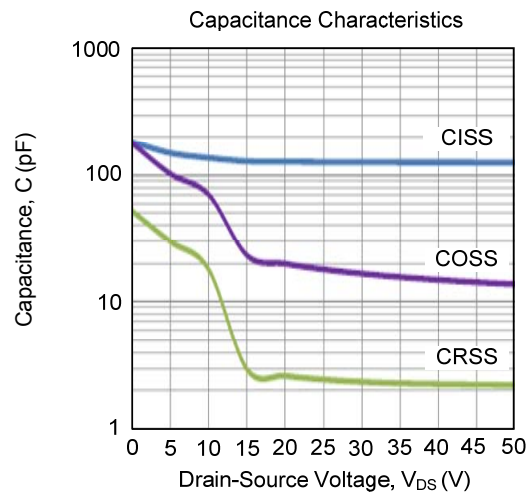
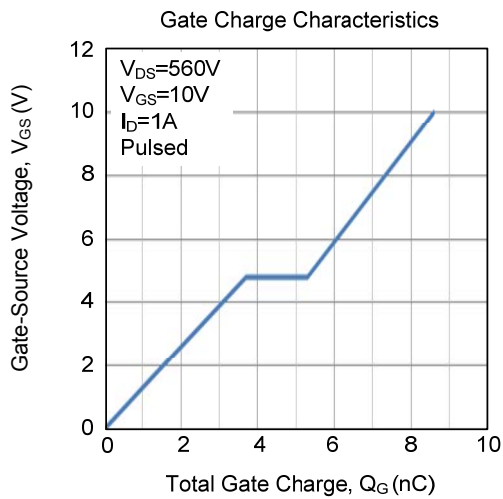
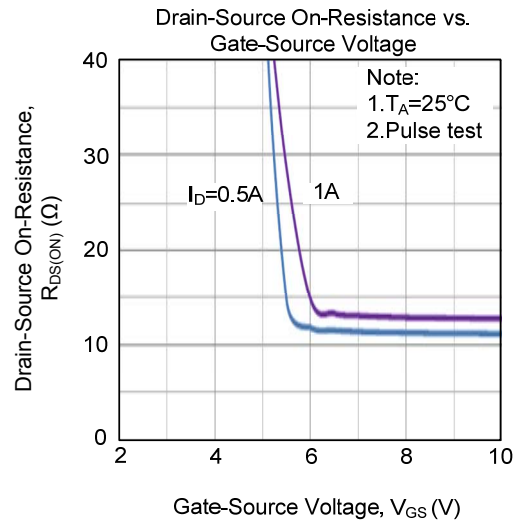
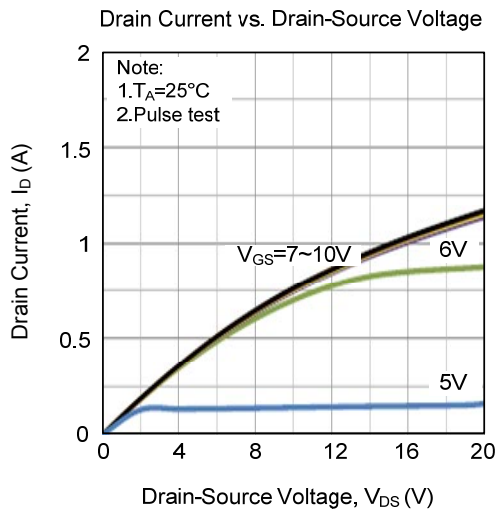


Unclamped Inductive Switching Test Circuit

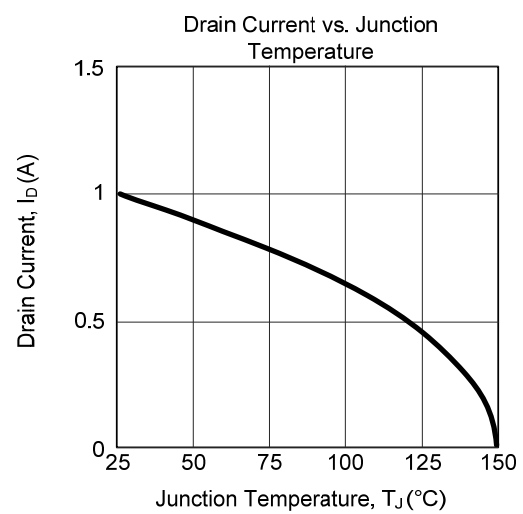
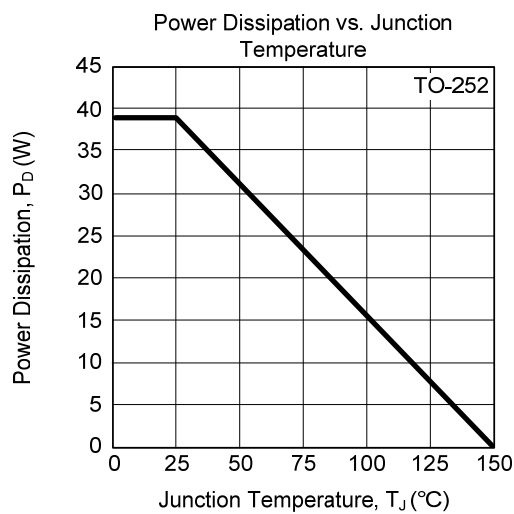
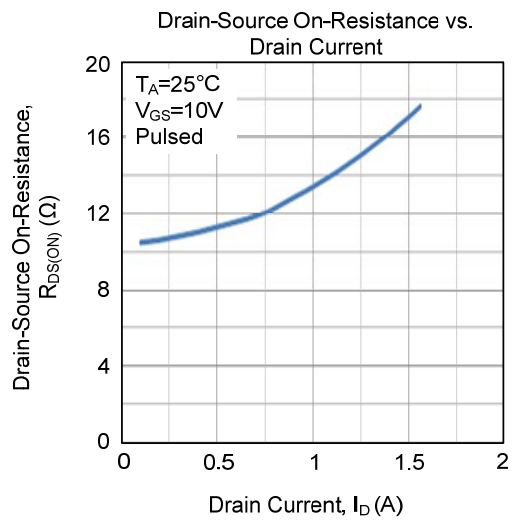
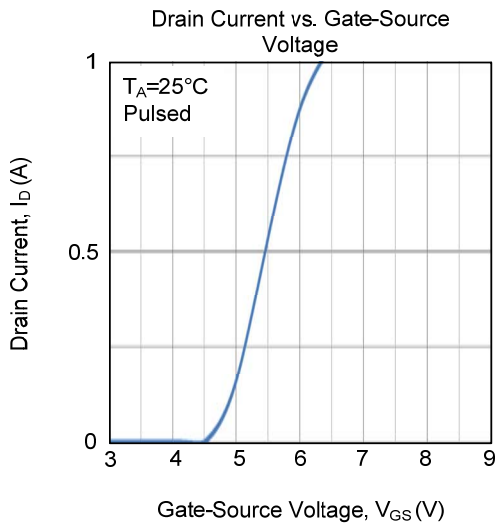
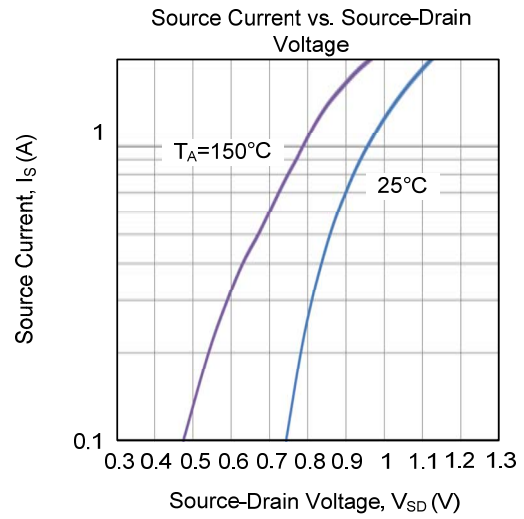
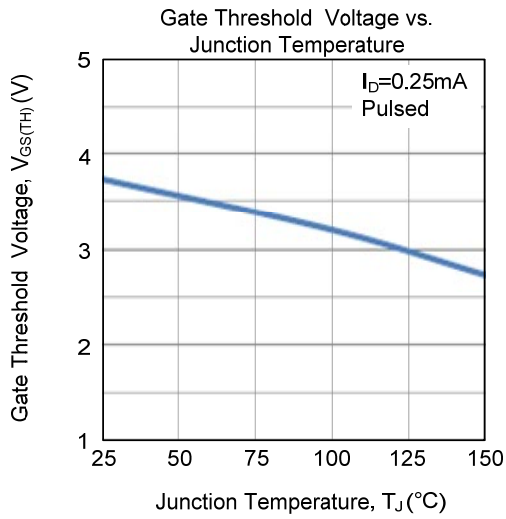


Unclamped Inductive Switching Waveforms

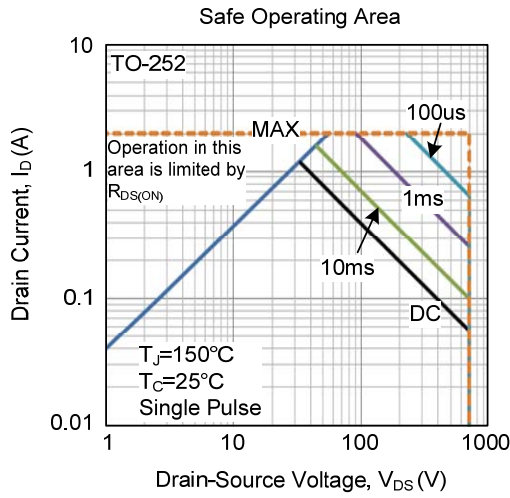
TYPICAL CHARACTERISTICS



TYPICAL CHARACTERISTICS (Cont.)



■ TYPICAL CHARACTERISTICS (Cont.)



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