

# 201U1LW28

- The device listed in these specification sheets was designed and manufactured for use in OA equipment.
- ◎In case of using the device for applications such as control and safety equipment for transportation(aircraft, trains, automobiles, etc.), rescue and security equipment and various safety related equipment which require higher reliability and safety, take into consideration that appropriate measures such as fail-safe functions and redundant system design should be taken.
- ◎In case of using the device for equipment that requires an extreme level of reliability, such as aerospace applications, telecommunication equipment(trunk lines), nuclear power control equipment and medical or other equipment for life support, contact and consult with a SHARP sales representative.
- ©SHARP assumes no responsibility for any damage resulting from the use of the device which does not comply with the instructions and the precautions specified in these specification sheets.
- ©Contact and consult with a SHARP sales representative for any questions about this device.

#### 2. Overview

This module is a color active matrix LCD module incorporating amorphous silicon TFT ( $\underline{\text{Thin }}\underline{\text{Film }}\underline{\text{T}}$ ransistor). It is composed of a color TFT-LCD panel, driver ICs, control circuit, power supply circuit and a back light unit. Graphics and texts can be displayed on a  $1600\times3\times1200$  dots panel with about 16 million colors by supplying 48 bit data signals( $8bit\times2pixel\times RGB$ ), two display enable signals, two dot clock signals, +12V DC supply voltages for TFT-LCD panel driving and supply voltage for back light.

It is a wide viewing-angle-module (Vertical viewing angle: $176^{\circ}$  Horizontal viewing angle: $176^{\circ}$  ,CR $\geq 10$ ). This module performance achieve 20ms response time (full; black to white, or white to black) done by improving Liquid crystal material.

## 3. Mechanical Specifications

Parameter	Specifications	Unit
Display size	51 (Diagonal)	cm
	20.1 (Diagonal)	Inch
Active area	408.0 (H)×306.0 (V)	mm
Pixel format	1600 (H)×1200 (V)	Pixel
	(1  pixel = R + G + B  dots)	
Pixel pitch	0.255(H)×0.255 (V)	mm
Pixel configuration	R,G,B vertical stripe	
Display mode	Normally black	
Unit outline dimensions	$432(W) \times 331.5 (H) \times 25 (D)$	mm
Mass	3.2 (Typ)	kg
Surface treatment	Anti-glare And hard-coating 3H	

Outline dimensions are shown in Fig.1.



# 4. Input Terminals and Function

#### 4-1. TFT-LCD panel driving

LVDS interface with 2 input signal, and +12VDC power supply, control signal

Connectors : (MDF76LARW-30S-1H(HIROSE))

Corresponding connectors : FI-X30M(JAE) LVDS receiver : Contained in a control IC

 $Corresponding\ LVDS\ transmitter\ \ \vdots\ THC63LVDM83R(Thine)\ or\ compatible$ 

Pin No.	Symbol	Function	Remark
1	Vcc	+12V power supply	
2	Vcc	+12V power supply	
3	Vcc	+12V power supply	
4	Vcc	+12V power supply	
5	Vss	Gnd	
6	Vss	Gnd	
7	RBIN3+	Positive (+) LVDS differential data input (B port)	LVDS
8	RBIN3-	Negative (-) LVDS differential data input (B port)	LVDS
9	CKBIN+	Positive (+) LVDS differential clock input (B port)	LVDS
10	CKBIN-	Negative (-) LVDS differential clock input (B port)	LVDS
11	RBIN2+	Positive (+) LVDS differential data input (B port)	LVDS
12	RBIN2-	Negative (-) LVDS differential data input (B port)	LVDS
13	RBIN1+	Positive (+) LVDS differential data input (B port)	LVDS
14	RBIN1-	Negative (-) LVDS differential data input (B port)	LVDS
15	RBIN0+	Positive (+) LVDS differential data input (B port)	LVDS
16	RBIN0-	Negative (-) LVDS differential data input (B port)	LVDS
17	Vss	Gnd	
18	Vss	Gnd	
19	RAIN3+	Positive (+) LVDS differential data input (A port)	LVDS
20	RAIN3-	Negative (-) LVDS differential data input (A port)	LVDS
21	CKAIN+	Positive (+) LVDS differential clock input (A port)	LVDS
22	CKAIN-	Negative (-) LVDS differential clock input (A port)	LVDS
23	RAIN2+	Positive (+) LVDS differential data input (A port)	LVDS
24	RAIN2-	Negative (-) LVDS differential data input (A port)	LVDS
25	RAIN1+	Positive (+) LVDS differential data input (A port)	LVDS
26	RAIN1-	Negative (-) LVDS differential data input (A port)	LVDS
27	RAIN0+	Positive (+) LVDS differential data input (A port)	LVDS
28	RAIN0-	Negative (-) LVDS differential data input (A port)	LVDS
29	Vss	Gnd	
30	Vss	Gnd	

[Note1] This module has dual pixel port to receive dual pixel data at the same time. A port receives first pixel data and B port receives second pixel data in dual pixel data.

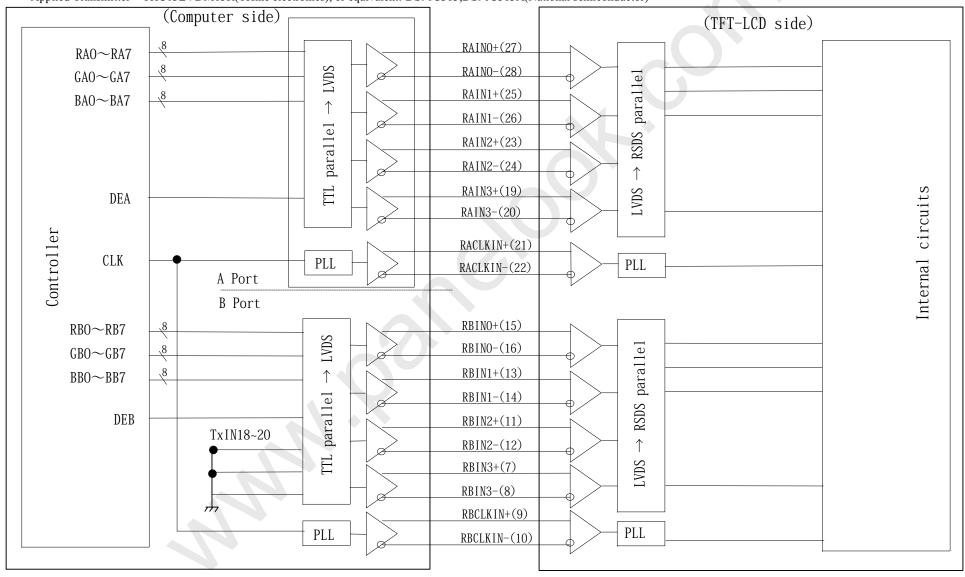
[Note2] Thine:THC63LVDM83R

Trans	smitter	Data arrangement					
Pin No	Data						
51	TA0	R0(LSB)					
52	TA1	R1					
54	TA2	R2					
55	TA3	R3					
56	TA4	R4					
3	TA5	R5					
4	TA6	G0(LSB)					
6	TB0	G1					
7	TB1	G2					
11	TB2	G3					
12	TB3	G4					
14	TB4	G5					
15	TB5	B0(LSB)					
19	TB6	B1					
20	TC0	B2					
22	TC1	В3					
23	TC2	B4					
24	TC3	B5					
27	TC4	(NA)					
28	TC5	(RSV1)					
30	TC6	DE					
50	TD0	R6					
2	TD1	R7(MSB)					
8	TD2	G6					
10	TD3	G7(MSB)					
16	TD4	В6					
18	TD5	B7(MSB)					
25	TD6	(NA)					

#### 4-2 Interface block diagram

LVDS receiver: Contained in a control IC.

Applied Transmitter: THC63LVDM83R(THine electronics), or equivalent: DS90C383,DS90C383A(National semiconductor)





## 4-2. Back light driving

CN1, 3 (High voltage side)

The module-side connector : XHP-7 (JST)
The user-side connector : S7B-XH-A (JST)

Cable : UL Style No. : 10267

rated voltage : 2kV (AC)

AWG : 26

		11.00	
Pin no.	symbol	Function	Cable color
1	$V_{H-1}$	Power supply for lamp 1 (High voltage side)	Pink
2	NC	This is electrically opened.	
3	NC	This is electrically opened.	
4	$V_{H-2}$	Power supply for lamp 2 (High voltage side)	Blue
5	NC	This is electrically opened.	
6	NC	This is electrically opened.	
7	V <sub>H-3</sub>	Power supply for lamp 3 (High voltage side)	Orange

## CN 2,4(Low voltage side)

The module-side connector : BHR - 03VS - 1 (JST)

The user-side connector : SM03(4.0)B-BHS-1-TB (JST)

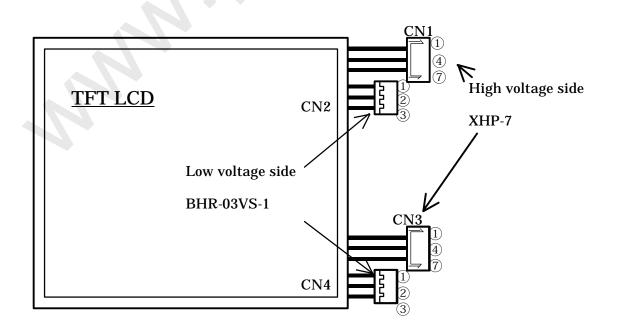
Cable : UL Style No. : 10368

rated voltage : 300V (AC)

AWG : 26

Pin no.	symbol	Function	Cable color
1	$V_{L-1}$	Power supply for lamp 1 (Low voltage side)	White
2	V <sub>L-2</sub>	Power supply for lamp 2 (Low voltage side)	Gray
3	V <sub>L-3</sub>	Power supply for lamp 3 (Low voltage side)	Brown

The pair of CN1 and CN2 is for the same CCFT lamps. The pair of CN3 and CN4 is in the same way.





## 5. Absolute Maximum Ratings

Parameter	Symbol	Condition	Ratings	Unit	Remark
+12.0V supply voltage	Vcc	Ta=25℃	$0 \sim +14.0$	V	
Storage temperature	Tstg	_	$-25 \sim +60$	$^{\circ}$	[Note1]
Operating temperature (Ambient)	Тора	_	$0 \sim +50$	$^{\circ}$ C	

[Note1] Humidity: 95%RH Max. ( $Ta \le 40^{\circ}C$ )

Maximum wet-bulb temperature at  $39^{\circ}$ C or less. (Ta> $40^{\circ}$ C)

No condensation.

#### 6. Electrical Characteristics

#### 6-1. TFT-LCD panel driving

. TFT-LO	TFT-LCD panel driving $Ta=25^{\circ}C$									
	Parameter	Symbol	Min.	Тур.	Max.	Unit	Remark			
Vcc	Supply voltage	Vcc	+10.8	+12.0	+13.2	V	[Note1]			
•	Current dissipation	Icc	_	420	600	mA	[Note2]			
·	Rush Current				3	A	[Note3]			
Permi	ssive input ripple voltage	$V_{RF}$	_	_	100	mVp-p				
		•	•							

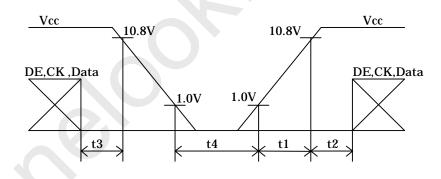
# [Note1]

1) On-off sequences of Vcc and data

$$0 \le t1 \le 60 ms$$

$$0 \le t2 \le 10 \text{ms}$$

t4≧100ms

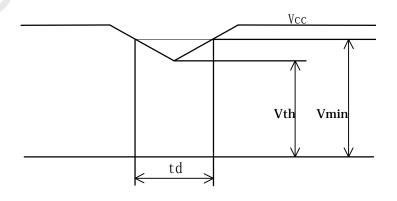


2) Dip conditions for supply voltage

Vmin,Vth=10.8V, 9.6V

- 1) Vth  $\leq$ Vcc< Vmin td≦ 20ms
- 2) Vcc < Vth

The LCD module shuts down.



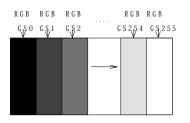
[Note2] Typical current situation: 256-gray-bar pattern

$$Vcc=+12.0V$$

The explanation of each gray scale, GS,

is described below section 8.

[Note3] The duration of rush current is about 1ms.



#### 6-2. Back light driving

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The back light system is an edge-lighting type with 6 CCFTs (Cold Cathode Fluorescent Tube).

The characteristics of the lamp are shown in the following table.

The value mentioned below is at the case of one CCFT.

CCFT Model Name: KTBE222MSTF-421MA77-Z(STANLEY.ELECTRIC.CO.,LTD)

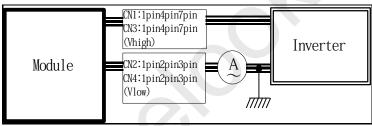
Parameter	Symbol	Min.	Typ.	Max.	Unit	Remark
Lamp current range	$I_{\rm L}$	3	6	7	mArms	[Note1]
Lamp voltage	$V_{L}$		800	900	Vrms	$Ta=25^{\circ}C$ $I_L=6.0$ m $A_{rms}$ $F_L=60$ kHz
Lamp power consumption	$P_{L}$		4.8	5.4	W	[Note2] $I_L$ =6.0m $A_{rms}F_L$ =60kHz
Lamp frequency	FL	35	60	70	KHz	[Note3]
Kick-off voltage	Vs			1800	Vrms	Ta=25°C【Note4】
				2000	Vrms	Ta=0°C [Note4]
Lamp life time	$T_{\rm L}$	50,000			Hour	[Note5]

A lamp can be light in the range of lamp current shown above.

Maximum rating for current is measured by high frequency current measurement equipment connected to V<sub>LOW</sub> at circuit showed below.

(Note: To keep enough kick-off voltage and necessary steady voltage for CCFT.)

Lamp frequency: 35~70kHz Ambient temperature :  $0\sim50^{\circ}$ C



[Note2] Referential data per one CCFT by calculation ( $I_L \times V_L$ ). The data doesn't include loss at inverter.

- [Note3] Lamp frequency of inverter may produce interference with horizontal synchronous frequency, and this may cause horizontal beat on the display. Therefore, adjust lamp frequency, and keep inverter as far as from module or use electronic shielding between inverter and module to avoid interference.
- [Note4] It is defined at 27pF for the ballast capacitor of a DC-AC inveter.

The Kick-off voltage may rise up in the user set, please decide the open output voltage by checking not to occur lighting failure under operating state.

The open output voltage should be applied to the lamp for more than 1 second to startup. Or when the ambient luminance around the lamp is more than 1lux, it should be applied to the lamp for more than 100ms.Otherwise the lamp may not be turned on..

- [Note5] Lamp life time is defined as the time when either ① or ② occurs in the continuous operation under the condition of Ta=25°C and IL=6.0 mArms.
  - ① Brightness becomes 50% of the original value under standard condition.
  - ② Kick-off voltage at Ta=0°C exceeds maximum value,2000Vrms.

《Note》

connector.

The performance of the backlight, for example lifetime or brightness, is much influenced by the characteristics of the DC-AC inverter for the lamp. When you design or order the inverter, please make sure that a poor lighting caused by the mismatch of the backlight and the inverter (miss-lighting, flicker, etc.) never occurs. When you confirm it, the module should be operated in the same condition as it is installed in your instrument.

Use the lamp inverter power source incorporating such safeguard as overvoltage / overcurrent protective circuit

or lamp voltage waveform detection circuit, which should have individual control of each lamp. In case one circuit without such individual control is connected to more than two lamps, excessive current may

flow into one lamp when the other one is not in operation. Synchronize frequency and phase of CCFT in the same connector. Otherwise it may exceed rated voltage of

# 7. Timing characteristics of input signals

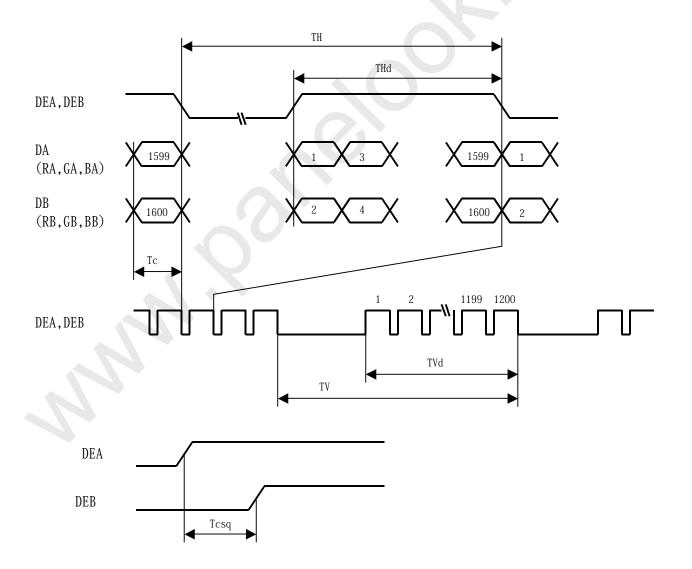
## 7-1-1. Timing characteristics

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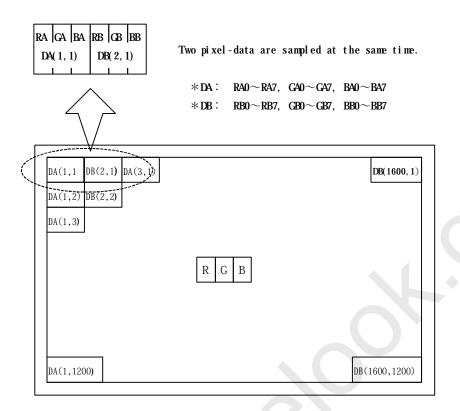
	Parameter	Symbol	Min.	Тур.	Max.	Unit	Remark
Clock	Frequency	1/Tc	60.0	81.0	85.0	MHz	
	Skew	Tcsq	-4	0	4	ns	[Note1]
Data enable	Horizontal period	TH	830	1080	1317	clock	
signal			10.0	13.3	15.5	μs	
	Horizontal period (High)	THd	800	800	800	clock	
	Vertical period	TV	1205	1250	2000	line	[Note2]
			12.1	16.7	_	ms	
	Vertical period (High)	TVd	1200	1200	1200	line	

[Note1] Lvds (A port)—Lvds (B port) phase difference

In case of using the long vertical period, the deterioration of display quality, flicker etc. may occur.



7-2 Input Data Signals and Display Position on the screen Graphics and texts can be displayed on a 1600  $\times$  3 $\times$  1200 dots panel with 16M colors by supplying 48 bit data signal (8bit/color [256 gray scales]  $\times$  3  $\times$  2 pixels).



Display position of input data (H, V)



8. Input Signals, Basic Display Colors and Gray Scale of Each Color

8. Ш	nput Signals, Basic Display Colors and Gray Scale of Each Color																									
											ı		Data	sign	nal											
	Colors &	Gray	RA0	RA1	RA2	RA3	RA4	RA5	RA6	RA7	GA0	GA1	GA2	GA3	GA4	GA5	GA6	GA7	BA0	BA1	BA2	BA3	BA4	BA5	BA6	BA7
	Gray scale	Scale	RB0	RB1	RB2	RB3	RB4	RB5	RB6	RB7	GB0	GB1	GB2	GB3	GB4	GB5	GB6	GB7	BB0	BB1	BB2	BB3	BB4	BB5	BB6	BB7
	Black	_	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0
	Blue	_	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	1	1	1	1	1	1	1	1
or	Green	_	0	0	0	0	0	0	0	0	1	1	1	1	1	1	1	1	0	0	0	0	0	0	0	0
Basic Color	Cyan	_	0	0	0	0	0	0	0	0	1	1	1	1	1	1	1	1	1	1	1	1	1	1	1	1
asic	Red	_	1	1	1	1	1	1	1	1	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0
В	Magenta	-	1	1	1	1	1	1	1	1	0	0	0	0	0	0	0	0	1	1	1	1	1	1	1	1
	Yellow	_	1	1	1	1	1	1	1	1	1	1	1	1	1	1	1	1	0	0	0	0	0	0	0	0
	White	_	1	1	1	1	1	1	1	1	1	1	1	1	1	1	1	1	1	1	1	1	1	1	1	1
	Black	GS0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0
	仓	GS1	1	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0
Red	Darker	GS2	0	1	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0
Gray Scale of Red	Û	<b>V</b>					l							,	ν,							,	ν			-
Scal	Û	<b>V</b>					L							,	ı							,	V			
ray	Brighter	GS253	1	0	1	1	1	1	1	1	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0
	Û	GS254	0	1	1	1	1	1	1	1	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0
	Red	GS255	1	1	1	1	1	1	1	1	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0
	Black	GS0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0
u	Û	GS1	0	0	0	0	0	0	0	0	1	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0
Gree	Darker	GS2	0	0	0	0	0	0	0	0	0	1	0	0	0	0	0	0	0	0	0	0	0	0	0	0
of (	Û	<b>V</b>					ν ν								l								ν			
Gray Scale of Green	Û	<b>+</b>													· L							,	↓			
ray S	Brighter	GS253	0	0	0	0	0	0	0	0	1	0	1	1	1	1	1	1	0	0	0	0	0	0	0	0
5	Û	GS254	0	0	0	0	0	0	0	0	0	1	1	1	1	1	1	1	0	0	0	0	0	0	0	0
	Green	GS255	0	0	0	0	0	0	0	0	1	1	1	1	1	1	1	1	0	0	0	0	0	0	0	0
	Black	GS0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0
0	Û	GS1	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	1	0	0	0	0	0	0	0
Blue	Darker	GS2	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	1	0	0	0	0	0	0
go e	Û	<b>V</b>													l I								Į.			
Scal	Û	<b>V</b>				<b>↓</b>									-								<b>.</b> ↓			
Gray Scale of Blue	Brighter	GS253	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	1	0	1	1	1	1	1	1
Ğ	₽ û	GS254	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	1	1	1	1	1	1	1
	Blue	GS254 GS255	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	1	1	1	1	1	1	1	1
		us233		U		11:0					U	U	U	U	U	U	U	U	1	1	1	1	1	1	1	1

0: Low level voltage,

1 : High level voltage.

Each basic color can be displayed in 256 gray scales from 8 bit data signals. According to the combination of total 48 bit data signals, the 16-million-color display can be achieved on the screen.

#### 9. Optical Characteristics

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 $Ta=25^{\circ}C$ , Vcc=+12V

								,
Parameter		Symbol	Condition	Min.	Тур.	Max.	Unit	Remark
Viewing	Vertical	θ11	CR≧10	80	88	_	Deg.	[Note1,4]
Angle range		θ 12		80	88	_	Deg	
	Horizontal	$\theta$ 21, $\theta$ 22		80	88	_	Deg.	
Contras	st ratio	CR	$\theta = 0^{\circ}$	350	500	_		[Note2,4]
Response R Time	ise +Decay	$\tau r + \tau d$		_	20	55	ms	[Note3,4]
Chroma	aticity of	Wx		0.283	0.313	0.343	_	[Note4]
w	hite	Wy		0.299	0.329	0.359	-	
Chroma	ticity of	Rx		0.614	0.644	0.674		
re	d	Ry		0.307	0.337	0.367	-	
Chroma	ticity of	Gx		0.264	0.294	0.324	_	
gre	en	Gy		0.574	0.604	0.634	_	
Chroma	ticity of	Bx		0.114	0.144	0.174	_	
blue		Ву		0.063	0.093	0.123	_	
Luminance	e of white	YL					cd/m <sup>2</sup>	IL=6.0mA rms
				200	250	_		F <sub>L</sub> =60kHz
								[Note4]
White Ur	niformity	$\delta$ w				1.25	_	[Note5]

<sup>\*</sup>The measurement shall be executed 30 minutes after lighting at rating.

The optical characteristics shall be measured in a dark room or equivalent state with the method shown in Fig.2 below.

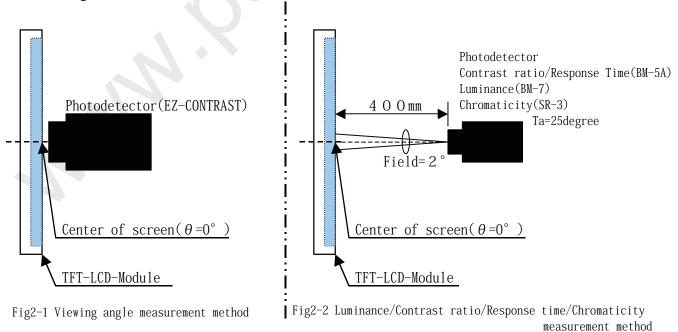
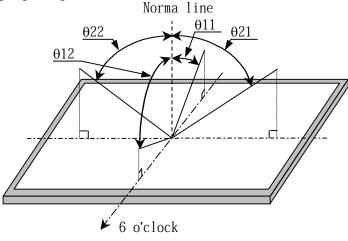


Fig2 Optical characteristics measurement method

### [Note1] Definitions of viewing angle range:

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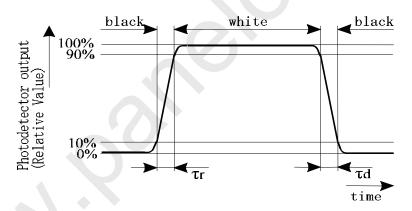


## [Note2] Definition of contrast ratio:

The contrast ratio is defined as the following.

#### [Note3] Definition of response time:

The response time is defined as the following figure and shall be measured by switching the input signal for "black" and "white".

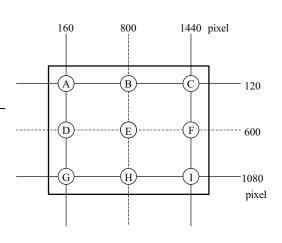


#### [Note4] This shall be measured at center of the screen.

#### [Note5] Definition of white uniformity:

White uniformity is defined as the following with nine measurements. (A∼I).

Maximum Luminance of nine points (brightness) ä₩= Minimum Luminance of nine points (brightness)





#### 10. Handling Precautions

- a) Be sure to turn off the power supply when inserting or disconnecting the cable.
- b) Be sure to design the cabinet so that the module can be installed without any extra stress such as warp or twist.
- c) Since the front polarize is easily damaged, pay attention not to scratch it.
- d) Since long contact with water may cause discoloration or spots, wipe off water drop immediately.
- e) When the panel surface is soiled, wipe it with absorbent cotton or other soft cloth.
- f) Since the panel is made of glass, it may break or crack if dropped or bumped on hard surface. Handle with care.
- g) Since CMOS LSI is used in this module, take care of static electricity and take the human earth into consideration when handling.
- h) Make sure the mounting holes of the module are grounded sufficiently. Take electro-magnetic interference (EMI) into consideration.
- i) The module has some printed circuit boards (PCBs) on the back side. Take care to keep them form any stress or pressure when handling or installing the module; otherwise some of electronic parts on the PCBs may be damaged.
- j) Observe all other precautionary requirements in handling components.
- k) When some pressure is added onto the module from rear side constantly, it causes display non-uniformity issue, functional defect, etc. So, please avoid such design.
- If the stress is applied onto the panel under operating conditions, display defects such as black dots may occur. So, do not press the display with fingers.
  - To recover this defect, turn off the power supply and restart after several seconds.

#### 11. Packing form

- a) Piling number of cartons : maximum 8 cartons
- b) Packing quantity in one carton: 2 modules
- c) Carton size : 583mm(W)  $\times$  478mm(H) $\times$  215mm(D)
- d) Total mass of one carton filled with full modules: 8.9kg

## 12. Reliability test items

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No.	Test item	Conditions							
1	High temperature storage test	Ta=60°C 240h							
2	Low temperature storage test	Ta=-25℃ 240h							
2	High temperature	Ta=40°C ; 95%RH 240h							
3	& high humidity operation test	(No condensation)							
	Tr. L.	Ta=50°C 240h							
4	High temperature operation test	(The panel temp. must be less than $60^{\circ}$ C)							
5	Low temperature operation test	Ta=0°C 240H							
		Waveform : Sine wave							
		Frequency: 10 ~ 57Hz/Vibration width (one side): 0.075mm							
	Vibration test	: $58 \sim 500$ Hz/Gravity : $9.8$ m/s <sup>2</sup>							
6	(non- operating)	Sweep time: 11minutes							
		Test period : 3 hours							
		(1 hour for each direction of X,Y,Z)							
		Max. gravity: 490m/s <sup>2</sup>							
	Shock test	Pulse width: 11ms, sine wave							
7	(non- operating)	Direction: $\pm X$ , $\pm Y$ , $\pm Z$ ,							
		once for each direction.							
8	Thermal shock test	Ta= $-20^{\circ}$ C $\sim$ 60 $^{\circ}$ C; 5 cycles							
	A 1/2 - 1	Test period: 10 hours (1 hour for each temperature)							
9	Altitude	Ta=50°C, 70kPa, 3,048m (10,000ft), t=24h (Operating) Ta=70°C, 12kPa, 15,240m (50,000ft), t=24h (Storage)							

## [Result Evaluation Criteria]

Under the display quality test conditions with normal operation state, these shall be no change which may affect practical display function.

# 14. Carton storage condition

 $0^{\circ}$ C to  $40^{\circ}$ C Temperature Humidity 95%RH or less

Reference condition:  $20^{\circ}\text{C}$  to  $35^{\circ}\text{C}$ ,  $85^{\circ}\text{RH}$  or less (summer)

:  $5^{\circ}$ C to  $15^{\circ}$ C , 85%RH or less (winter)

• the total storage time  $(40^{\circ}\text{C},95\%\text{RH})$ : 240H or less

Sunlight Be sure to shelter a product from the direct sunlight.

Harmful gas, such as acid and alkali which bites electronic components and/or Atmosphere

wires, must not be detected.

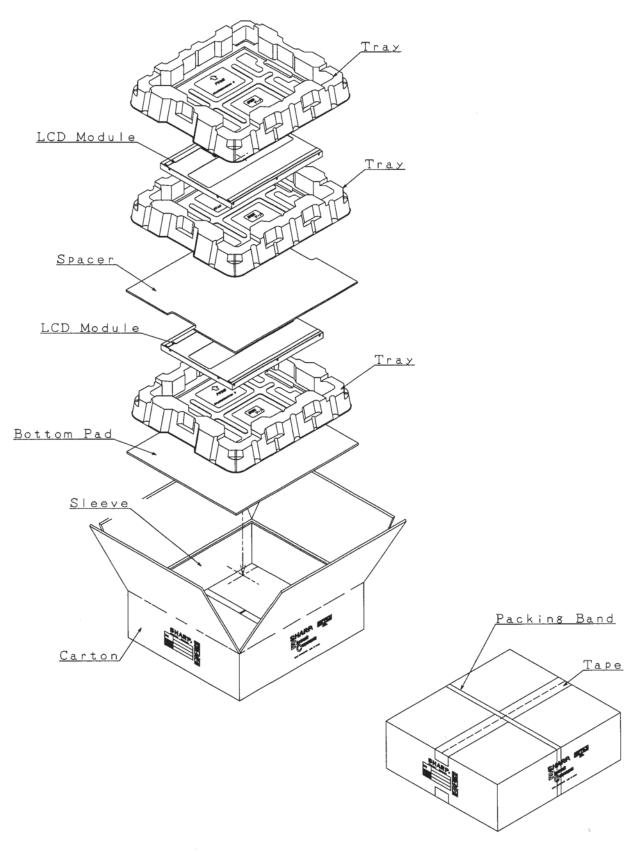
Be sure to put cartons on palette or base, don't put it on floor, and store them with Notes

removing from wall.

Please take care of ventilation in storehouse and around cartons, and control

changing temperature is within limits of natural environment.

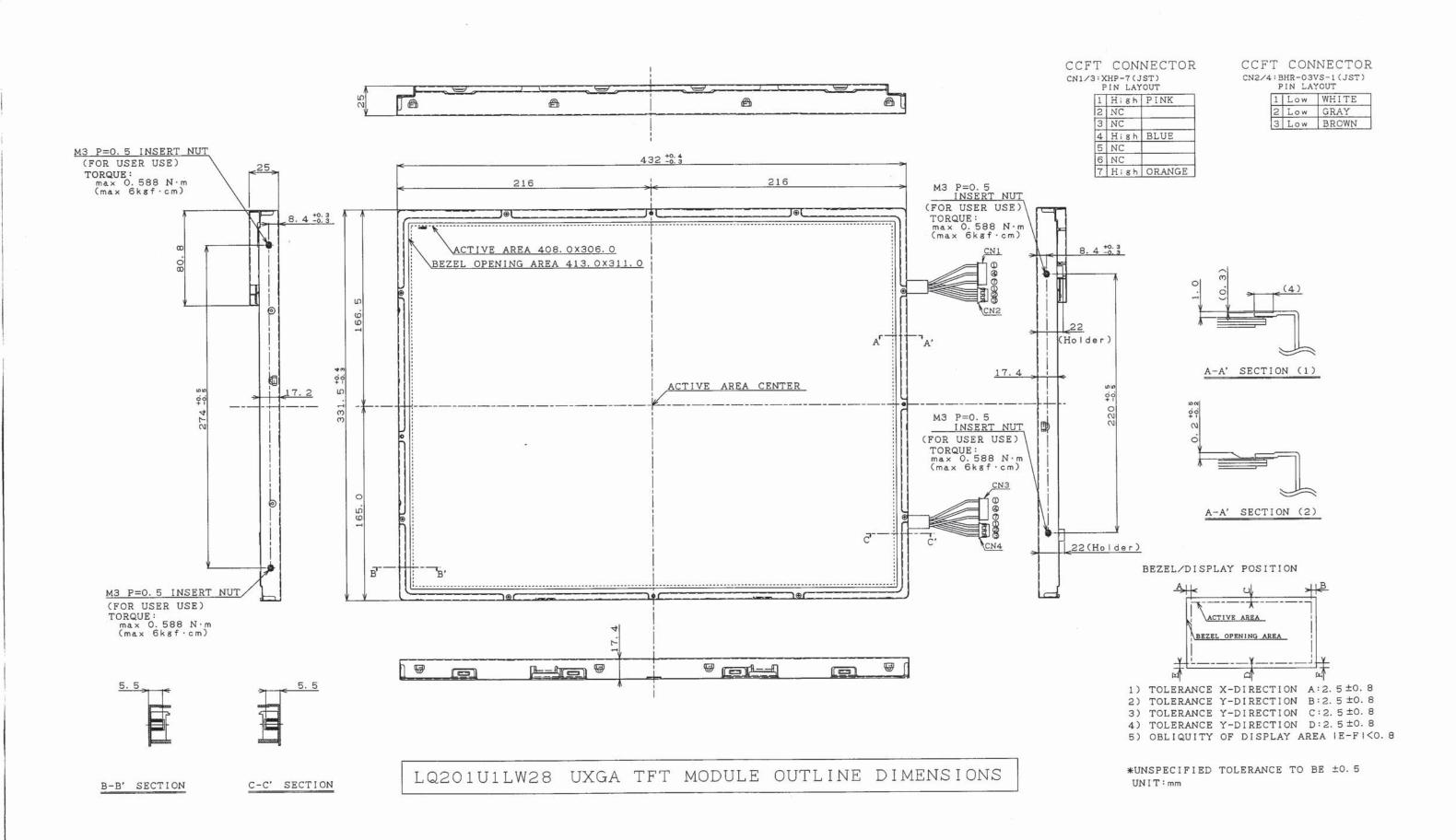
Storage period 1 year

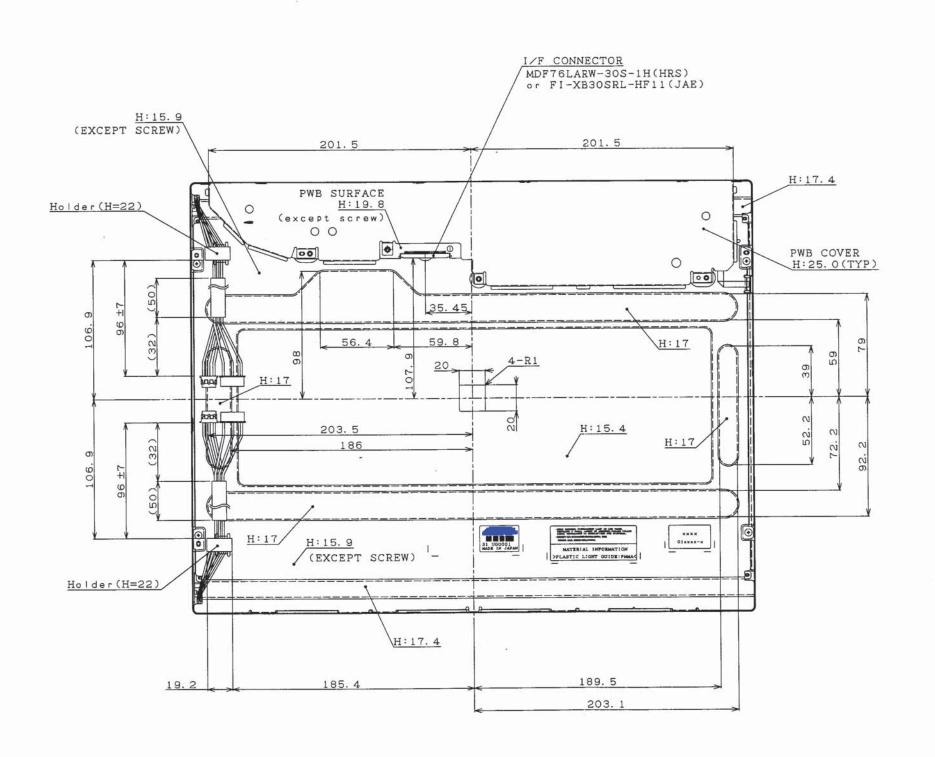


Packing Form

Global LCD Panel Exchange Center

LD15616-19





LQ201U1LW28 UXGA TFT MODULE OUTLINE DIMENSIONS