TOSHIBA HIGH EFFICIENCY DIODE STACK (HED) SILICON EPITAXIAL TYPE

# 20DL2CZ47A, 20FL2CZ47A

# SWITCHING MODE POWER SUPPLY APPLICATION CONVERTER & CHOPPER APPLICATION

• Repetitive Peak Reverse Voltage : VRRM = 200 V, 300 V

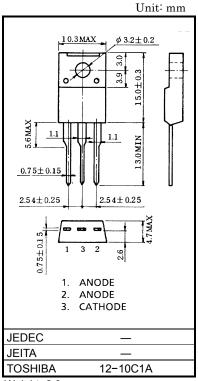
• Average Output Rectified Current : Io = 20 A

• Ultra Fast Reverse-Recovery Time : trr = 35 ns (Max)

• Low Switching Losses and Low Output Noise

# MAXIMUM RATINGS (Ta = 25°C)

CHARAC	TERISTIC	SYMBOL	RATING	UNIT	
Repetitive Peak Reverse Voltage	20DL2CZ47A	V <sub>RRM</sub>	200	V	
	20FL2CZ47A	V RRM	300		
Average Output Rectified Current (Full Sine Waveform)		IO	20	Α	
Peak One Cycle Surge Forward Current (Non-Repetitive)		l	100 (50H <sub>Z</sub> )	А	
		<sup>I</sup> FSM	110 (60H <sub>Z</sub> )		
Junction Temperature		Tj	-40~150	°C	
Storage Temperature Range		T <sub>stg</sub>	-40~150	°C	
Screw Torque		_	— 0.6		



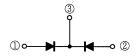
Weight: 2.0g

# **ELECTRICAL CHARACTERISTICS (Ta = 25°C)**

CHARACTERISTIC		SYMBOL	TEST CONDITION	MIN	TYP.	MAX	UNIT
Peak Forward Voltage (Note 1)	20DL2CZ47A	- V <sub>FM</sub>	I <sub>FM</sub> = 10A	_	_	0.98	V
	20FL2CZ47A				_	1.3	, v
Repetitive Peak Reverse Current (Note 1)		I <sub>RRM</sub>	V <sub>RRM</sub> = Rated	ı	ı	50	μΑ
Reverse Recovery Tim	ne (Note 1)	t <sub>rr</sub>	$I_F = 2.0A$ , di / dt = -50A / $\mu$ s		_	35	ns
Forward Recovery Tim	ne (Note 1)	t <sub>fr</sub>	I <sub>F</sub> = 1.0A		_	100	ns
Thermal Resistance		R <sub>th (j-c)</sub>	DC Total, Junction to Case	_	_	3.0	°C/W

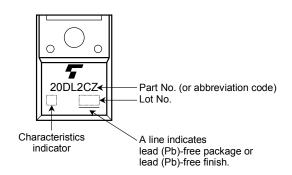
Note 1: A value applied to one cell.

#### **POLARITY**





#### **MARKING**



Abbreviation Code	Part No.			
20DL2CZ	20DL2CZ47A			
20FL2CZ	20FL2CZ47A			

### **Handling Precaution**

The maximum ratings denote the absolute maximum ratings, which are rated values and must not be exceeded during operation, even for an instant. The following are the general derating methods that we recommend when you design a circuit with a device.

VRRM: We recommend that the worst case voltage, including surge voltage, be no greater than 80% of the maximum rating of VRRM for a DC circuit and be no greater than 50% of that of VRRM for an AC circuit. VRRM has a temperature coefficient of 0.1%/°C. Take this temperature coefficient into account designing a device at low temperature.

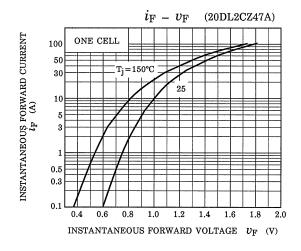
IO: We recommend that the worst case current be no greater than 80% of the maximum rating of IO. Carry out adequate heat design. If you can't design a circuit with excellent heat radiation, set the margin by using an allowable Tamax-IO curve.

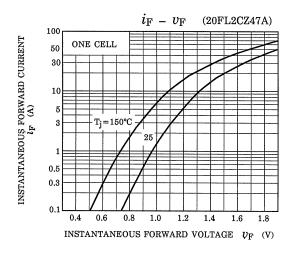
This rating specifies the non-repetitive peak current in one cycle of a 50-Hz sine wave, condition angle 180. Therefore, this is only applied for an abnormal operation, which seldom occurs during the lifespan of the device.

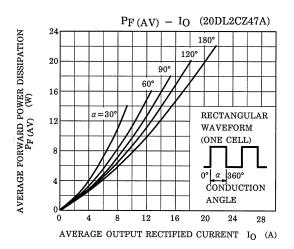
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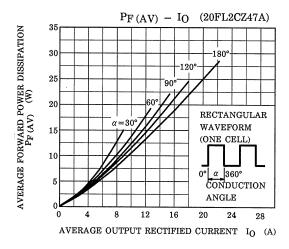
We recommend that a device be used at a Tj of below 120°C under the worst load and heat radiation conditions.

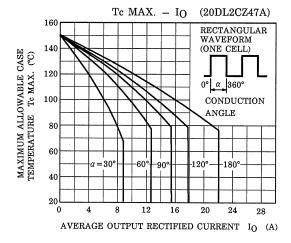
Please refer to the Rectifiers databook for further information.

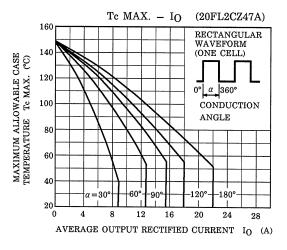


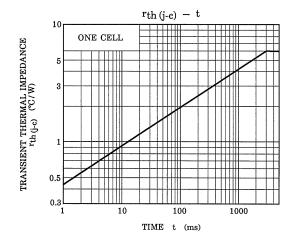


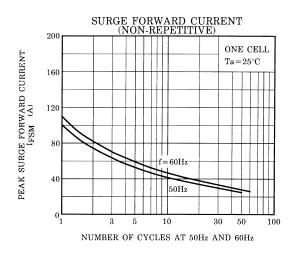


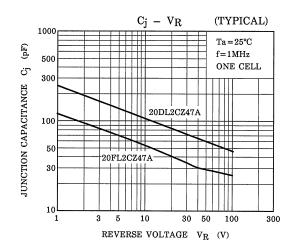












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