

- T-52-33-01

## 2100 PC/AT Integrated System Controller

The 2100 is an integrated high performance CMOS PC/AT system controller that integrates the following functions and logic into one single chip: clock generator and selector, bus controller, bus swap logic, coprocessor interface logic, memory decoder, command delay and wait state generation circuits, reset and shut down logic, and ADDR/DATA control logic.

### Features

- 100% hardware and software compatible with the IBM PC/AT
- Fully compatible with Intel's 82284 clock generator, and 82288 bus controller
- Built-in 80287 coprocessor interface logic
- Built-in command delay and wait state generation logic
- ROM chip select for 27128 or 27256
- Built-in memory controller
- Turbo speed change performed through hardware or software
- 8 MHz I/O AT BUS compatible
- Bus swap function
- 1.5 micron high performance CMOS technology
- TTL compatible
- 84-L PLCC package

### Block Diagram

