



# 28N50-CB

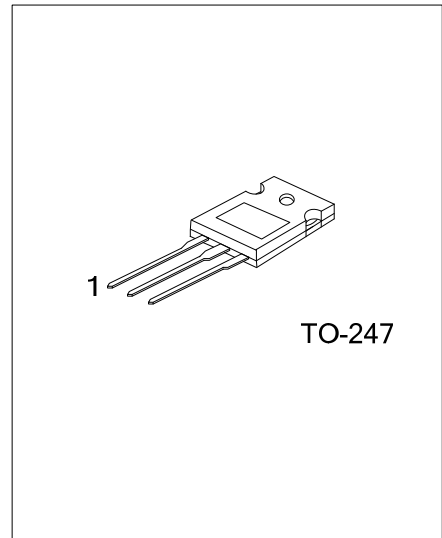
*Power MOSFET*

## 28A, 500V N-CHANNEL POWER MOSFET

■ DESCRIPTION

The UTC **28N50-CB** is a N-Channel enhancement mode power MOSFET. The device adopts planar stripe and uses DMOS technology to minimize and provide lower on-state resistance and faster switching speed. It can also withstand high energy pulse under the avalanche and commutation mode conditions.

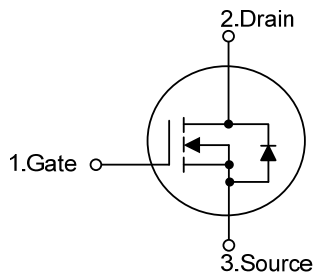
The UTC **28N50-CB** is ideally suitable for high efficiency switch mode power supply, power factor correction, electronic lamp ballast based on half bridge topology.



■ FEATURES

- \*  $R_{DS(ON)} \leq 0.22\Omega$  @  $V_{GS}=10V, I_D=14A$
- \* Fast switching capability
- \* Avalanche energy tested
- \* Improved dv/dt capability, high ruggedness

■ SYMBOL



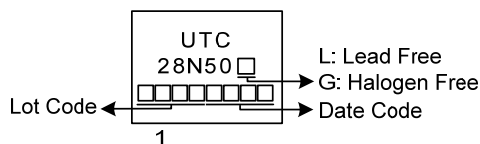
■ ORDERING INFORMATION

Ordering Number		Package	Pin Assignment			Packing
Lead Free	Halogen Free		1	2	3	
28N50L-T47-T	28N50G-T47-T	TO-247	G	D	S	Tube

Note: Pin Assignment: G: Gate D: Drain S: Source

<p>28N50G-T47-T</p> <p>(1)Packing Type</p> <p>(2)Package Type</p> <p>(3)Green Package</p>	<p>(1) T: Tube</p> <p>(2) T47: TO-247</p> <p>(3) G: Halogen Free and Lead Free, L: Lead Free</p>
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■ MARKING



■ ABSOLUTE MAXIMUM RATINGS ( $T_C = 25^\circ\text{C}$ , unless otherwise specified)

PARAMETER		SYMBOL	RATINGS	UNIT
Drain-Source Voltage		$V_{DSS}$	500	V
Gate-Source Voltage		$V_{GSS}$	$\pm 30$	V
Drain Current	Continuous	$I_D$	28	A
	Pulsed (Note 2)	$I_{DM}$	56	A
Avalanche Energy	Single Pulsed (Note 3)	$E_{AS}$	240	mJ
Peak Diode Recovery dv/dt (Note 4)		dv/dt	3.8	V/ns
Power Dissipation		$P_D$	280	W
Junction Temperature		$T_J$	+150	$^\circ\text{C}$
Storage Temperature		$T_{STG}$	-55 ~ +150	$^\circ\text{C}$

Notes: 1. Absolute maximum ratings are those values beyond which the device could be permanently damaged.

Absolute maximum ratings are stress ratings only and functional device operation is not implied.

2. Repetitive Rating : Pulse width limited by maximum junction temperature.

3.  $L = 10\text{mH}$ ,  $I_{AS} = 6.9\text{A}$ ,  $V_{DD} = 50\text{V}$ ,  $R_G = 25\Omega$ , Starting  $T_J = 25^\circ\text{C}$

4.  $I_{SD} \leq 28\text{A}$ ,  $di/dt \leq 200\text{A}/\mu\text{s}$ ,  $V_{DD} \leq BV_{DSS}$ , Starting  $T_J = 25^\circ\text{C}$

■ THERMAL DATA

PARAMETER	SYMBOL	RATINGS	UNIT
Junction to Ambient	$\theta_{JA}$	60	$^\circ\text{C}/\text{W}$
Junction to Case	$\theta_{JC}$	2.24	$^\circ\text{C}/\text{W}$

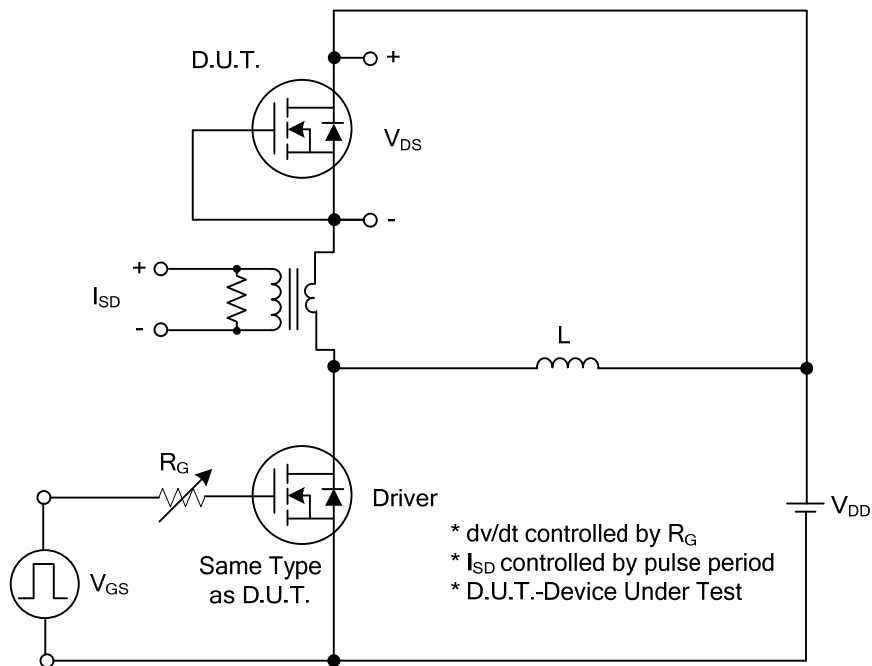
■ ELECTRICAL CHARACTERISTICS ( $T_J=25^\circ\text{C}$ , unless otherwise specified)

PARAMETER	SYMBOL	TEST CONDITIONS	MIN	TYP	MAX	UNIT
<b>OFF CHARACTERISTICS</b>						
Drain-Source Breakdown Voltage	$BV_{DSS}$	$V_{GS}=0V, I_D=250\mu A$	500			V
Drain-Source Leakage Current	$I_{DSS}$	$V_{DS}=500V, V_{GS}=0V$			10	$\mu A$
Gate-Source Leakage Current	$I_{GSS}$	$V_{GS}=30V, V_{DS}=0V$			100	nA
		$V_{GS}=-30V, V_{DS}=0V$			-100	nA
<b>ON CHARACTERISTICS</b>						
Gate Threshold Voltage	$V_{GS(TH)}$	$V_{DS}=V_{GS}, I_D=250\mu A$	2.0		4.0	V
Static Drain-Source On-State Resistance	$R_{DS(ON)}$	$V_{GS}=10V, I_D=14A$			0.22	$\Omega$
<b>DYNAMIC CHARACTERISTICS</b>						
Input Capacitance	$C_{ISS}$	$V_{DS}=25V, V_{GS}=0V, f=1.0\text{MHz}$		4100		pF
Output Capacitance	$C_{OSS}$			380		pF
Reverse Transfer Capacitance	$C_{RSS}$			8.5		pF
<b>SWITCHING CHARACTERISTICS</b>						
Total Gate Charge (Note 1)	$Q_G$	$V_{DS}=100V, V_{GS}=10V, I_D=28A$ $I_G=1\text{mA}$ (Note 1, 2)		72		nC
Gate-Source Charge	$Q_{GS}$			17		nC
Gate-Drain Charge	$Q_{GD}$			12		nC
Turn-On Delay Time (Note 1)	$t_{D(ON)}$	$V_{DD}=100V, V_{GS}=10V, I_D=28A,$ $R_G=25\Omega$ (Note 1, 2)		50		nS
Turn-On Rise Time	$t_R$			22		nS
Turn-Off Delay Time	$t_{D(OFF)}$			180		nS
Turn-Off Fall Time	$t_F$			53		nS
<b>DRAIN-SOURCE DIODE CHARACTERISTICS AND MAXIMUM RATINGS</b>						
Maximum Continuous Drain-Source Diode Forward Current	$I_S$				28	A
Maximum Pulsed Drain-Source Diode Forward Current	$I_{SM}$				56	A
Drain-Source Diode Forward Voltage (Note 1)	$V_{SD}$	$I_S=28A, V_{GS}=0V$			1.4	V
Body Diode Reverse Recovery Time	$t_{rr}$	$I_S=28A, V_{GS}=0V,$ $di_f/dt=100A/\mu s$		360		ns
Body Diode Reverse Recovery Charge	$Q_{rr}$			4.6		$\mu C$

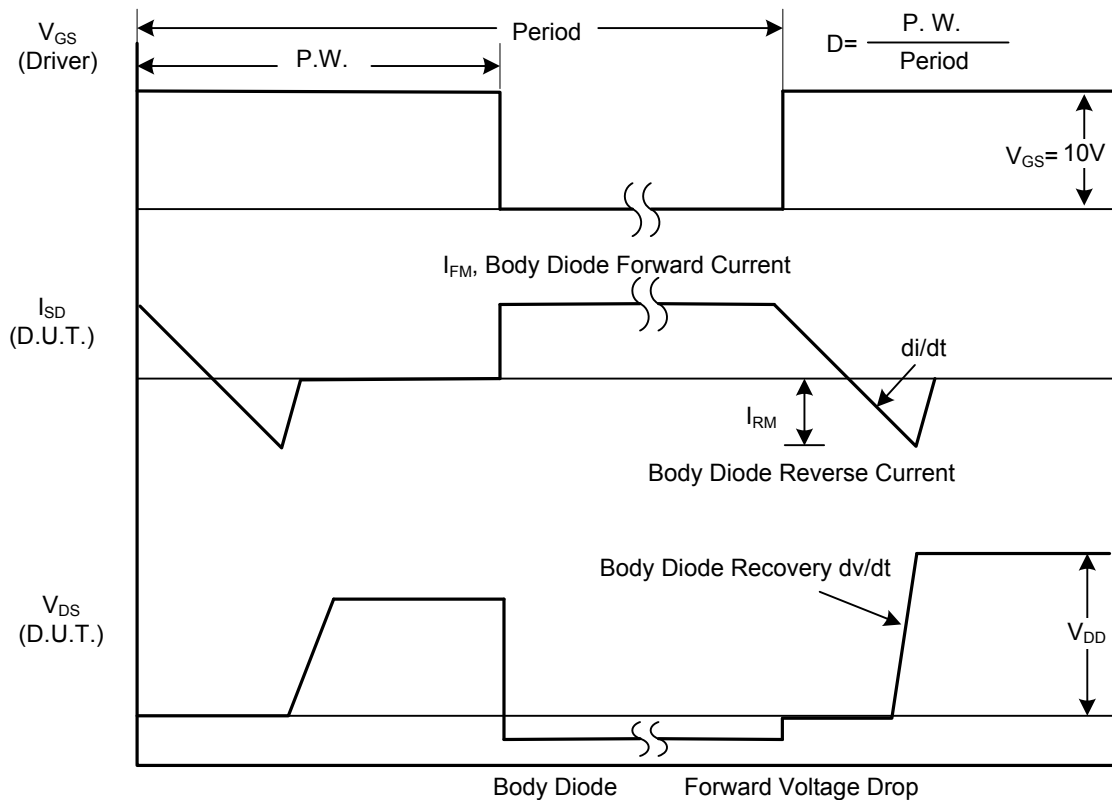
Notes: 1. Pulse Test : Pulse width $\leq 300\mu s$ , Duty cycle $\leq 2\%$

2. Essentially independent of operating ambient temperature

■ TEST CIRCUITS AND WAVEFORMS

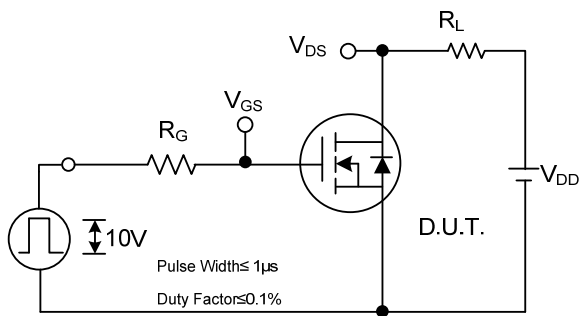


Peak Diode Recovery  $dv/dt$  Test Circuit

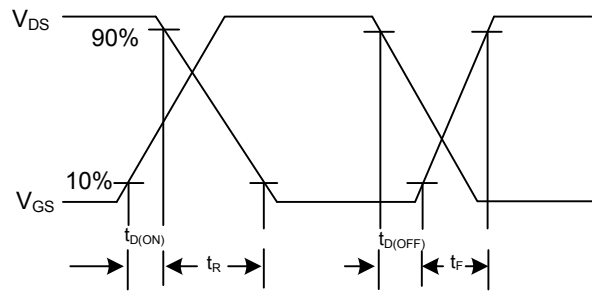


Peak Diode Recovery  $dv/dt$  Waveforms

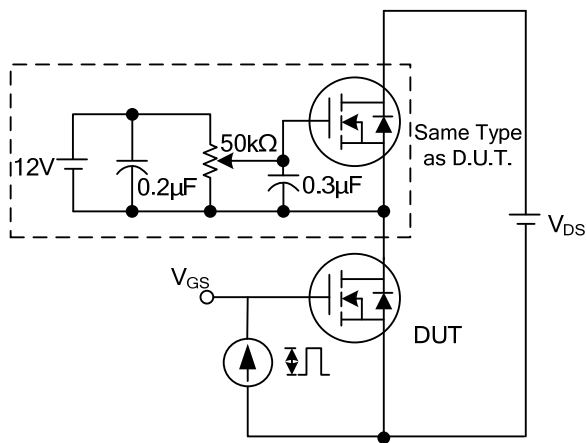
## TEST CIRCUITS AND WAVEFORMS (Cont.)



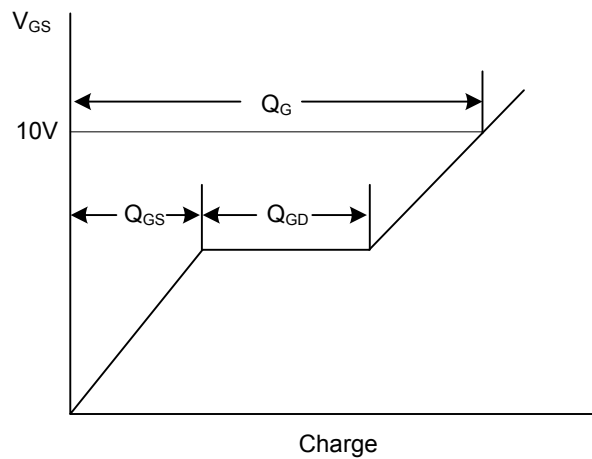
**Switching Test Circuit**



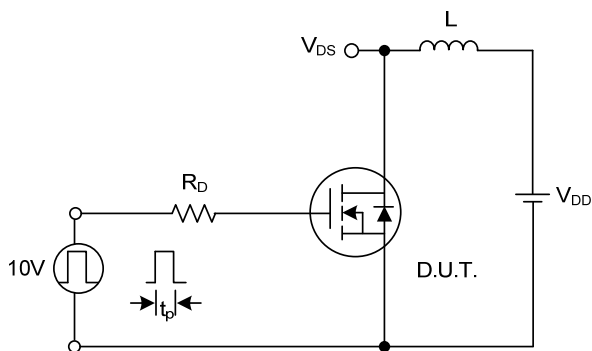
**Switching Waveforms**



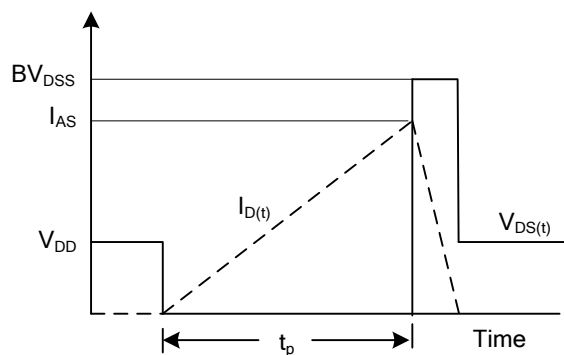
**Gate Charge Test Circuit**



**Gate Charge Waveform**

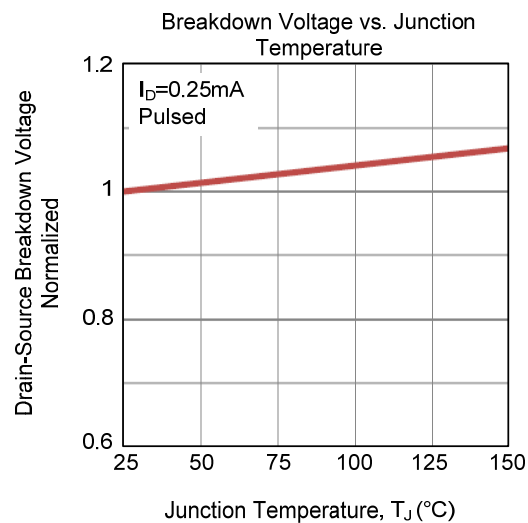
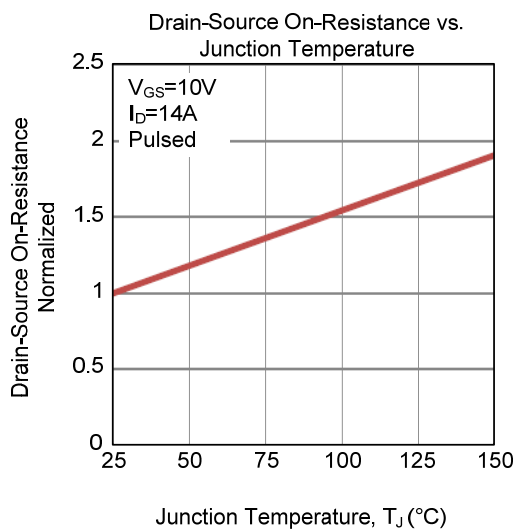
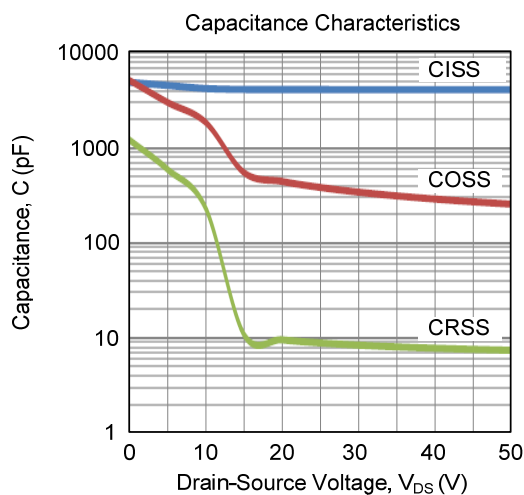
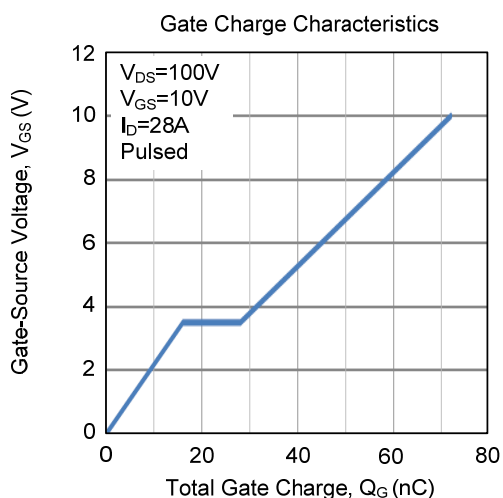
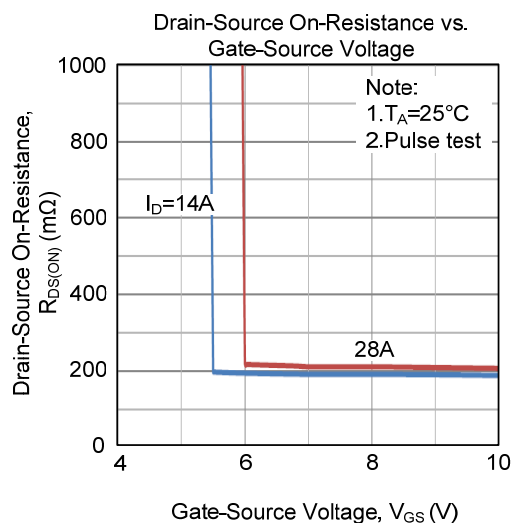
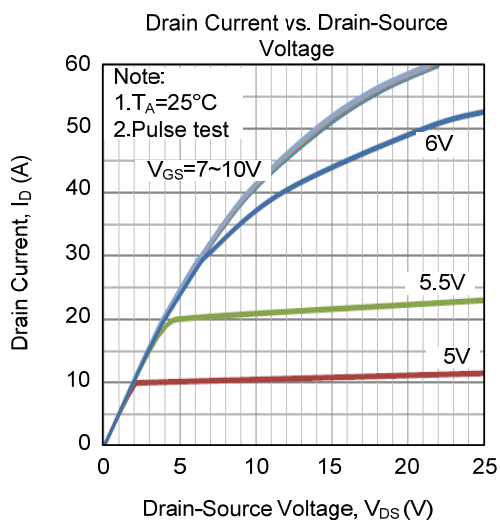


**Unclamped Inductive Switching Test Circuit**

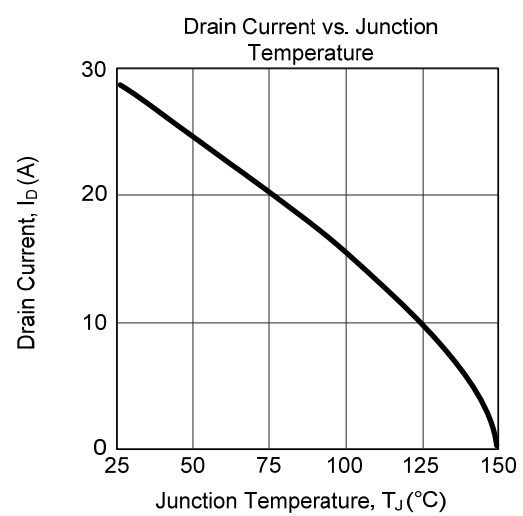
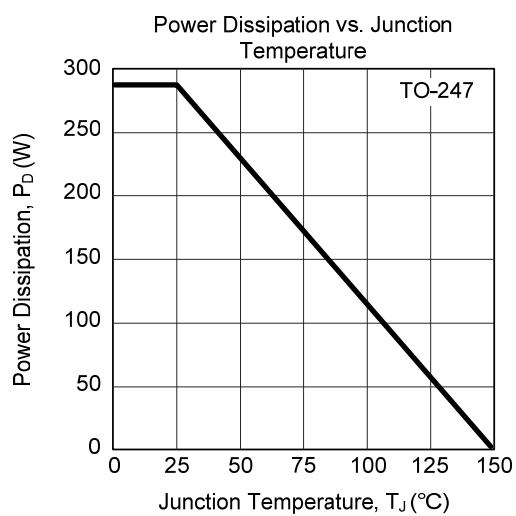
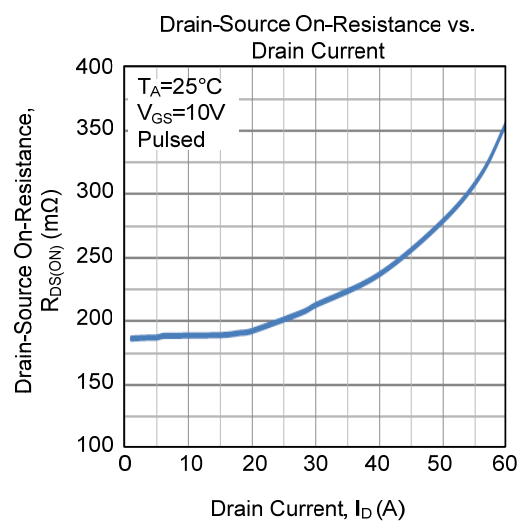
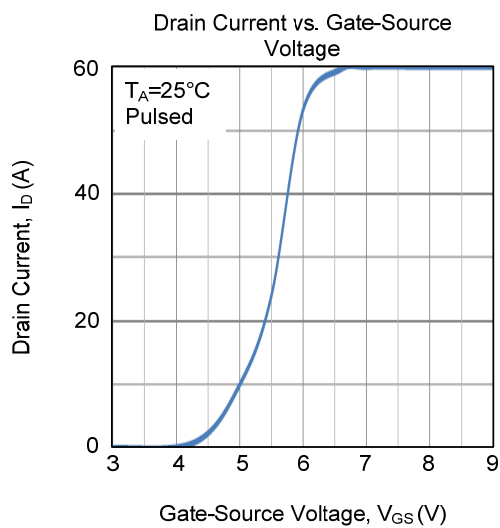
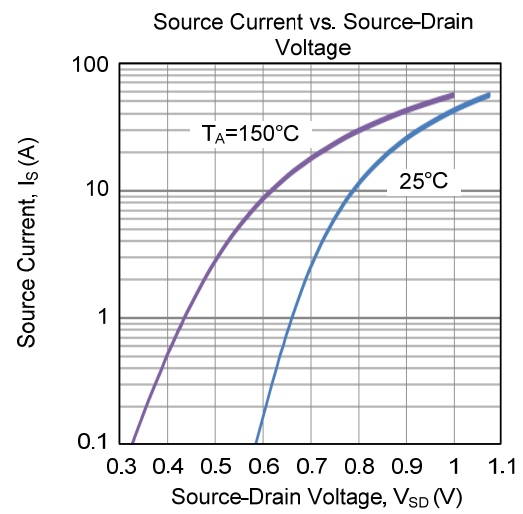
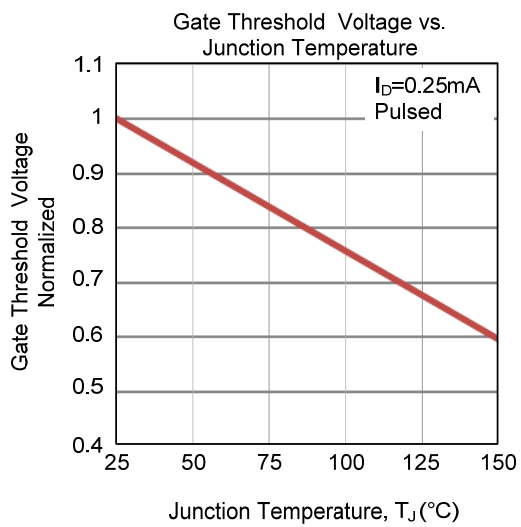


**Unclamped Inductive Switching Waveforms**

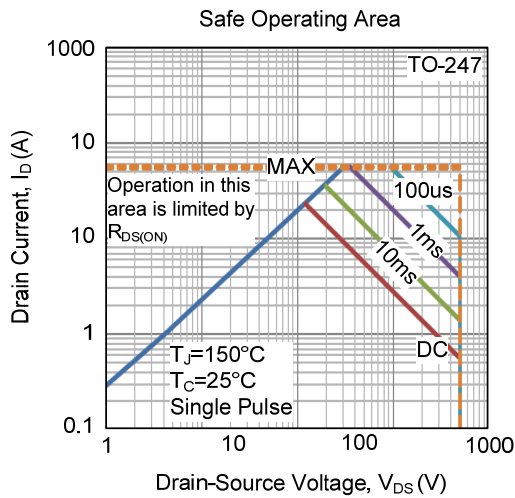
## TYPICAL CHARACTERISTICS



## ■ TYPICAL CHARACTERISTICS (Cont.)



■ TYPICAL CHARACTERISTICS (Cont.)



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