

2MBI600VT-170E

IGBT Modules

IGBT MODULE (V series) 1700V / 600A / 2 in one package

■ Features

- High speed switching
- Voltage drive
- Low Inductance module structure

■ Applications

- Inverter for Motor Drive
- AC and DC Servo Drive Amplifier
- Uninterruptible Power Supply
- Industrial machines, such as Welding machines

■ Maximum Ratings and Characteristics

● Absolute Maximum Ratings (at $T_c=25^\circ\text{C}$ unless otherwise specified)

Items	Symbols	Conditions	Maximum ratings	Units
Collector-Emitter voltage	V_{CES}		1700	V
Gate-Emitter voltage	V_{GES}		± 20	V
Collector current	I_c	Continuous	$T_c=25^\circ\text{C}$ 800 $T_c=100^\circ\text{C}$ 600	A
	I_{cP}	1ms	1200	
	$-I_c$		600	
	$-I_{c\ pulse}$	1ms	1200	
Collector power dissipation	P_c	1 device	4280	W
Junction temperature	T_j		175	$^\circ\text{C}$
Operating junction temperature (under switching conditions)	T_{jop}		150	
Storage temperature	T_{stg}		$-40 \sim +125$	
Isolation voltage	between terminal and copper base (*1) V_{iso}	AC : 1min.	4000	VAC
Screw torque (*2)	Mounting	-	5.75	N m
	Main Terminals	-	10	
	Sense Terminals	-	2.5	

Note *1: All terminals should be connected together when isolation test will be done.

Note *2: Recommendable Value :

Mounting 4.25~5.75 Nm (M6) , Main Terminals 8~10 Nm (M8) , Sense Terminals 1.7~2.5 Nm (M4)

● Electrical characteristics (at $T_j=25^\circ\text{C}$ unless otherwise specified)

Items	Symbols	Conditions	Characteristics			Units	
			min.	typ.	max.		
Zero gate voltage collector current	I_{CES}	$V_{GE} = 0V, V_{CE} = 1700V$	-	-	1.0	mA	
Gate-Emitter leakage current	I_{GES}	$V_{CE} = 0V, V_{GE} = \pm 20V$	-	-	1200	nA	
Gate-Emitter threshold voltage	$V_{GE(th)}$	$V_{CE} = 20V, I_c = 600mA$	6.0	6.5	7.0	V	
Collector-Emitter saturation voltage	$V_{CE(sat)}$ (main terminal)	$V_{GE} = 15V$ $I_c = 600A$	$T_j=25^\circ\text{C}$	-	2.18	2.46	V
			$T_j=125^\circ\text{C}$	-	2.58	-	
			$T_j=150^\circ\text{C}$	-	2.63	-	
	$V_{CE(sat)}$ (chip)		$T_j=25^\circ\text{C}$	-	2.00	2.25	
			$T_j=125^\circ\text{C}$	-	2.40	-	
			$T_j=150^\circ\text{C}$	-	2.45	-	
Internal gate resistance	$R_{G(int)}$		-	2.92	-	Ω	
Input capacitance	C_{ies}	$V_{CE} = 10V, V_{GE} = 0V, f = 1MHz$	-	59	-	nF	
Turn-on	t_{on}	$V_{CC} = 900V, R_{gon} = 0.47\Omega$	-	1.51	-	μs	
	t_r	$I_c = 600A, R_{goff} = 0.82\Omega$	-	0.50	-		
Turn-off	t_{off}	$L_m=75nH$	-	2.07	-		
	t_r	$V_{GE} = \pm 15V, T_j=125^\circ\text{C}$	-	0.58	-		
Forward on voltage	V_F (main terminal)	$V_{GE} = 0V$ $I_F = 600A$	$T_j=25^\circ\text{C}$	-	1.84	2.18	V
			$T_j=125^\circ\text{C}$	-	2.00	-	
			$T_j=150^\circ\text{C}$	-	1.97	-	
	V_F (chip)		$T_j=25^\circ\text{C}$	-	1.66	1.98	
			$T_j=125^\circ\text{C}$	-	1.82	-	
			$T_j=150^\circ\text{C}$	-	1.79	-	
Reverse recovery	t_{rr}	$I_F = 600A, T_j = 125^\circ\text{C}$	-	0.31	-	μs	
Lead resistance, terminal-chip	R lead		-	0.291	-	m Ω	

● Thermal resistance characteristics

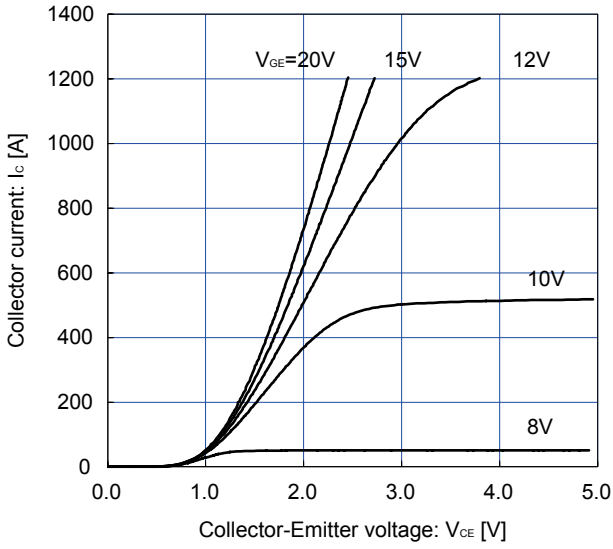
Items	Symbols	Conditions	Characteristics			Units
			min.	typ.	max.	
Thermal resistance(1device)	$R_{th(j-c)}$	IGBT	-	-	0.0350	$^\circ\text{C/W}$
		FWD	-	-	0.0470	
Contact thermal resistance (1module) (*3)	$R_{th(c-f)}$	with Thermal Compound	-	0.0077	-	

Note *3: This is the value which is defined mounting on the additional cooling fin with thermal compound.

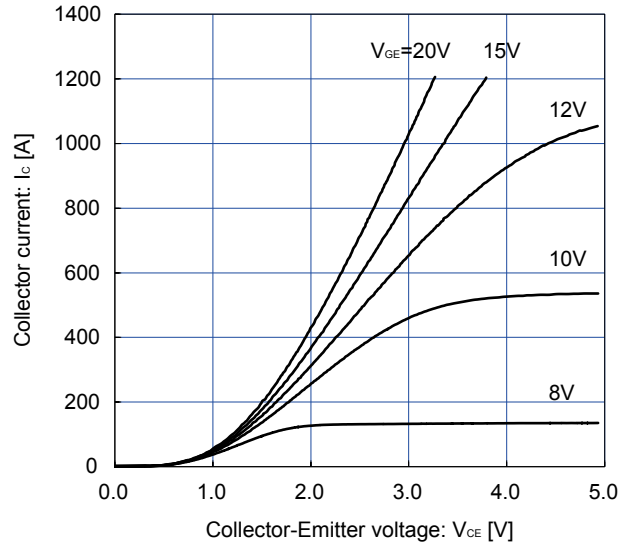


■ Characteristics (Representative)

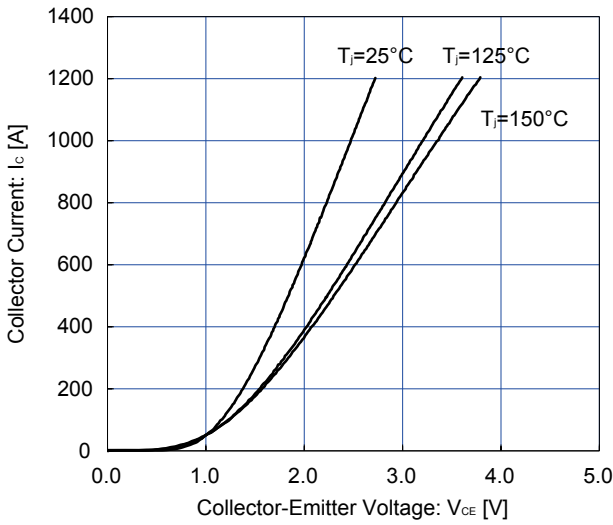
Collector current vs. Collector-Emitter voltage (typ.)
 $T_J = 25^\circ\text{C}$, chip



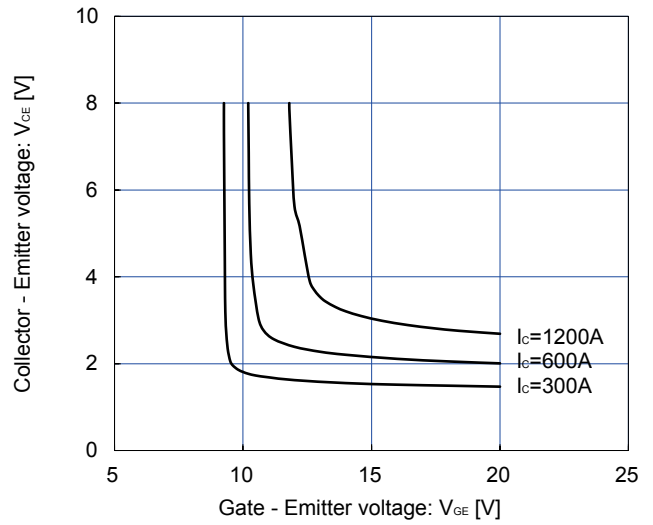
Collector current vs. Collector-Emitter voltage (typ.)
 $T_J = 150^\circ\text{C}$, chip



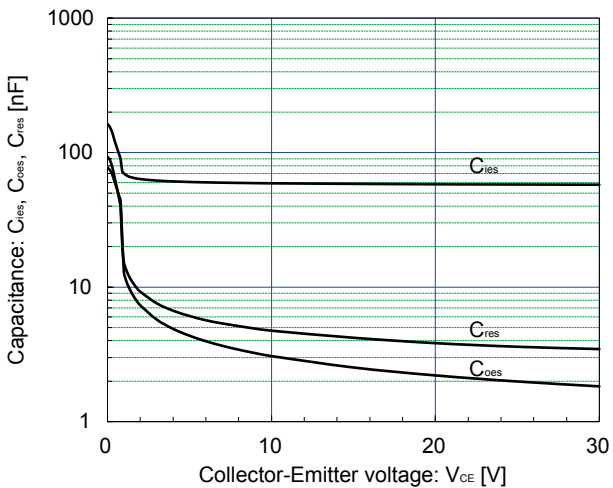
Collector current vs. Collector-Emitter voltage (typ.)
 $V_{GE} = +15\text{V}$, chip



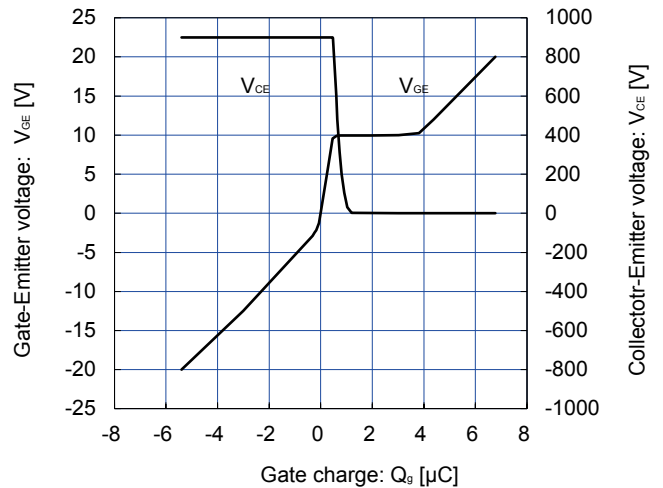
Collector-Emitter voltage vs. Gate-Emitter voltage (typ.)
 $T_J = 25^\circ\text{C}$, chip



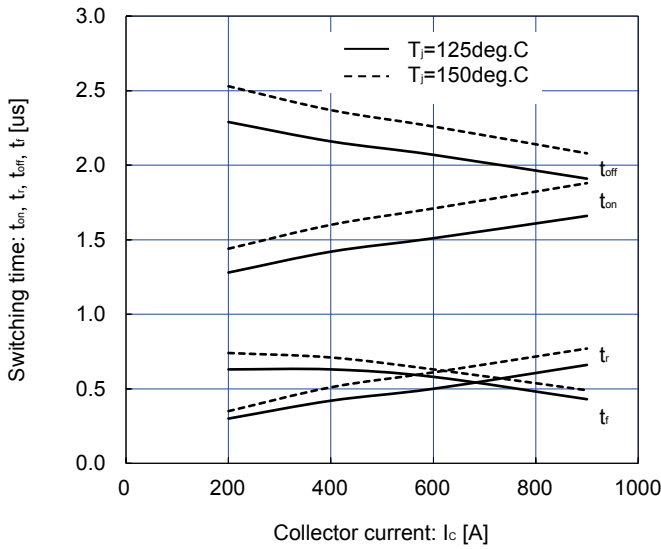
Capacitance vs. Collector-Emitter voltage (typ.)
 $V_{GE} = 0\text{V}$, $f = 1\text{MHz}$, $T_J = 25^\circ\text{C}$



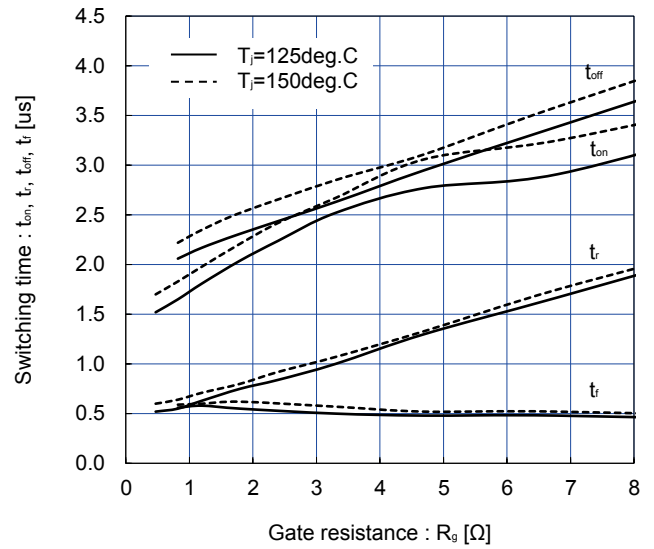
Dynamic Gate charge (typ.)
 $T_J = 25^\circ\text{C}$



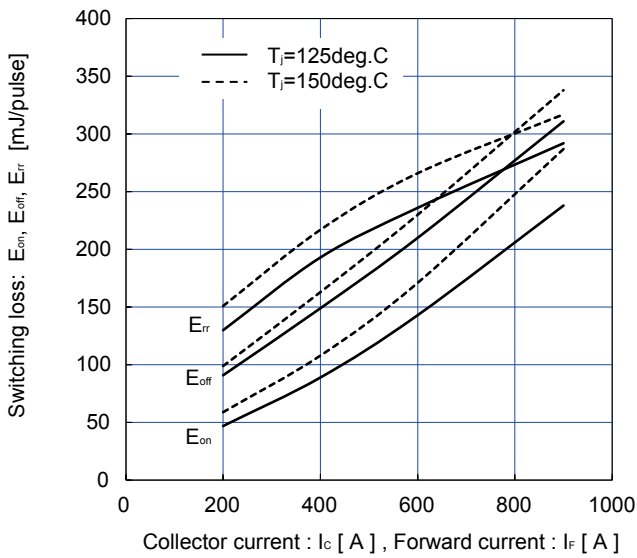
Switching time vs. Collector current (typ.)
 $V_{CC}=900V, V_{GE}=\pm 15V, R_{gon}=0.47\Omega, R_{goff}=0.82\Omega$



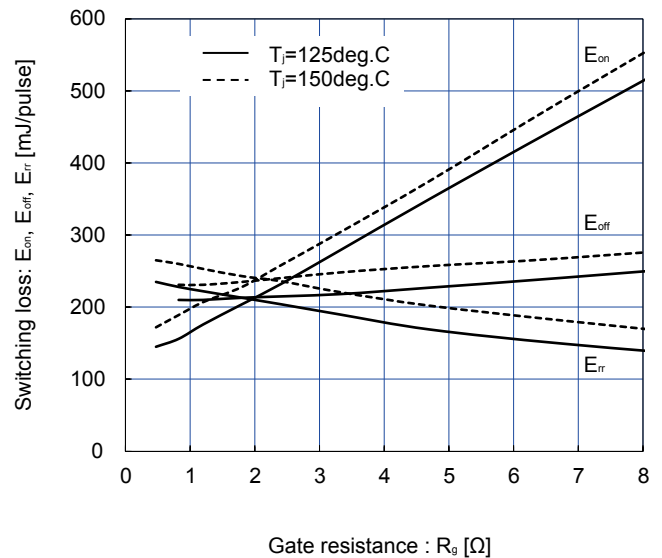
Switching time vs. Gate resistance (typ.)
 $V_{CC}=900V, V_C=600A, V_{GE}=\pm 15V$



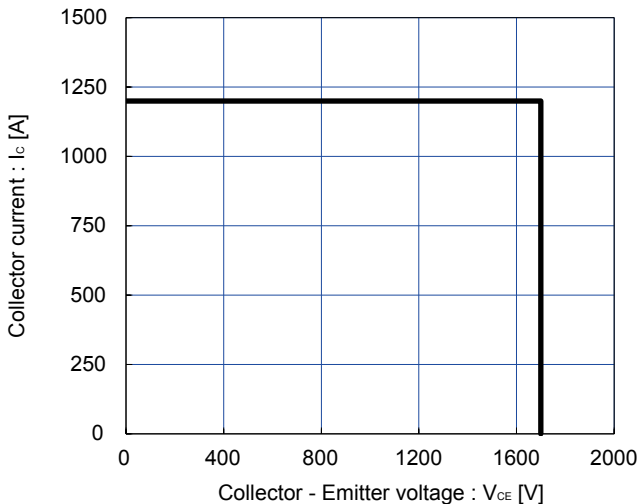
Switching loss vs. Collector current (typ.)
 $V_{CC}=900V, V_{GE}=\pm 15V, R_{gon}=0.47\Omega, R_{goff}=0.82\Omega$



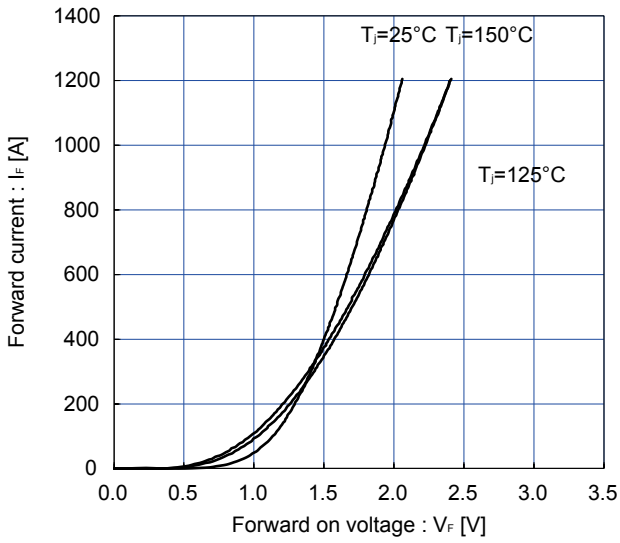
Switching loss vs. Gate resistance (typ.)
 $V_{CC}=900V, Ic=600A, V_{GE}=\pm 15V$



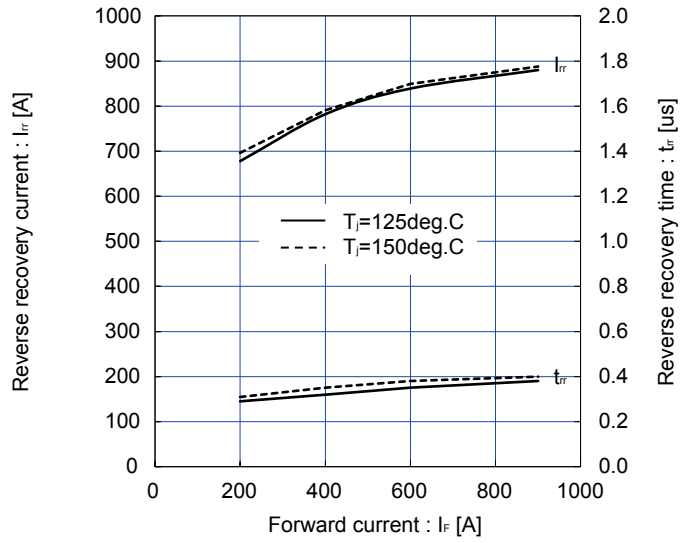
Reverse bias safe operating area (max.)
 $\pm V_{GE}=15V, Tj=150^\circ C / \text{chip}$



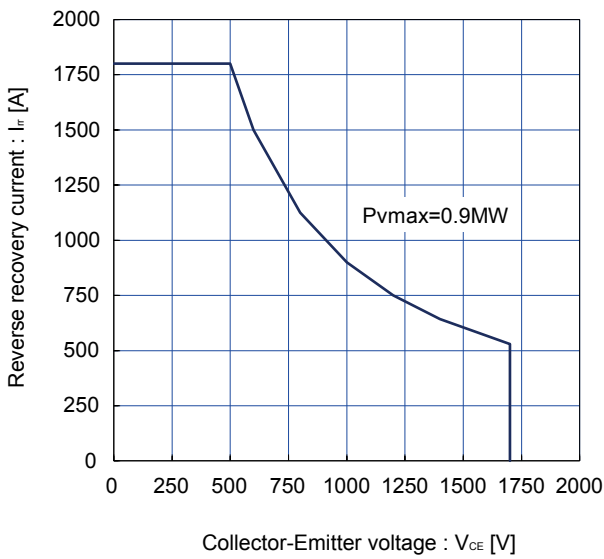
Forward current vs. Forward on voltage (typ.) chip



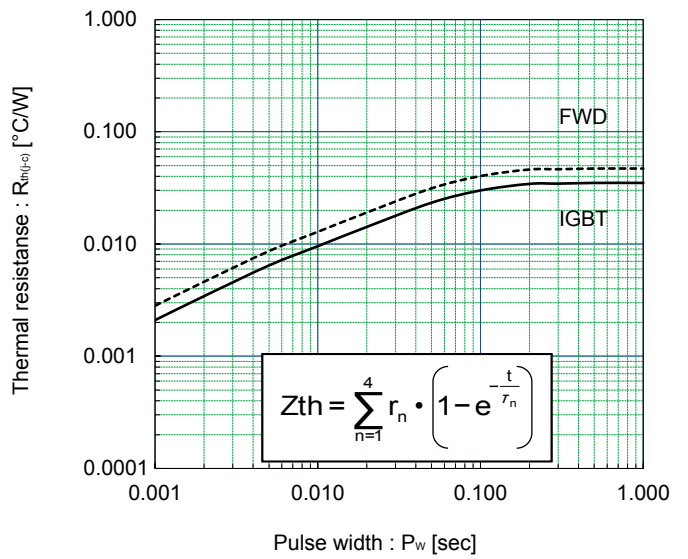
Reverse recovery characteristics (typ.)
V_{CC}=900V, V_{GE}=±15V, R_{gon}=0.47Ω



FWD safe operating area (max.)
T_j=150°C / sence terminals

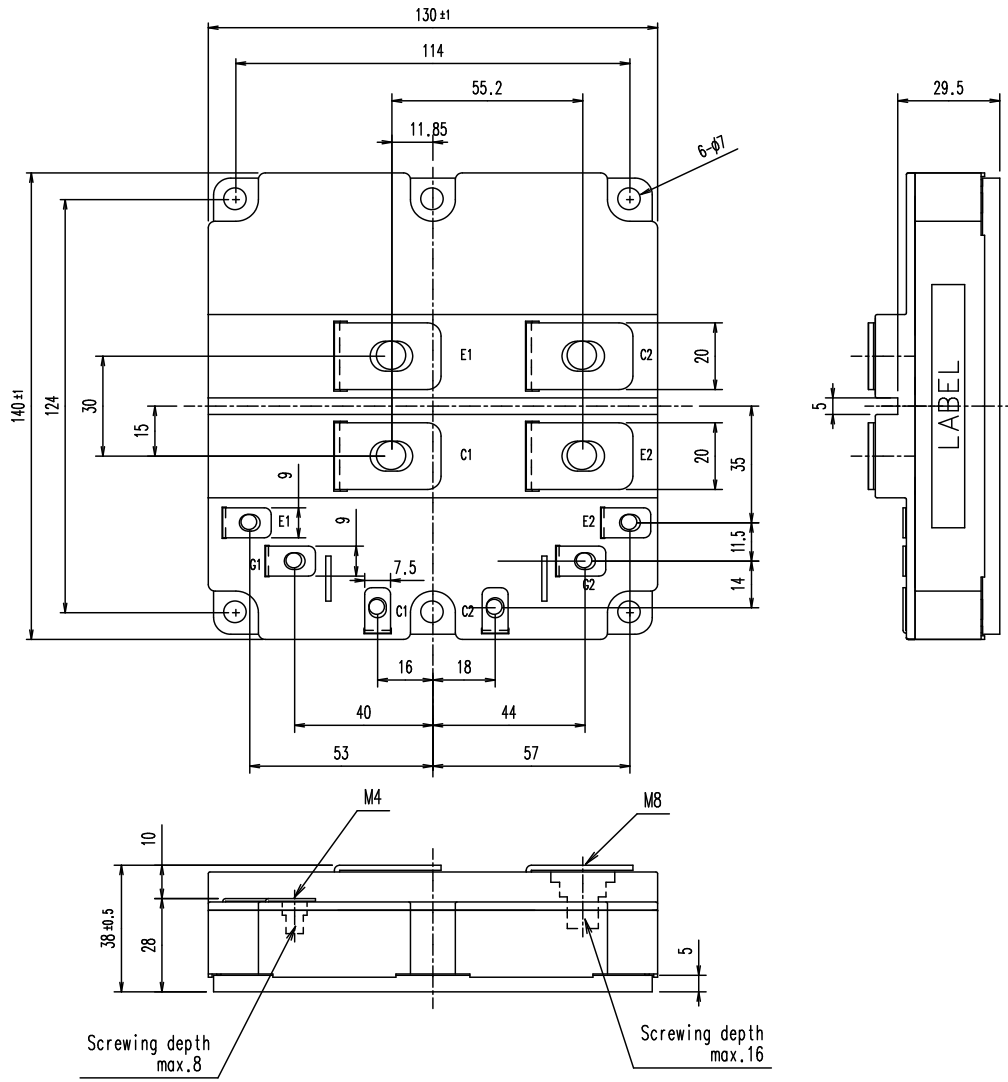


Transient thermal resistance (max.)

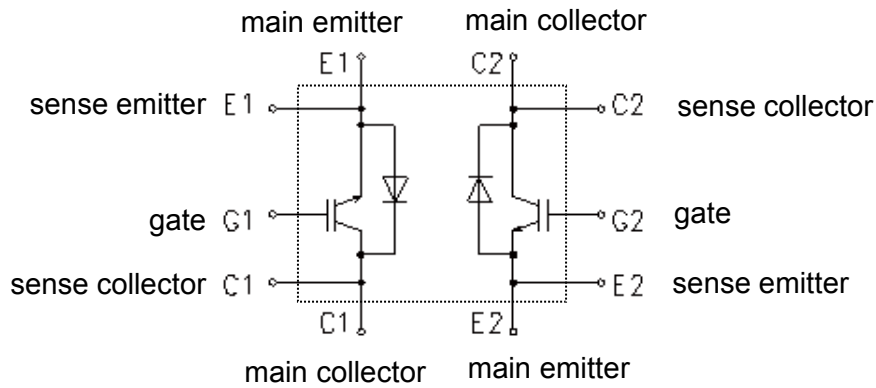


	IGBT	FWD
r1	0.00386	0.00518
r1	0.01347	0.01800
r3	0.00966	0.01295
r4	0.00801	0.01087
t1	0.0023	0.0023
t2	0.0352	0.0350
t3	0.0656	0.0668
t4	0.0712	0.0696

■ Outline Drawing (Unit : mm)



■ Equivalent circuit



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