

2N1131 (SILICON)

2N1131JAN AVAILABLE

2N1131A

2N1991

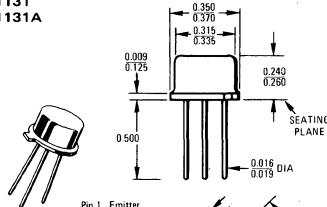
PNP SILICON ANNULAR TRANSISTORS

. . . designed for medium-speed switching and amplifier applications where low DC current gain is essential.

- Low DC Current Gain – $hFE = 45$ (Max) @ $I_C = 150$ mAdc – 2N1131,A
- Turn-On Time – $t_{on} = 45$ ns (Max) – 2N1131A
- Turn-Off Time – $t_{off} = 35$ ns (Max) – 2N1131A

PNP SILICON AMPLIFIER AND SWITCHING TRANSISTORS

2N1131
2N1131A

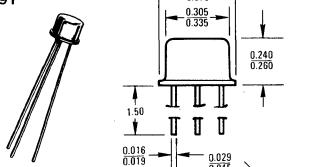


CASE 79 (1)
TO-39

Collector Connected
to Case

To convert inches to millimeters multiply by 25.4.
All JEDEC TO-39 dimensions and notes apply.

2N1991



CASE 31 (1)
TO-5

Pin 1. Emitter
2. Base
3. Collector

0.100 T.P. 0.100 T.P.

0.200 T.P. 0.200 T.P.

Weight ~ 1.15 gram

To convert inches to millimeters multiply by 25.4.

All JEDEC TO-5 dimensions and notes apply.

*MAXIMUM RATINGS

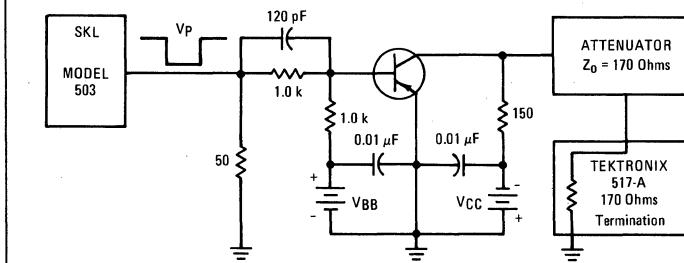
Rating	Symbol	2N1131	2N1131A	2N1991	Unit
Collector-Emitter Voltage	V_{CEO}	35	40	20	Vdc
Collector-Emitter Voltage	V_{CER}	50	50	—	Vdc
Collector-Base Voltage	V_{CB}	50	60	30	Vdc
Emitter-Base Voltage	V_{EB}	5.0	5.0	5.0	Vdc
Collector Current – Continuous	I_C	600	600	600	mAdc
Total Device Dissipation @ $T_A = 25^\circ\text{C}$ Derate above 25°C	P_D	0.6 4.0	0.6 4.0	0.6 4.8	Watt mW/ $^\circ\text{C}$
Total Device Dissipation @ $T_C = 25^\circ\text{C}$ @ $T_C = 100^\circ\text{C}$ Derate above 25°C	P_D	2.0 1.0 13.3	2.0 1.0 13.3	2.0 1.0 16	Watts mW/ $^\circ\text{C}$
Operating Junction Temperature Range	T_J	175	175	150	$^\circ\text{C}$
Storage Temperature Range	T_{stg}	-65 to +200		-65 to +150	$^\circ\text{C}$

THERMAL CHARACTERISTICS

Characteristic	Symbol	2N1131,A	2N1991	Unit
Thermal Resistance, Junction to Case	θ_{JC}	75	62.5	$^\circ\text{C/W}$
Thermal Resistance, Junction to Ambient	θ_{JA}	250	208	$^\circ\text{C/W}$

*Indicates JEDEC Registered Data.

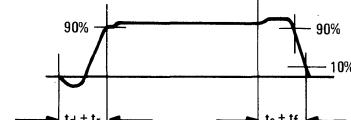
FIGURE 1 – SWITCHING TIME TEST CIRCUIT – 2N1131A



CONDITIONS:

$V_{CC} = -15$ Volts $V_p = -7.5$ Volts

$V_{BB} = 1.5$ Volts Pulse Width = 150 ns



2N1131, 2N1131A, 2N1991 (continued)

*ELECTRICAL CHARACTERISTICS ($T_A = 25^\circ\text{C}$ unless otherwise noted)

Characteristic		Symbol	Min	Max	Unit
OFF CHARACTERISTICS					
Collector-Emitter Sustaining Voltage(1) ($I_C = 100 \text{ mA}_\text{dc}, I_B = 0$)	2N1131 2N1131A 2N1991	$V_{CEO}(\text{sus})$	35 40 20	— — —	Vdc
Collector-Emitter Sustaining Voltage(1) ($I_C = 100 \text{ mA}_\text{dc}, R_{BE} \leq 10 \text{ ohms}$)	2N1131, 2N1131A	$V_{CE(\text{sus})}$	50	—	Vdc
Collector-Base Breakdown Voltage ($I_C = 100 \mu\text{A}_\text{dc}, I_E = 0$) ($I_C = 1.0 \text{ mA}_\text{dc}, I_E = 0$)	2N1131A 2N1991	BV_{CBO}	60 30	— —	Vdc
Emitter-Base Breakdown Voltage ($I_E = 1.0 \text{ mA}_\text{dc}, I_C = 0$)	2N1131A	BV_{EBO}	5.0	—	Vdc
Collector Cutoff Current ($V_{CB} = 30 \text{ Vdc}, I_E = 0$) ($V_{CB} = 30 \text{ Vdc}, I_E = 0, T_A = +150^\circ\text{C}$) ($V_{CB} = 50 \text{ Vdc}, I_E = 0$) ($V_{CB} = 45 \text{ Vdc}, I_E = 0$) ($V_{CB} = 45 \text{ Vdc}, I_E = 0, T_A = +150^\circ\text{C}$) ($V_{CB} = 10 \text{ Vdc}, I_E = 0$) ($V_{CB} = 10 \text{ Vdc}, I_E = 0, T_A = +150^\circ\text{C}$)	2N1131 2N1131 2N1131 2N1131A 2N1131A 2N1991 2N1991	I_{CBO}	— — — — — — —	1.0 100 100 0.5 50 5.0 200	μA_dc
Emitter Cutoff Current ($V_{BE} = 2.0 \text{ Vdc}, I_C = 0$) ($V_{BE} = 5.0 \text{ Vdc}, I_C = 0$) ($V_{BE} = 1.0 \text{ Vdc}, I_C = 0$)	2N1131 2N1131A 2N1991	I_{EBO}	— — —	100 100 200	μA_dc
ON CHARACTERISTICS					
DC Current Gain ($I_C = 5.0 \text{ mA}_\text{dc}, V_{CE} = 10 \text{ Vdc}$) ($I_C = 30 \text{ mA}_\text{dc}, V_{CE} = 10 \text{ Vdc}$) ($I_C = 150 \text{ mA}_\text{dc}, V_{CE} = 10 \text{ Vdc}$)	2N1131, 2N1131A 2N1991 2N1131, 2N1131A 2N1991	h_{FE}	15 15 20 15	— — 45 60	—
Collector-Emitter Saturation Voltage ($I_C = 150 \text{ mA}_\text{dc}, I_B = 15 \text{ mA}_\text{dc}$)		$V_{CE(\text{sat})}$	—	1.5	Vdc
Base-Emitter Saturation Voltage ($I_C = 150 \text{ mA}_\text{dc}, I_B = 15 \text{ mA}_\text{dc}$)	2N1131, 2N1131A 2N1991	$V_{BE(\text{sat})}$	— —	1.3 1.5	Vdc
SMALL-SIGNAL CHARACTERISTICS					
Current-Gain-Bandwidth Product(2) ($I_C = 50 \text{ mA}_\text{dc}, V_{CE} = 10 \text{ Vdc}, f = 20 \text{ MHz}$)	2N1131, 2N1131A 2N1991	f_T	50 40	— —	MHz
Output Capacitance ($V_{CB} = 10 \text{ Vdc}, I_E = 0, f = 140 \text{ kHz}$) ($V_{CB} = 10 \text{ Vdc}, I_E = 0, f = 1.0 \text{ MHz}$)	2N1131, 2N1991 2N1131A	C_{ob}	— —	45 30	pF
Input Capacitance ($V_{EB} = 0.5 \text{ Vdc}, I_C = 0, f = 140 \text{ kHz}$) ($V_{EB} = 0.5 \text{ Vdc}, I_C = 0, f = 1.0 \text{ MHz}$)	2N1131 2N1131A	C_{ib}	— —	80 80	pF
Input Impedance ($I_C = 1.0 \text{ mA}_\text{dc}, V_{CE} = 5.0 \text{ Vdc}, f = 1.0 \text{ kHz}$) ($I_C = 5.0 \text{ mA}_\text{dc}, V_{CE} = 10 \text{ Vdc}, f = 1.0 \text{ kHz}$)	2N1131, 2N1131A 2N1131, 2N1131A	h_{ib}	25 —	35 10	ohms
Voltage Feedback Ratio ($I_C = 1.0 \text{ mA}_\text{dc}, V_{CE} = 5.0 \text{ Vdc}, f = 1.0 \text{ kHz}$) ($I_C = 5.0 \text{ mA}_\text{dc}, V_{CE} = 10 \text{ Vdc}, f = 1.0 \text{ kHz}$)	2N1131, 2N1131A 2N1131, 2N1131A	h_{rb}	— —	8.0 8.0	$\times 10^{-4}$
Small-Signal Current Gain ($I_C = 1.0 \text{ mA}_\text{dc}, V_{CE} = 5.0 \text{ Vdc}, f = 1.0 \text{ kHz}$) ($I_C = 5.0 \text{ mA}_\text{dc}, V_{CE} = 10 \text{ Vdc}, f = 1.0 \text{ kHz}$)	2N1131, 2N1131A 2N1131, 2N1131A	h_{fe}	15 20	50 —	—
Output Admittance ($I_C = 1.0 \text{ mA}_\text{dc}, V_{CE} = 5.0 \text{ Vdc}, f = 1.0 \text{ kHz}$) ($I_C = 5.0 \text{ mA}_\text{dc}, V_{CE} = 10 \text{ Vdc}, f = 1.0 \text{ kHz}$)	2N1131, 2N1131A 2N1131, 2N1131A	h_{ob}	— —	1.0 5.0	μmhos
SWITCHING CHARACTERISTICS (Figure 1)					
Turn-On Time	2N1131A	t_{on}	—	45	ns
Turn-Off Time	2N1131A	t_{off}	—	35	ns

* Indicates JEDEC Registered Data.

(1)Pulse Test: Pulse Width $\leq 300 \mu\text{s}$, Duty Cycle $\leq 2.0\%$.

(2) f_T is defined as the frequency at which $|h_{fe}|$ extrapolates to unity.