

**MAXIMUM RATINGS**

Rating	Symbol	2N1132	2N1132A	Unit
Collector-Emitter Voltage	V <sub>CEO</sub>	35	40	Vdc
Collector-Emitter Voltage (R <sub>BE</sub> ≤ 10 Ohms)	V <sub>CER</sub>	← 50 →		Vdc
Collector-Base Voltage	V <sub>CBO</sub>	50	60	Vdc
Emitter-Base Voltage	V <sub>EBO</sub>	← 5.0 →		Vdc
Collector Current — Continuous	I <sub>C</sub>	← 600 →		mA
Total Device Dissipation @ T <sub>A</sub> = 25°C Derate above 25°C	P <sub>D</sub>	← 600 → ← 3.43 →		mW mW/°C
Total Device Dissipation @ T <sub>C</sub> = 25°C Derate above 25°C	P <sub>D</sub>	← 2.0 → ← 11.43 →		Watts mW/°C
Total Device Dissipation @ T <sub>C</sub> = 100°C 2N1132A	P <sub>D</sub>	← 1.0 →		Watts
Operating and Storage Junction Temperature Range	T <sub>J</sub> , T <sub>stg</sub>	- 65 to +200		°C

**2N1132,A**

**JAN AVAILABLE  
CASE 79-02, STYLE 1  
TO-39 (TO-205AD)**

**SWITCHING TRANSISTOR**

**PNP SILICON**

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**THERMAL CHARACTERISTICS**

Characteristic	Symbol	Max	Unit
Thermal Resistance, Junction to Case	R <sub>θJC</sub>	87.49	°C/W
Thermal Resistance, Junction to Ambient	R <sub>θJA</sub>	291.55	°C/W

Refer to 2N2904 for graphs.

**ELECTRICAL CHARACTERISTICS (T<sub>A</sub> = 25°C unless otherwise noted.)**

Characteristic	Symbol	Min	Max	Unit
<b>OFF CHARACTERISTICS</b>				
Collector-Emitter Breakdown Voltage (I <sub>C</sub> = 10 mA)	2N1132A 2N1132	V <sub>(BR)CEO</sub>	40 35	— — Vdc
Collector-Base Breakdown Voltage (I <sub>C</sub> = 100 μAdc, I <sub>E</sub> = 0)	2N1132, 2N1132A	V <sub>(BR)CBO</sub>	50 60	— — Vdc
Emitter-Base Breakdown Voltage (I <sub>E</sub> = 100 μAdc, I <sub>C</sub> = 0) (I <sub>E</sub> = 1.0 mA, I <sub>C</sub> = 0)	2N1132, 2N1132A	V <sub>(BR)EBO</sub>	5.0 5.0	— — Vdc
Collector Cutoff Current (V <sub>CB</sub> = 30 Vdc, I <sub>E</sub> = 0) (V <sub>CB</sub> = 50 Vdc, I <sub>E</sub> = 0) (V <sub>CB</sub> = 30 Vdc, I <sub>E</sub> = 0, T <sub>A</sub> = 150°C) (V <sub>CB</sub> = 45 Vdc, I <sub>E</sub> = 0) (V <sub>CB</sub> = 45 Vdc, I <sub>E</sub> = 0, T <sub>A</sub> = 150°C)	2N1132 2N1132 2N1132 2N1132A 2N1132A	I <sub>CBO</sub>	— — — — —	1.0 100 100 0.5 50 μAdc
Collector Cutoff Current (V <sub>CE</sub> = 50 V, R <sub>BE</sub> = ≤ 10 Ohms)	2N1132 2N1132A	I <sub>CER</sub>	— —	10 10 mA mA
Emitter Cutoff Current (V <sub>BE</sub> = 5.0 Vdc, I <sub>C</sub> = 0) (V <sub>BE</sub> = 2.0 Vdc, I <sub>C</sub> = 0)	2N1132A 2N1132	I <sub>EBO</sub>	— —	100 100 μAdc
<b>ON CHARACTERISTICS(1)</b>				
DC Current Gain (I <sub>C</sub> = 5.0 mAdc, V <sub>CE</sub> = 10 Vdc) (I <sub>C</sub> = 150 mAdc, V <sub>CE</sub> = 10 Vdc)		h <sub>FE</sub>	25 30	— — —
Collector-Emitter Saturation Voltage (I <sub>C</sub> = 150 mAdc, I <sub>B</sub> = 15 mAdc)		V <sub>CE(sat)</sub>	—	1.5 Vdc
Base-Emitter Saturation Voltage (I <sub>C</sub> = 150 mAdc, I <sub>B</sub> = 15 mAdc)		V <sub>BE(sat)</sub>	—	1.3 Vdc

# 2N1132,A

## ELECTRICAL CHARACTERISTICS (continued) ( $T_A = 25^\circ\text{C}$ unless otherwise noted.)

Characteristic	Symbol	Min	Max	Unit
<b>SMALL-SIGNAL CHARACTERISTICS</b>				
Current-Gain — Bandwidth Product ( $I_C = 5.0 \text{ mA dc}$ , $V_{CE} = 10 \text{ V dc}$ , $f = 20 \text{ MHz}$ )	$f_T$	60	—	MHz
Output Capacitance ( $V_{CB} = 10 \text{ V dc}$ , $I_E = 0$ , $f = 1.0 \text{ MHz}$ ) ( $V_{CB} = 10 \text{ V dc}$ , $I_E = 0$ , $f = 1.0 \text{ MHz}$ )	$C_{obo}$	— —	45 30	pF
Input Capacitance ( $V_{BE} = 0.5 \text{ V dc}$ , $I_C = 0$ , $f = 1.0 \text{ kHz}$ ) ( $V_{BE} = 0.5 \text{ V dc}$ , $I_C = 0$ , $f = 1.0 \text{ MHz}$ )	$C_{ibo}$	— —	80 80	pF
Input Impedance ( $I_C = 1.0 \text{ mA dc}$ , $V_{CB} = 5.0 \text{ V dc}$ , $f = 1.0 \text{ kHz}$ ) ( $I_C = 5.0 \text{ mA dc}$ , $V_{CB} = 10 \text{ V dc}$ , $f = 1.0 \text{ kHz}$ )	$h_{ib}$	25 —	35 10	Ohms
Voltage Feedback Ratio ( $I_C = 5.0 \text{ mA dc}$ , $V_{CE} = 5.0 \text{ V dc}$ , $f = 1.0 \text{ kHz}$ ) ( $I_C = 5.0 \text{ mA dc}$ , $V_{CE} = 10 \text{ V dc}$ , $f = 1.0 \text{ kHz}$ )	$h_{rb}$	— —	8.0 8.0	$\times 10^{-4}$
Small-Signal Current Gain ( $I_C = 1.0 \text{ mA dc}$ , $V_{CE} = 5.0 \text{ V dc}$ , $f = 1.0 \text{ kHz}$ )  ( $I_C = 5.0 \text{ mA dc}$ , $V_{CE} = 10 \text{ V dc}$ , $f = 1.0 \text{ kHz}$ )	$h_{fe}$	25 25	100 75	—
Output Admittance ( $I_C = 1.0 \text{ mA dc}$ , $V_{CE} = 5.0 \text{ V dc}$ , $f = 1.0 \text{ kHz}$ ) ( $I_C = 5.0 \text{ mA dc}$ , $V_{CE} = 10 \text{ V dc}$ , $f = 1.0 \text{ kHz}$ )	$h_{ob}$	— —	1.0 5.0	$\mu\text{hos}$
<b>SWITCHING CHARACTERISTICS</b>				
Turn-On Time	2N1132A	$t_{on}$	—	45 ns
Turn-Off Time	2N1132A	$t_{off}$	—	35 ns

(1) Pulse Test: Pulse Width  $\leq 300 \mu\text{s}$ , Duty Cycle  $\leq 2.0\%$ .

FIGURE 1 SWITCHING TIMES TEST CIRCUIT

