NPN Medium Power Silicon Transistor

FeaturesAvailable

- Available in JAN Quality Level per MIL-PRF-19500/207
- TO-205AA (TO-5) and TO-205AD (TO-39) Packages
- "S" Suffix Denotes TO-205AD (TO-39) Package Style
- General Purpose Transistors for Medium Power Applications Requiring High Frequency Switching

Electrical Characteristics (25°C unless otherwise specified)

Parameter	Test Conditions	Symbol	Units	Min.	Max.
Collector - Emitter Breakdown Voltage	I _E = 50 mA dc 2N1479, 2N1481 2N1480, 2N1482	2N1479, 2N1481 V _{(BR)CEO}		40 50	_
Collector - Emitter Breakdown Voltage	I _C = 0.25 mA dc, V _{EB} = 1.5V dc 2N1479, 2N1481 2N1480, 2N1482		V dc	60 100	
Emitter - Base Cutoff Current	V _{EB} = 12 V dc		µA dc	_	10
Collector - Emitter Cutoff Current	V _{CE} = 60 Vdc	I _{CEO} I _{CEO}	µA dc	_	10
Collector - Base Cutoff Current	V_{CB} = 30 V dc 2N1479, 2N1481 V_{CB} = 50 V dc 2N1480, 2N1482	I _{CBO1}	µA dc	_	5 5
Forward-Current Transfer Ratio	I _C = 200 mA dc; V _{CE} = 4 V dc 2N1479, 2N1480 2N1481, 2N1482			20 35	60 100
Collector - Base Cutoff Current	T _A = 150°C V _{CB} = 30 V dc 2N1479, 2N1481 V _{CB} = 50 V dc 2N1480, 2N1482	I _{CBO2}	mA dc	_	0.75 0.75
Forward-Current Transfer Ratio	T _A = -55°C I _C = 200 mA dc, V _{CE} = 4 V dc 2N1479, 2N1480 2N1481, 2N1482	h _{FE2}		10 17	_
Collector-Emitter Saturation Voltage	$I_{\rm C}$ = 200 mA dc $I_{\rm B}$ = 20 mA dc, 2N1479, 2N1480 $I_{\rm B}$ = 10 mA dc, 2N1481, 2N1482	V _{CE(SAT)}	V dc	_	0.75 0.75
Base-Emitter Voltage (unsaturated)	$I_{\rm C}$ = 200 mA dc, $V_{\rm CE}$ = 4 V dc	$V_{\text{BE(on)}}$	V dc	_	1.5
Pulse Response	$\begin{array}{l} {\sf R}_{\sf C} = 59 \; \Omega, \; {\sf V}_{\sf CC} = 12 \; {\sf V} \; dc, \\ {\sf I}_{\sf B(0)} = {\sf I}_{\sf B(2)} = 8.5 \; m{\sf A} \; dc \\ {\sf I}_{\sf B(1)} = 20 \; m{\sf A} \; dc \end{array}$	t _{on} + t _{off}	μs	_	25
Small-Signal, Short-Circuit, Forward-Current Transfer Ratio Cut Off Frequency	$I_{\rm C}$ = 5 mA dc, $V_{\rm CB}$ = 28 V dc	f _{ab}	kHz	800	_

1

VPT Components and its affiliates reserve the right to make changes to the product(s) or information contained herein without notice. Visit <u>www.vptcomponents.com</u> for additional data sheets and product information.



Rev. V3





NPN Medium Power Silicon Transistor

Rev. V3

Absolute Maximum Ratings (25°C unless otherwise specified)

Ratings	Symbol	Value
Collector - Emitter Voltage 2N1479, 2N1481 2N1480, 2N1482	V _{CEO}	40 V dc 55 V dc
Collector - Emitter Voltage 2N1479, 2N1481 2N1480, 2N1482	V _{CEX}	60 V dc 100 V dc
Collector - Base Voltage 2N1479, 2N1481 2N1480, 2N1482	V _{CBO}	60 V dc 100 V dc
Emitter - Base Voltage	V_{EBO}	12 V dc
Collector Current	Ι _C	1.5 A dc
Base Current	I _B	1.0 A dc
Total Power Dissipation @ $T_A = +25^{\circ}C$	P _T ⁽¹⁾	1.0 W
Operating & Storage Temperature Range	T _{OP} , T _{STG}	-65°C to +200°C

(1) This power dissipation is for 1,000 hours expected life at $T_A = +25^{\circ}C$.

Thermal Characteristics

Characteristics	Symbol	Max. Value
Thermal Resistance Junction to Case	$R_{ extsf{ heta}JC}$	35°C/W

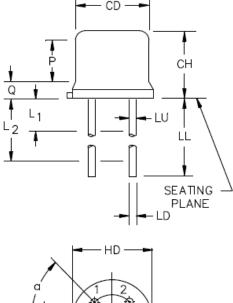


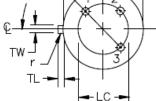
NPN Medium Power Silicon Transistor

Rev. V3

Outline Drawing: TO-205AA, TO-205AD Package Types

Dimensions					
Symbol	Inches		Millimeters		Note
	Min	Мах	Min	Max	
CD	.305	.335	7.75	8.51	3
CH	.240	.260	6.10	6.60	
HD	.335	.370	8.51	9.40	
LC	.200	TP	5.08 TP		4
LD	.016	.019	0.41	0.48	5, 6
LL	See notes 6, 7, and 8				
LU	.016	.019	0.41	0.48	5, 6
L ₁		.050		1.27	5, 6
L ₂	.250		6.35		5, 6
P	.100		2.54		9
Q		.030		0.76	3
TL	.029	.045	0.74	1.14	10, 11
TW	.028	.034	0.71	0.86	10
r		.010		0.25	12
α	45°	45° TP		45° TP	





NOTES:

- 1. Dimensions are in inches. Millimeters are given for general information only.
- 2. Lead 1 = emitter, lead 2 = base, lead 3 = collector. The collector shall be internally connected to the case.
- 3. CD shall not vary more than .010 inch (0.25 mm) in zone P. This zone is controlled for automatic handling.
- 4. Leads at gauge plane .054 +.001 -.000 inch (1.37 +0.03 -0.00 mm) below seating plane shall be within .007 inch (0.18 mm) radius of true position (TP) at maximum material condition (MMC) relative to tab at MMC. The device may be measured by direct methods or by gauging procedure.
- Dimension LU applies between L₁ and L₂. Dimension LD applies between L₂ and LL minimum. Diameter is uncontrolled in and beyond LL minimum.
- 6. All three leads.
- For the modified TO-205AA (formerly TO-5) package (PINs without the S suffix), dimension LL is 1.500 inch (38.10 mm) minimum and 1.750 inch (44.45 mm) maximum.
- For the modified TO-205AD (formerly TO-39) package (PINs with the S suffix), dimension LL is .500 inch (12.70 mm) minimum and .750 inch (19.05 mm) maximum.
- 9. Body contour optional within zone defined by dimensions HD, CD, and Q.
- 10. Beyond r (radius) maximum, dimension TW shall be held for a minimum length of .011 (0.28 mm).
- 11. Dimension TL measured from maximum HD.
- 12. Dimension r (radius) applies to both inside corners of tab.
- 13. In accordance with ASME Y14.5M, diameters are equivalent to Φx symbology.

FIGURE 1. Physical dimensions and configuration of TO-205AA and TO-205AD package.

3

VPT Components and its affiliates reserve the right to make changes to the product(s) or information contained herein without notice. Visit <u>www.vptcomponents.com</u> for additional data sheets and product information.

NPN Medium Power Silicon Transistor



Rev. V3

VPT COMPONENTS. ALL RIGHTS RESERVED.

Information in this document is provided in connection with VPT Components products. These materials are provided by VPT Components as a service to its customers and may be used for informational purposes only. Except as provided in VPT Components Terms and Conditions of Sale for such products or in any separate agreement related to this document, VPT Components assumes no liability whatsoever. VPT Components assumes no responsibility for errors or omissions in these materials. VPT Components may make changes to specifications and product descriptions at any time, without notice. VPT Components makes no commitment to update the information and shall have no responsibility whatsoever for conflicts or incompatibilities arising from future changes to its specifications and product descriptions. No license, express or implied, by estoppels or otherwise, to any intellectual property rights is granted by this document.

THESE MATERIALS ARE PROVIDED "AS IS" WITHOUT WARRANTY OF ANY KIND, EITHER EXPRESS OR IMPLIED, RELATING TO SALE AND/OR USE OF VPT COMPONENTS PRODUCTS INCLUDING LIABILITY OR WARRANTIES RELATING TO FITNESS FOR A PARTICULAR PURPOSE, CONSEQUENTIAL OR INCI-DENTAL DAMAGES, MERCHANTABILITY, OR INFRINGEMENT OF ANY PATENT, COPYRIGHT OR OTHER INTELLECTUAL PROPERTY RIGHT. VPT COMPONENTS FURTHER DOES NOT WARRANT THE ACCURA-CY OR COMPLETENESS OF THE INFORMATION, TEXT, GRAPHICS OR OTHER ITEMS CON-TAINED WITHIN THESE MATERIALS. VPT COMPONENTS SHALL NOT BE LIABLE FOR ANY SPECIAL, IN-DIRECT, INCIDENTAL, OR CONSEQUENTIAL DAMAGES, INCLUDING WITHOUT LIMITATION, LOST REVE-NUES OR LOST PROFITS, WHICH MAY RESULT FROM THE USE OF THESE MATERIALS.

VPT Components products are not intended for use in medical, lifesaving or life sustaining applications. VPT Components customers using or selling VPT Components products for use in such applications do so at their own risk and agree to fully indemnify VPT Components for any damages resulting from such improper use or sale.

⁴

VPT Components and its affiliates reserve the right to make changes to the product(s) or information contained herein without notice. Visit <u>www.vptcomponents.com</u> for additional data sheets and product information.