

# 2N1595 thru 2N1599 (SILICON)



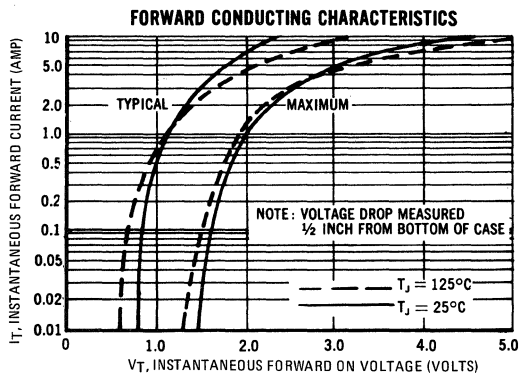
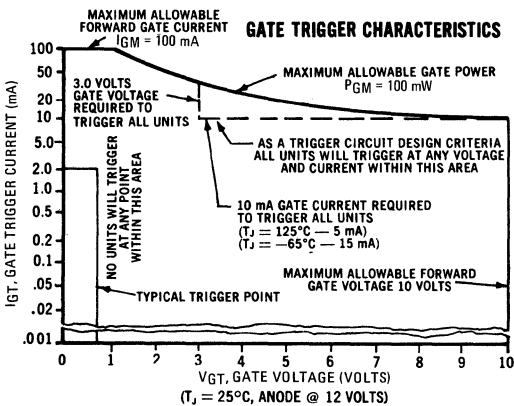
Industrial-type, low-current silicon controlled rectifiers in a three-lead package ideal for printed-circuit applications. Current handling capability of 1.6 amperes at junction temperatures to 125°C.

**CASE 31(2)**  
(TO-5)

## MAXIMUM RATINGS (T<sub>J</sub> = 125°C unless otherwise noted)

Rating	Symbol	Value	Unit
Peak Reverse Blocking Voltage*	V <sub>RSM(rep)</sub> *	2N1595: 50 2N1596: 100 2N1597: 200 2N1598: 300 2N1599: 400	Volts
Forward Current RMS (All Conduction Angles)	I <sub>T(RMS)</sub>	1.6	Amp
Peak Forward Surge Current (One Cycle, 60 Hz, T <sub>J</sub> = -65 to +125°C)	I <sub>TSM</sub>	15	Amp
Peak Gate Power - Forward	P <sub>GM</sub>	0.1	Watt
Average Gate Power - Forward	P <sub>G(AV)</sub>	0.01	Watt
Peak Gate Current - Forward	I <sub>GM</sub>	0.1	Amp
Peak Gate Voltage - Forward	V <sub>GF(M)</sub>	10	Volts
Reverse	V <sub>GR(M)</sub>	10	Volts
Operating Junction Temperature Range	T <sub>J</sub>	-65 to +125	°C
Storage Temperature Range	T <sub>stg</sub>	-65 to +150	°C

\*V<sub>RSM</sub> for all types can be applied on a continuous dc basis without incurring damage.



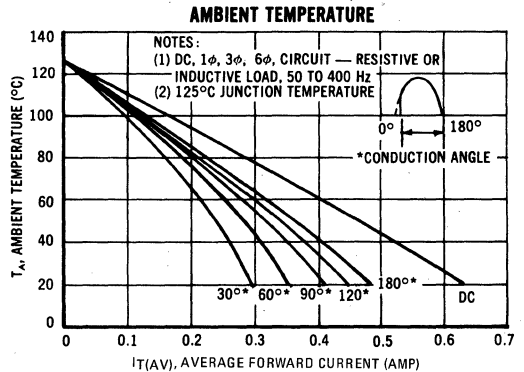
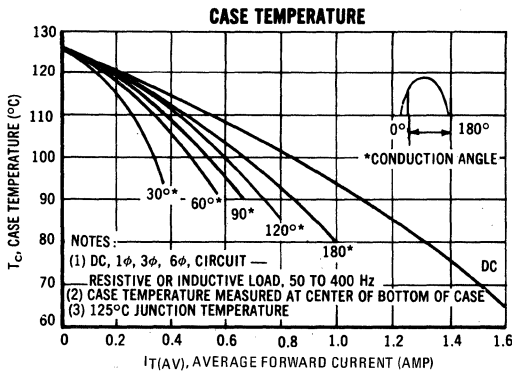
**2N1595 thru 2N1599 (continued)**

**ELECTRICAL CHARACTERISTICS** ( $T_C = 25^\circ\text{C}$  unless otherwise noted)

Characteristic	Symbol	Min	Typ	Max	Units
Peak Forward Blocking Voltage* ( $T_J = 125^\circ\text{C}$ )	$V_{DRM}^*$	50 100 200 300 400	—	—	Volts
Peak Forward Blocking Current (Rated $V_{DRM}$ with gate open, $T_J = 125^\circ\text{C}$ )	$I_{DRM}$	—	—	1.0	mA
Peak Reverse Blocking Current (Rated $V_{RSM}$ , $T_J = 125^\circ\text{C}$ )	$I_{RRM}$	—	—	1.0	mA
Gate Trigger Current (Continuous dc) (Anode Voltage = 7 Vdc, $R_L = 12 \Omega$ )	$I_{GT}$	—	2.0	10.0	mA
Gate Trigger Voltage (Continuous dc) (Anode Voltage = 7 Vdc, $R_L = 12 \Omega$ ) ( $V_{DRM} = \text{Rated}$ , $R_L = 12 \Omega$ , $T_J = 125^\circ\text{C}$ )	$V_{GT}$ $V_{GNT}$	— 0.2	0.7 —	3.0 —	Volts
Holding Current (Anode Voltage = 7 Vdc, Gate Open)	$I_H$	—	5.0	—	mA
Forward On Voltage ( $I_T = 1 \text{ Adc}$ )	$V_{TM}$	—	1.1	2.0	Volts
Turn-On Time ( $t_d + t_r$ ) ( $I_{GT} = 10 \text{ mA}$ , $I_T = 1 \text{ A}$ )	$t_{gt}$	—	0.8	—	$\mu\text{s}$
Turn-Off Time ( $I_T = 1 \text{ A}$ , $I_R = 1 \text{ A}$ , $dv/dt = 20 \text{ V}/\mu\text{s}$ , $T_J = 125^\circ\text{C}$ ) ( $V_{DRM} = \text{rated voltage}$ )	$t_q$	—	10	—	$\mu\text{s}$

\* $V_{DRM}$  for all types can be applied on a continuous dc basis without incurring damage.

**CURRENT DERATING**



**2N1613 (SILICON)**

For Specifications, See 2N718A Data.