

### **isc Silicon NPN Power Transistor**

### **DESCRIPTION**

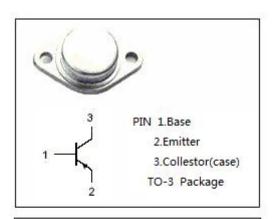
- · Collector-Emitter Breakdown Voltage-
- : V<sub>CEO</sub>=60V(Min)
- Minimum Lot-to-Lot variations for robust device Performance and reliable operation

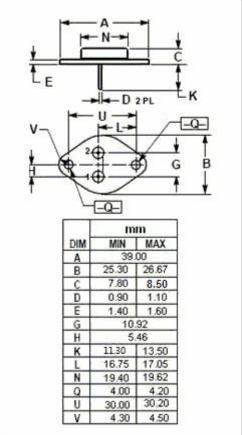
#### **APPLICATIONS**

Power amplifier and switching applications

## ABSOLUTE MAXIMUM RATINGS(Ta=25°C)

SYMBOL	PARAMETER	VALUE	UNI T
V <sub>CBO</sub>	Collector-Base Voltage	60	V
Vceo	Collector-Emitter Voltage	60	V
$V_{EBO}$	Emitter-Base Voltage	6	V
lc	Collector Current-Continuous	5	Α
P <sub>D</sub>	Collector Power Dissipation @ T <sub>C</sub> =25 °C 75		W
TJ	Junction Temperature	-65~200	$^{\circ}$
T <sub>stg</sub>	Storage Temperature Range	-65~200	$^{\circ}$ C







# isc Silicon NPN Power Transistor

2N1702

#### **ELECTRICAL CHARACTERISTICS**

Tc=25℃ unless otherwise specified

SYMBOL	PARAMETER	CONDITIONS	MIN	TYP.	MAX	UNIT
V(BR)CEO	Collector-Emitter Breakdown Voltage	I <sub>B</sub> =10uA	40			V
Iceo	Collector-Emitter Cutoff Current	V <sub>CE</sub> = 30V			40	uA
V <sub>CE</sub> (sat)	Collector-Emitter Saturation Voltage	I <sub>C</sub> =0.8A; I <sub>B</sub> = 0.08A			3.2	V
V <sub>BE</sub> (sat)	Base-Emitter Saturation Voltage	I <sub>C</sub> = 0.8A; I <sub>B</sub> = 0.08A			3.5	V
h <sub>FE-1</sub>	DC Current Gain	I <sub>C</sub> =0.8A; V <sub>CE</sub> =4V	15		60	

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