

2N1842-2N1850A TR1010-TR9010

SILICON CONTROLLED RECTIFIER

FEATURES

- Available as "HR" (high reliability) screened per MIL-PRF-19500, JANTX level. Add "HR" suffix to base part number.
- Available as non-RoHS (Sn/Pb plating), standard, and as RoHS by adding "-PBF" suffix.

MAXIMUM RATINGS

Rating	Symbol	Value	Unit
RMS on-state current @ T _c = 80°C	I _{T(RMS)}	16	А
Mean on-state current @ T _C = 80°C	I _{T(AV)}	10	А
Non-repetitive surge peak on-state current @ T₁≤ 125°C			
t = 8.3ms	I _{TSM}	157	Α
t = 10ms		150	
I2t for fusing @ T₁≤ 125°C, t = 10ms	I ² t	112.5	A ² s
Critical rate of rise of on-state current	di/dt	100	A/μs
Operating junction temperature range	T _J	-40 to +150	°C
Storage temperature range	T _{stg}	-40 to +125	°C

VOLTAGE RATINGS

T _J = 125°C	2N 1842(A)	2N 1843(A)	2N 1844(A)	2N 1846(A)	2N 1848(A)	2N 1849(A)	2N 1850(A)	TR 6010	TR 7010	TR 8010	TR 9010	TR 1010	TR 1110	TR 1210
125 C	VOLTS													
V _{DRM} = V _{RRM}	25	50	100	200	300	400	500	600	700	800	900	1000	1100	1200

THERMAL RESISTANCE

Thermal resistance	Symbol	Value	Unit	
Junction to case for DC	$R_{th(j-c)}$	2	°C/W	
Case to heatsink	R _{th(c-h)}	0.4	°C/W	

ELECTRICAL CHARACTERISTICS (T_A = 25°C unless otherwise specified)

Charactaristic	Cb. a.l	Value			Unit		Took conditions			
Characteristic	Symbol	Min	Тур	Max			Test conditions			
Gate trigger current	I _{GT}	-	-	80	mA	T _J = 25°C	V _D = 12V	$R_L = 33\Omega$	t _p ≥ 20μs	
Gate trigger voltage	V_{GT}	-	-	3	V	T _J = 25°C	V _D = 12V	$R_L = 33\Omega$	t _p ≥ 20μs	
Peak gate voltage	V_{GD}	0.25	-	-		T _J = 125°C	$V_D = V_{DRM}$	$R_L = 3.3\Omega$		
Holding current	I _H	-	20	-	mA	T _J = 25°C	I _T = 0.5A	Gate open		
Peak on-state voltage	V _{TM}	-	-	2.2	V	T _J = 25°C	I _{TM} = 30A	t _p = 10ms		
Maximum off-state current	I _{DRM}	-	-	5	mA	T _J = 125°C	V _{DRM} specified			
Maximum off-state current	I _{RRM}	-	-	5	mA	T _J = 125°C	V _{RRM} specified			
Turn on time	t _{gt}	-	2	-	μs	T _J = 25°C I _G = 200mA	$I_T = 30A$ $di_G/dt = 2A/\mu s$	$V_D = V_{DRM}$		
Turn off time	tq	-	100	-	μs	$T_J = 125$ °C $di_R/dt = 30A/\mu s$	I _T = 10A dv/dt = 20V/μs	V _R = 30V	$V_D = 0.67$ V_{DRM} Gate open	
Critical rise of off-state voltage	dv/dt	100	-	-	V/µs	T _J = 125°C Linear slope up to 0.67 V _{DRM} specified		specified		

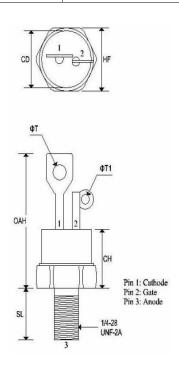


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MECHANICAL CHARACTERISTICS

Case	TO-48
Marking	Alpha-numeric
Polarity	Cathode



	TO-48							
	Inc	hes	Millimeters					
	Min	Max	Min	Max				
CD		0.543	2	13.793				
CH		0.550	-	13.970				
HF	0.544	0.563	13.817	14.301				
OAH	-	1.193	-	30.303				
SL	0.422	0.453	10.718	11.507				
ФТ	0.125	0.165	3.175	4.191				
ΦT ₁	0.060	0.075	1.524	1.905				



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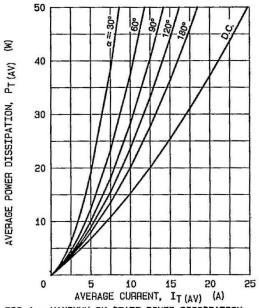


FIG.1 - MAXIMUM ON-STATE POWER DISSIPATION FOR SINUSOIDAL CURRENT WAVEFORM

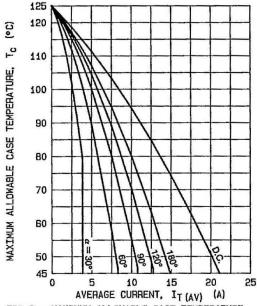


FIG.2 - MAXIMUM ALLOWABLE CASE TEMPERATURE FOR SINUSOIDAL CURRENT WAVEFORM

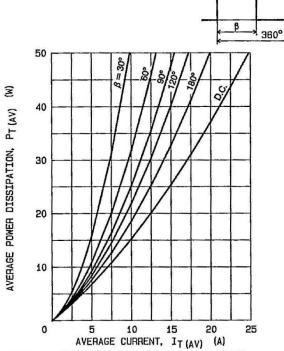


FIG.3 - MAXIMUM ON-STATE POWER DISSIPATION FOR RECTANGULAR CURRENT WAVEFORM

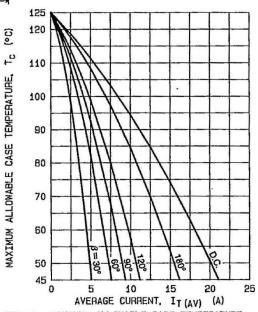


FIG.4 - MAXIMUM ALLOWABLE CASE TEMPERATURE FOR RECTANGULAR CURRENT WAVEFORM



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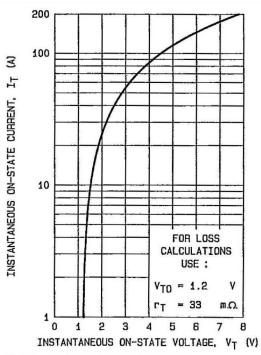


FIG.5 - MAXIMUM ON-STATE CONDUCTION CHARACTERISTIC ($T_J = 125.^{\circ}C$).

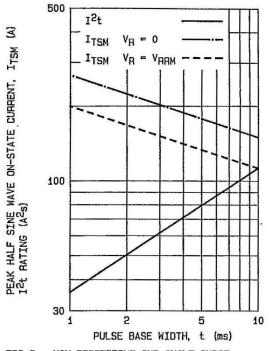


FIG.6 - NON REPETITIVE SUB-CYCLE SURGE ON-STATE CURRENT AND I²t RATING (INITIAL T_J = 125 °C).

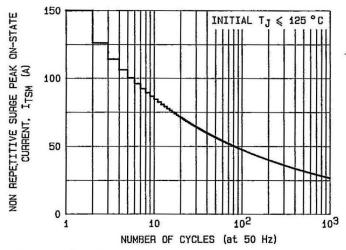


FIG.7 - NON REPETITIVE SURGE PEAK ON-STATE CURRENT VERSUS NUMBER OF CYCLES.



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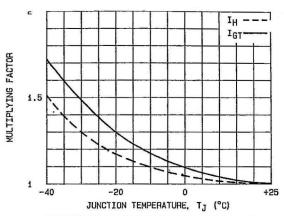


FIG.8 - RELATIVE VARIATION OF GATE TRIGGER CURRENT AND HOLDING CURRENT VERSUS JUNCTION TEMPERATURE.

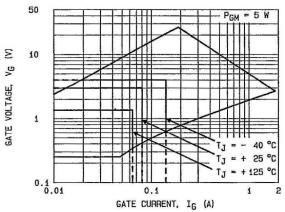
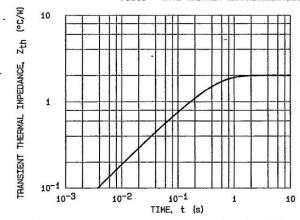


FIG.8 - GATE TRIGGER CHARACTERISTICS.



Conduction angle (α,β)	Effective thermal resistance (°C/W) junction to case					
	Sinusoidal	al Rectangular				
180°	2.23	2.18				
120°	2.31	3.09				
80°	2.47	3.50				
60°	2.88	3.91				
30°	3.71	4.94				

FIG. 10 - TRANSIENT THERMAL IMPEDANCE JUNCTION TO CASE.