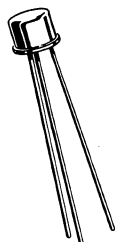


2N1983 (SILICON)

2N1984



NPN silicon annular small-signal transistor.

CASE 31
(TO-5)

Collector connected to case

MAXIMUM RATINGS

Rating	Symbol	Value	Unit
Collector-Emitter Voltage	V_{CEO}	25	Vdc
Collector-Base Voltage	V_{CB}	50	Vdc
Emitter-Base Voltage	V_{EB}	5.0	Vdc
Collector Current	I_C	1.0	Adc
Total Device Dissipation @ $T_A = 25^\circ\text{C}$ Derate above 25°C	P_D	0.6 4.8	Watt mW/ $^\circ\text{C}$
Total Device Dissipation @ $T_C = 25^\circ\text{C}$ Derate above 25°C	P_D	2.0 16	Watts mW/ $^\circ\text{C}$
Operating and Storage Junction Temperature Range	T_J, T_{stg}	-65 to +150	$^\circ\text{C}$

THERMAL CHARACTERISTICS

Characteristic	Symbol	Max	Unit
Thermal Resistance, Junction to Case	θ_{JC}	62.5	$^\circ\text{C}/\text{W}$
Thermal Resistance, Junction to Ambient	θ_{JA}	208	$^\circ\text{C}/\text{W}$

2N1983, 2N1984 (continued)

ELECTRICAL CHARACTERISTICS ($T_A = 25^\circ\text{C}$ unless otherwise noted)

Characteristic	Symbol	Min	Max	Unit
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OFF CHARACTERISTICS

Collector-Emitter Sustaining Voltage ⁽¹⁾ ($I_C = 100 \text{ mAdc}$, $I_B = 0$)	$BV_{CEO(sus)}$	25	-	Vdc
Collector-Emitter Sustaining Voltage ⁽¹⁾ ($I_C = 100 \text{ mAdc}$, $R_{BE} \leq 10 \text{ ohms}$)	$BV_{CER(sus)}$	30	-	Vdc
Collector Cutoff Current ($V_{CB} = 30 \text{ Vdc}$, $I_E = 0$) ($V_{CB} = 30 \text{ Vdc}$, $I_E = 0$, $T_A = 150^\circ\text{C}$)	I_{CBO}	-	5.0 200	μAdc
Emitter-Cutoff Current ($V_{EB(off)} = 2.0 \text{ Vdc}$, $I_C = 0$)	I_{EBO}	-	100	μAdc

ON CHARACTERISTICS

Collector-Emitter Saturation Voltage ($I_C = 5.0 \text{ mAdc}$, $I_B = 0.5 \text{ mAdc}$)	$V_{CE(sat)}$	-	0.25	Vdc
Base-Emitter On Voltage ($I_C = 1.0 \text{ mAdc}$, $V_{CE} = 5.0 \text{ Vdc}$)	$V_{BE(on)}$	-	0.85	Vdc

SMALL-SIGNAL CHARACTERISTICS

Current-Gain-Bandwidth Product ($I_C = 50 \text{ mAdc}$, $V_{CE} = 10 \text{ Vdc}$, $f = 20 \text{ MHz}$)	f_T	40	-	MHz
Output Capacitance ($V_{CB} = 10 \text{ Vdc}$, $I_E = 0$)	C_{ob}	-	45	pF
Input Impedance ($I_C = 5.0 \text{ mAdc}$, $V_{CE} = 5.0 \text{ Vdc}$)	h_{ie}	-	2.0 1.2	k ohm
Input Resistance ($I_C = 1.0 \text{ mAdc}$, $V_{CE} = 5.0 \text{ Vdc}$, $f = 1.0 \text{ kHz}$) ($I_C = 5.0 \text{ mAdc}$, $V_{CE} = 5.0 \text{ Vdc}$, $f = 1.0 \text{ kHz}$)	h_{ib}	20 4.0	30 8.0	ohm
Voltage Feedback Ratio ($I_C = 1.0 \text{ mAdc}$, $V_{CE} = 5.0 \text{ Vdc}$, $f = 1.0 \text{ kHz}$) ($I_C = 5.0 \text{ mAdc}$, $V_{CE} = 5.0 \text{ Vdc}$, $f = 1.0 \text{ kHz}$)	h_{rb}	- - -	7.0 5.0 7.0 5.0	$\times 10^{-4}$
Small-Signal Current Gain ($I_C = 1.0 \text{ mAdc}$, $V_{CE} = 5.0 \text{ Vdc}$, $f = 1.0 \text{ kHz}$) ($I_C = 5.0 \text{ mAdc}$, $V_{CE} = 5.0 \text{ Vdc}$, $f = 1.0 \text{ kHz}$)	h_{fe}	70 35 80 40	210 100 240 120	-
Output Admittance ($I_C = 5.0 \text{ mAdc}$, $V_{CE} = 5.0 \text{ Vdc}$)	h_{oe}	- -	200 100	-
Output Admittance ($I_C = 1.0 \text{ mAdc}$, $V_{CE} = 5.0 \text{ Vdc}$, $f = 1.0 \text{ kHz}$) ($I_C = 5.0 \text{ mAdc}$, $V_{CE} = 5.0 \text{ Vdc}$, $f = 1.0 \text{ kHz}$)	h_{ob}	- -	1.0 1.5	μmho

⁽¹⁾ Pulse Test: Pulse Width $\leq 300 \mu\text{s}$, Duty Cycle $\leq 2.0\%$.