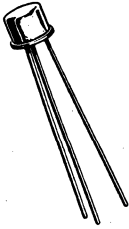


2N1893 (SILICON) 2N2405

NPN silicon annular transistors designed for medium-power amplifier and switching applications.



**CASE 31
(TO-5)**
Collector connected
to case

MAXIMUM RATINGS

Rating	Symbol	2N1893	2N2405	Unit
Collector-Emitter Voltage	V_{CEO}	80	90	Vdc
Collector-Emitter Voltage	V_{CER}	100	140	Vdc
Collector-Base Voltage	V_{CB}	120		Vdc
Emitter-Base Voltage	V_{EB}	7.0		Vdc
Collector Current	I_C	0.5	1.0	Adc
Total Device Dissipation @ $T_A = 25^\circ\text{C}$ Derate above 25°C	P_D	0.8 4.57	1.0 5.71	Watt mW/ $^\circ\text{C}$
Total Device Dissipation @ $T_C = 25^\circ\text{C}$ Derate above 25°C	P_D	3.0 17.2	5.0 28.6	Watts mW/ $^\circ\text{C}$
Operating and Storage Junction Temperature Range	T_J, T_{stg}	-65 to +200		$^\circ\text{C}$

THERMAL CHARACTERISTICS

Characteristic	Symbol	2N1893	2N2405	Unit
Thermal Resistance, Junction to Case	θ_{JC}	58.3	35	$^\circ\text{C}/\text{W}$
Thermal Resistance, Junction to Ambient	θ_{JA}	219	175	$^\circ\text{C}/\text{W}$

ELECTRICAL CHARACTERISTICS ($T_A = 25^\circ\text{C}$ unless otherwise noted)

Characteristic	Symbol	Min	Max	Unit
OFF CHARACTERISTICS				
Collector-Emitter Sustaining Voltage* ($I_C = 30 \text{ mAdc}, I_B = 0$) ($I_C = 100 \text{ mAdc}, I_B = 0$)	$BV_{CEO(sus)}$ *	80 90	-	Vdc
Collector-Emitter Sustaining Voltage ($I_C = 100 \text{ mAdc}, R_{BE} = 10 \text{ ohms}$)	$BV_{CER(sus)}$	100 140	-	Vdc
Collector-Base Breakdown Voltage ($I_C = 100 \mu\text{Adc}, I_E = 0$)	BV_{CBO}	120	-	Vdc
Emitter-Base Breakdown Voltage ($I_E = 100 \mu\text{Adc}, I_C = 0$)	BV_{EBO}	7.0	-	Vdc
Collector Cutoff Current ($V_{CB} = 90 \text{ Vdc}, I_E = 0$) ($V_{CB} = 90 \text{ Vdc}, I_E = 0, T_A = 150^\circ\text{C}$)	I_{CBO}	-	0.01 15 10	μAdc
Emitter Cutoff Current ($V_{BE} = 5.0 \text{ Vdc}, I_C = 0$)	I_{EBO}	-	0.01	μAdc

2N1893, 2N2405 (continued)

ELECTRICAL CHARACTERISTICS (continued)

Characteristic		Symbol	Min	Max	Unit
ON CHARACTERISTICS					
DC Current Gain ⁽¹⁾ ($I_C = 0.1 \text{ mAdc}$, $V_{CE} = 10 \text{ Vdc}$)	2N1893	h_{FE}	20	-	-
($I_C = 10 \text{ mAdc}$, $V_{CE} = 10 \text{ Vdc}$)	2N1893		35	-	
($I_C = 10 \text{ mAdc}$, $V_{CE} = 10 \text{ Vdc}$, $T_A = -55^\circ\text{C}$)	2N1893		20	-	
($I_C = 150 \text{ mAdc}$, $V_{CE} = 10 \text{ Vdc}$)	2N1893		40	120	
	2N2405		60	200	
Collector-Emitter Saturation Voltage ($I_C = 50 \text{ mAdc}$, $I_B = 5.0 \text{ mAdc}$)	2N1893	$V_{CE(sat)}$	-	1.2	Vdc
($I_C = 150 \text{ mAdc}$, $I_B = 15 \text{ mAdc}$)	2N1893		-	5.0	
	2N2405		-	0.5	
Base-Emitter Saturation Voltage ($I_C = 50 \text{ mAdc}$, $I_B = 5.0 \text{ mAdc}$)	2N1893	$V_{BE(sat)}$	-	0.9	Vdc
($I_C = 150 \text{ mAdc}$, $I_B = 15 \text{ mAdc}$)	2N1893		-	1.3	
	2N2405		-	1.1	

SMALL-SIGNAL CHARACTERISTICS

Current-Gain-Bandwidth Product ($I_C = 50 \text{ mAdc}$, $V_{CE} = 10 \text{ Vdc}$; $f = 20 \text{ MHz}$)	2N1893	f_T	50	-	MHz
Output Capacitance ($V_{CB} = 10 \text{ Vdc}$, $I_E = 0$, $100 \text{ kHz} \leq f \leq 1.0 \text{ MHz}$)		C_{ob}	-	15	pF
Input Capacitance ($V_{BE} = 0.5 \text{ Vdc}$, $I_C = 0$, $100 \text{ kHz} \leq f \leq 1.0 \text{ MHz}$)	2N1893	C_{ib}	-	85	pF
Input Impedance ($I_C = 1.0 \text{ mAdc}$, $V_{CB} = 5.0 \text{ Vdc}$, $f = 1.0 \text{ kHz}$)	2N1893	h_{ib}	20	30	ohms
($I_C = 5.0 \text{ mAdc}$, $V_{CB} = 10 \text{ Vdc}$, $f = 1.0 \text{ kHz}$)	2N1893, 2N2405		4.0	8.0	
Voltage Feedback Ratio ($I_C = 1.0 \text{ mAdc}$, $V_{CB} = 5.0 \text{ Vdc}$, $f = 1.0 \text{ kHz}$)	2N1893	h_{rb}	-	1.25	$\times 10^{-4}$
($I_C = 5.0 \text{ mAdc}$, $V_{CB} = 10 \text{ Vdc}$, $f = 1.0 \text{ kHz}$)	2N1893		-	1.5	
	2N2405		-	3.0	
Small-Signal Current Gain ($I_C = 1.0 \text{ mAdc}$, $V_{CE} = 5.0 \text{ Vdc}$, $f = 1.0 \text{ kHz}$)	2N1893	h_{fe}	30	100	-
($I_C = 5.0 \text{ mAdc}$, $V_{CE} = 5.0 \text{ Vdc}$, $f = 1.0 \text{ kHz}$)	2N2405		50	275	
($I_C = 5.0 \text{ mAdc}$, $V_{CE} = 10 \text{ Vdc}$, $f = 1.0 \text{ kHz}$)	2N1893		45	-	
Output Admittance ($I_C = 1.0 \text{ mAdc}$, $V_{CB} = 5.0 \text{ Vdc}$, $f = 1.0 \text{ kHz}$)	2N1893	h_{ob}	-	0.5	μmho
($I_C = 5.0 \text{ mAdc}$, $V_{CB} = 10 \text{ Vdc}$, $f = 1.0 \text{ kHz}$)	2N1893, 2N2405		-	0.5	

⁽¹⁾ Pulse Test: Pulse Width $\leq 300 \mu\text{s}$, Duty Cycle $\leq 2.0\%$.