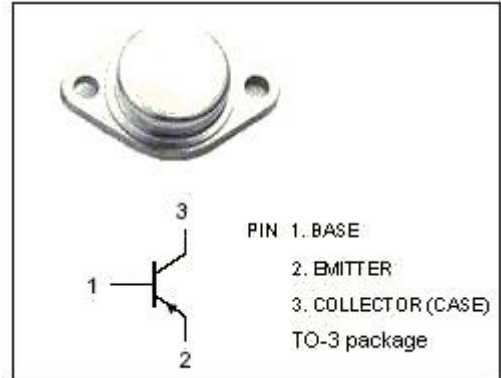


isc Silicon PNP Power Transistor
2N3185
DESCRIPTION

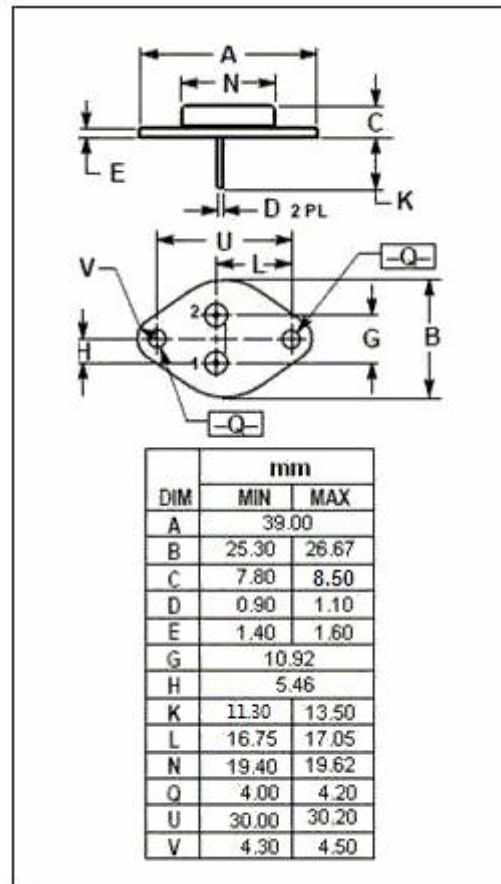
- Excellent Safe Operating Area
- With TO-3 package
- Low collector saturation voltage
- 100% avalanche tested
- Minimum Lot-to-Lot variations for robust device performance and reliable operation

APPLICATIONS

- For medium-speed switching and amplifier applications


ABSOLUTE MAXIMUM RATINGS(T_a=25°C)

SYMBOL	PARAMETER	VALUE	UNIT
V _{CBO}	Collector-Base Voltage	-80	V
V _{CEO}	Collector-Emitter Voltage	-80	V
V _{EBO}	Emitter-Base Voltage	-10	V
I _c	Collector Current-Continuous	-5	A
P _c	Collector Power Dissipation@T _c =25°C	75	W
T _J , T _{stg}	Operating and Storage Junction Temperature Range	-65~+200	°C


THERMAL CHARACTERISTICS

SYMBOL	PARAMETER	MAX	UNIT
R _{th j-c}	Thermal Resistance, Junction to Case	1.17	°C/W

isc Silicon PNP Power Transistors

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ELECTRICAL CHARACTERISTICS

 $T_C=25^\circ\text{C}$ unless otherwise specified

SYMBOL	PARAMETER	CONDITIONS	MIN	MAX	UNIT
$V_{CE(sat)}$	Collector-Emitter Saturation Voltage	$I_C = -2A; I_B = -0.3A$		-1.0	V
$V_{BE(sat)}$	Base-Emitter Saturation Voltage	$I_C = -2A; V_{CE} = -0.3V$		-2.0	V
I_{CEO}	Collector Cutoff Current	$V_{CE} = -80V; I_B = 0$		-5.0	mA
I_{EBO}	Emitter Cutoff Current	$V_{EB} = -5V; I_C = 0$		-1.0	mA
h_{FE}	DC Current Gain	$I_C = -2A; V_{CE} = -3V$	10	30	

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