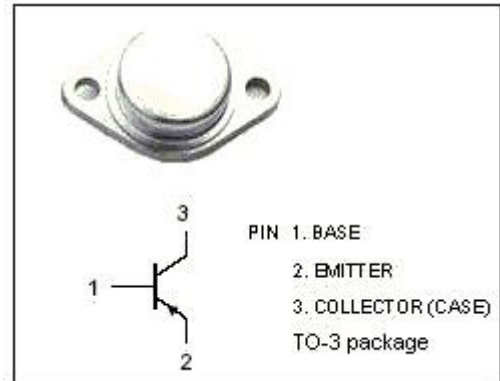


isc Silicon PNP Power Transistor
2N3196
DESCRIPTION

- Excellent Safe Operating Area
- With TO-3 package
- Low collector saturation voltage
- 100% avalanche tested
- Minimum Lot-to-Lot variations for robust device performance and reliable operation

APPLICATIONS

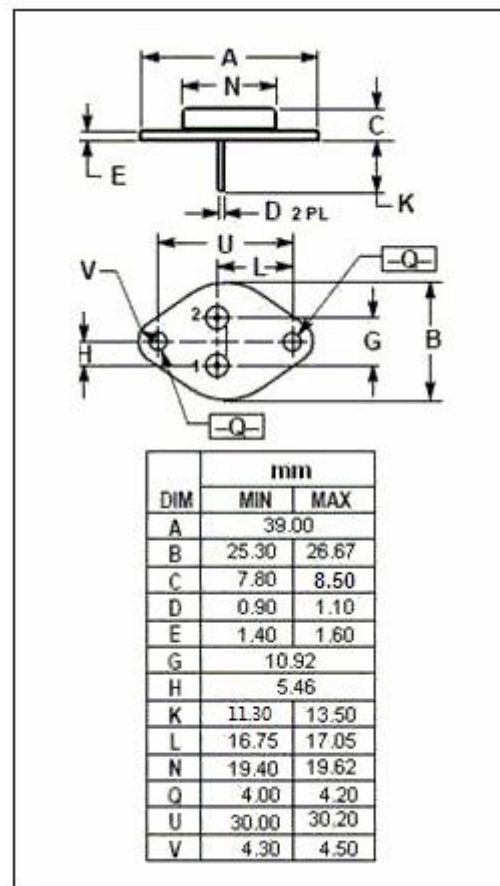
- For medium-speed switching and amplifier applications


ABSOLUTE MAXIMUM RATINGS($T_a=25^\circ\text{C}$)

SYMBOL	PARAMETER	VALUE	UNIT
V_{CBO}	Collector-Base Voltage	-60	V
V_{CEO}	Collector-Emitter Voltage	-60	V
V_{EBO}	Emitter-Base Voltage	-10	V
I_c	Collector Current-Continuous	-5	A
P_C	Collector Power Dissipation@ $T_C=25^\circ\text{C}$	75	W
T_J, T_{stg}	Operating and Storage Junction Temperature Range	-65~+200	$^\circ\text{C}$

THERMAL CHARACTERISTICS

SYMBOL	PARAMETER	MAX	UNIT
$R_{th\ j-c}$	Thermal Resistance, Junction to Case	1.17	$^\circ\text{C/W}$



isc Silicon PNP Power Transistors**2N3196****ELECTRICAL CHARACTERISTICS** $T_C=25^{\circ}\text{C}$ unless otherwise specified

SYMBOL	PARAMETER	CONDITIONS	MIN	MAX	UNIT
$V_{CE(sat)}$	Collector-Emitter Saturation Voltage	$I_C = -3\text{A}; I_B = -0.6\text{A}$		-0.9	V
$V_{BE(sat)}$	Base-Emitter Saturation Voltage	$I_C = -3\text{A}; V_{CE} = -0.6\text{V}$		-1.9	V
I_{CEO}	Collector Cutoff Current	$V_{CE} = -60\text{V}; I_B = 0$		-5.0	mA
I_{EBO}	Emitter Cutoff Current	$V_{EB} = -5\text{V}; I_C = 0$		-1.0	mA
h_{FE}	DC Current Gain	$I_C = -3\text{A}; V_{CE} = -3\text{V}$	10	30	

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