

isc Silicon PNP Power Transistor

2N3198

DESCRIPTION

- Excellent Safe Operating Area
- With TO-3 package
- · Low collector saturation voltage
- 100% avalanche tested
- Minimum Lot-to-Lot variations for robust device performance and reliable operation

APPLICATIONS

· For medium-speed switching and amplifier applications

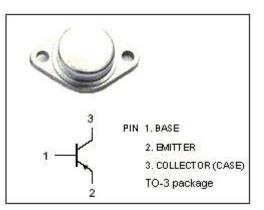
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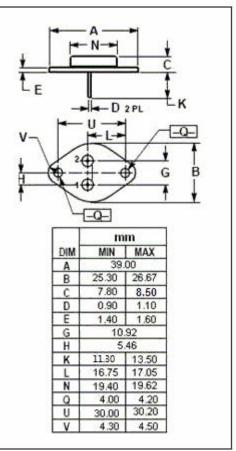
ABSOLUTE MAXIMUM RATINGS(Ta=25℃)

SYMBOL	PARAMETER	VALUE	UNIT
Vсво	Collector-Base Voltage	-100	V
V _{CEO}	Collector-Emitter Voltage	-100	V
V _{EBO}	Emitter-Base Voltage -10		V
lc	Collector Current-Continuous -5		A
Pc	Collector Power Dissipation@Tc=25°C	75	W
T _{J,} T _{stg}	Operating and Storage Junction Temperature Range -65~+200		°C

THERMAL CHARACTERISTICS

SYMBOL	PARAMETER	МАХ	UNIT
R _{th j-c}	Thermal Resistance, Junction to Case	1.17	°C/W







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ELECTRICAL CHARACTERISTICS

T_c=25℃ unless otherwise specified

SYMBOL	PARAMETER	CONDITIONS	MIN	МАХ	UNIT
V _{CE(sat)}	Collector-Emitter Saturation Voltage	I _C = -3A; I _B = -0.6A		-0.9	V
V _{BE(sat)}	Base-Emitter Saturation Voltage	I _C = -3A; V _{CE} =-0.6V		-1.9	V
I _{CEO}	Collector Cutoff Current	V _{CE} = -100V; I _B =0		-5.0	mA
Іево	Emitter Cutoff Current	V _{EB} = -5V; I _C =0		-1.0	mA
h _{FE}	DC Current Gain	I _C = -3A ; V _{CE} = -3V	10	30	

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