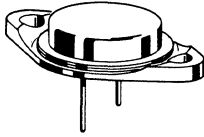


2N3232 (SILICON)

2N3235



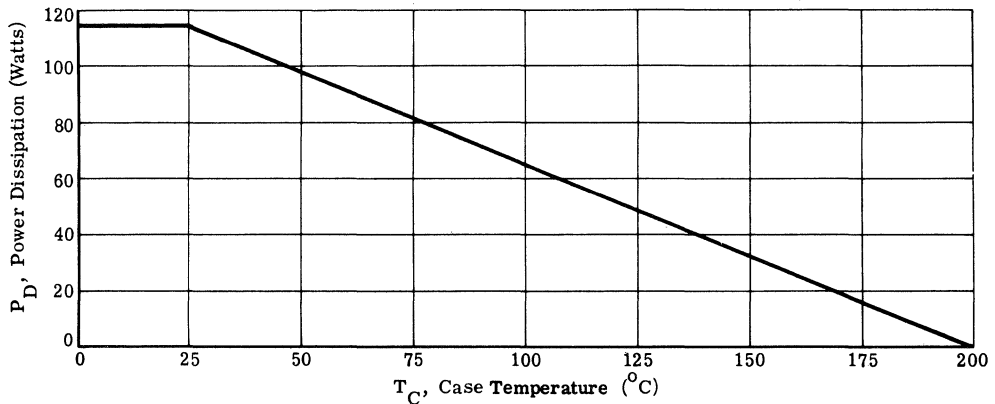
NPN silicon power transistors designed for switching and amplifier applications,

CASE 11
(TO-3)

MAXIMUM RATINGS

Rating	Symbol	2N3232	2N3235	Units
Collector-Base Voltage	V_{CB}	60	55	Vdc
Collector-Emitter Voltage	V_{CEO}	60	55	Vdc
Emitter-Base Voltage	V_{EB}	6.0	7.0	Vdc
Collector Current (Continuous)	I_C	7.5	15	Adc
Base Current (Continuous)	I_B	3.0	7.0	Adc
Power Dissipation	P_D	117		Watts
Thermal Resistance, Junction to Case	θ_{JC}	1.5		$^{\circ}C/W$
Junction Operating Temperature Range	T_J	-65 to +200		$^{\circ}C$

FIGURE 1 — POWER-TEMPERATURE DERATING CURVE



2N3232, 2N3235 (continued)

ELECTRICAL CHARACTERISTICS ($T_C = 25^\circ\text{C}$ unless otherwise noted)

Characteristic	Symbol	Min	Max	Unit
Emitter-Base Cutoff Current ($V_{EB} = 6.0 \text{ Vdc}$) ($V_{EB} = 7.0 \text{ Vdc}$)	I_{EBO}	-	1.0 5.0	mAdc
Collector-Emitter Cutoff Current ($V_{CE} = 60 \text{ Vdc}$, $V_{BE} = -1.5 \text{ Vdc}$) ($V_{CE} = 30 \text{ Vdc}$, $V_{BE} = -1.5 \text{ Vdc}$, $T_C = 150^\circ\text{C}$)	I_{CEX}	-	1.0 1.0 5.0 5.0	mAdc
Collector-Emitter Sustaining Voltage* ($I_C = 100 \text{ mAdc}$, $I_B = 0$)	$V_{CEO(sus)}$ *	60 55	- -	Vdc
Collector Current ($V_{CE} = 60 \text{ Vdc}$, $I_B = 0$) ($V_{CE} = 55 \text{ Vdc}$, $I_B = 0$)	I_{CEO}	-	10 10	mAdc
DC Current Gain* ($I_C = 1.5 \text{ Adc}$, $V_{CE} = 10 \text{ Vdc}$) ($I_C = 3.0 \text{ Adc}$, $V_{CE} = 10 \text{ Vdc}$) ($I_C = 2 \text{ Adc}$, $V_{CE} = 4 \text{ Vdc}$) ($I_C = 4 \text{ Adc}$, $V_{CE} = 4 \text{ Vdc}$)	h_{FE}	18 18 20 20	- 55 - 70	-
Collector-Emitter Saturation Voltage ($I_C = 3.0 \text{ Adc}$, $I_B = 0.2 \text{ Adc}$) ($I_C = 4.0 \text{ Adc}$, $I_B = 0.4 \text{ Adc}$)	$V_{CE(sat)}$	-	2.5 1.1	Vdc
Base-Emitter Voltage* ($I_C = 3.0 \text{ Adc}$, $V_{CE} = 10 \text{ Vdc}$) ($I_C = 4.0 \text{ Adc}$, $V_{CE} = 4 \text{ Vdc}$)	V_{BE}	-	3.5 1.8	Vdc
Small Signal Current Gain ($V_{CE} = 10 \text{ Vdc}$, $I_C = 3.0 \text{ Adc}$, $f = 1.0 \text{ MHz}$) ($V_{CE} = 4 \text{ Vdc}$, $I_C = 4.0 \text{ Adc}$, $f = 1.0 \text{ MHz}$)	h_{fe}	1.0 1.0	- -	-

*Use sweep test to prevent overheating.