

p-channel JFETs designed for . . .



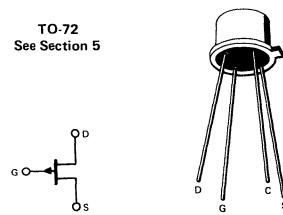
Performance Curves PC
See Section 4

- Small-Signal Amplifiers
- Analog Multipliers
- Modulators

BENEFITS

- Ease of Amplifier Design
- I_{DSS} & G_f Closely Specified

TO-72
See Section 5



*ABSOLUTE MAXIMUM RATINGS (25°C)

Gate-Drain and Gate-Source Voltage (Note 1)	20 V
Gate Current	10 mA
Total Device Dissipation at (or below)	
25°C Free-Air Temperature (Note 2)	300 mW
Storage Temperature Range	-65 to +200°C
Lead Temperature	
(1/16" from case for 10 seconds)	230°C

*ELECTRICAL CHARACTERISTICS (25°C unless otherwise noted)

Characteristic		2N3329		2N3330		2N3331		2N3332		Unit	Test Conditions			
		Min	Max	Min	Max	Min	Max	Min	Max		V _{GS} = 10 V, V _{DSD} = 0	V _{GS} = 10 V, V _{DSD} = 0, TA = 150°C	V _{GS} = 10 μA, V _{DSD} = 0	
1	I _{GSS}	Gate Reverse Current		0.01	0.01	0.01	0.01	0.01	0.01	μA	V _{GS} = 10 V, V _{DSD} = 0			
		10	10	10	10	10	10	10	10		V _{GS} = 10 V, V _{DSD} = 0, TA = 150°C			
3	BV _{GSS}	Gate-Source Breakdown Voltage		20	20	20	20	20	20	V	I _G = 10 μA, V _{DSD} = 0			
		5	6	6	8	8	6	6	6		V _{DSD} = -15 V, I _D = -10 μA			
5	I _{DSS}	Saturation Drain Current		-1	-3	-2	-6	-5	-15	-1	-6	mA	V _{DSD} = -10 V, V _{GS} = 0	
		1000	1000	800	800	600	600	600	600	Ω	I _D = -100 μA, V _{GS} = 0			
7	g _{fs}	Common-Source Input Conductance		0.2	0.2	0.2	0.2	0.2	0.2	μmho	2N3329: I _D = -1 mA			
		0.1	0.1	0.1	0.1	0.1	0.1	0.1	0.1		2N3330: I _D = -2 mA			
9	g _{os}	Common-Source Output Conductance		20	40	100	100	20	20		2N3331: I _D = -5 mA			
		1000	2000	1500	3000	2000	4000	1000	2200		2N3332: I _D = -1 mA			
10	g _{fs}	Common-Source Forward Transconductance		900	1350	1800	1800	900	900		f = 10 MHz			
		20	20	20	20	20	20	20	20		f = 1 MHz			
12	C _{iss}	Common-Source Input Capacitance		3	3	4	4	1	1	pF	V _{DSD} = -10 V, V _{GS} = 1 V			
		3	3	4	4	1	1	5	5		V _{DSD} = -5 V, I _D = -1 mA			
13	NF	Noise Figure		1	1	1	1	1	1	dB	R _{gen} = 1 MΩ			
		5	5	5	5	5	5	5	5		V _{DSD} = -5 V, I _D = -1 mA			
14	NF	Noise Figure		1	1	1	1	1	1		R _{gen} = 10 MΩ			
		5	5	5	5	5	5	5	5		V _{DSD} = -5 V, I _D = -1 mA			

* JEDEC registered data

PC

NOTES:

1. Due to symmetrical geometry, these units may be operated with source and drain leads interchanged.
2. Derate linearly to 175°C free-air temperature at rate of 2.0 mW/°C

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2N3329 2N3330 2N3331 2N3332