

p-channel JFETs designed for . . .



Performance Curves PE
See Section 5

- Analog Switches
- Choppers
- Commutators
- Amplifiers

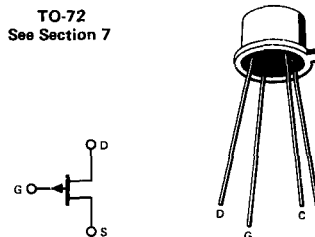
BENEFITS

- Low Insertion Loss
 $R_{DS(on)} < 150 \Omega$ (2N3386)

*ABSOLUTE MAXIMUM RATINGS (25°C)

Gate-Drain Voltage (Note 1)	30 V
Gate-Source Voltage (Note 1)	30 V
Gate Current	50 mA
Storage Temperature Range	-65 to +200°C
Total Dissipation at 25°C T_A (Note 2)	300 mW

TO-72
See Section 7



*ELECTRICAL CHARACTERISTICS (25°C unless otherwise noted)

Characteristic		2N3382		2N3384		2N3386		Unit	Test Conditions	
		Min	Max	Min	Max	Min	Max			
S T A T I C	1	I_{GSS}		15		15		15	nA	$V_{GS} = 30 V$ $V_{DS} = 0$
	2	I_{GSS}		15		15		15	μA	$V_{GS} = 5 V$ $V_{DS} = 0$ $T_A = 150^\circ C$
	3	BV_{GSS}	30		30		30		V	$I_G = 1 \mu A$ $V_{DS} = 0$
	4	$V_{GS(off)}$	1.0	5.0	4.0	5.0	4.0	9.5		$V_{DS} = -5 V$ $I_D = -1 \mu A$
	5	I_{DSS}	-3.0	-30.0	-15.0	-30.0	-15.0	-50.0	mA	$V_{DS} = -10 V$ $V_{GS} = 0$
D Y N A M I C	6	$I_{D(off)}$		-2 (6)		-2 (6)		-2.5 (10)	nA (V)	$V_{DS} = -5 V$ $V_{GS} = ()$
	7	$r_{ds(on)}$		300		180		150	Ω	$V_{GS} = 0$ $V_{DS} = 0$ $f = 1 \text{ kHz}$
	8	g_{fs}	4500	12,500	7500	12,500	7500	15,000	μmho	$V_{DS} = -10 V$ $V_{GS} = 0$
	9	C_{sgs} + C_{dgs}		6.0		6.0		6.0	pF	$V_{DS} = 0$ $V_{GS} = 10 V$ $f = 140 \text{ kHz}$
	10	C_{iss}	16 Typ							

*JEDEC registered data.

PE

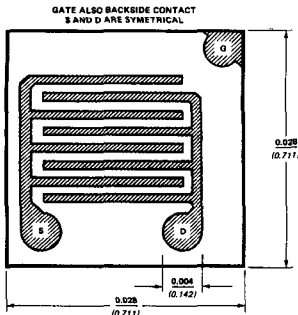
NOTE:

1. Due to symmetrical geometry, units may be operated with source and drain leads interchanged.
2. Derate linearly to +175°C at 2 mW/°C
3. Pulswidth = 2 ms, duty cycle $\leq 3\%$.

2N3382 2N3384 2N3386

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Siliconix



ALL DIMENSIONS IN INCHES
(ALL DIMENSIONS IN MILLIMETERS)

p-channel JFET designed for . . .

- General Purpose Amplifiers
- Switches

TYPE
Single
Single

PACKAGE
TO-72
Chip



BENEFITS:

- Wide Range of Transconductance

PRINCIPAL DEVICES

2N3382, 2N3384, 2N3386, VCR3P
2N3382CHP-866CHP, VCR3PCHP

PERFORMANCE CURVES (25°C unless otherwise noted)

