

# 2N3553

JAN, JTX, JTXV AVAILABLE  
CASE 79-02, STYLE 1  
TO-39 (TO-205AD)

HIGH FREQUENCY TRANSISTOR

NPN SILICON



## MAXIMUM RATINGS

Rating	Symbol	Value	Unit
Collector-Emitter Voltage	$V_{CE0}$	40	Vdc
Collector-Base Voltage	$V_{CB0}$	65	Vdc
Emitter-Base Voltage	$V_{EBO}$	4.0	Vdc
Collector Current — Continuous	$I_C$	1.0	Adc
Total Device Dissipation @ $T_C = 25^\circ\text{C}$ Derate above $25^\circ\text{C}$	$P_D$	7.0 40	Watts mW/°C
Operating and Storage Junction Temperature Range	$T_J, T_{stg}$	-65 to +200	°C

## ELECTRICAL CHARACTERISTICS ( $T_A = 25^\circ\text{C}$ unless otherwise noted.)

Characteristic	Symbol	Min	Typ	Max	Unit
----------------	--------	-----	-----	-----	------

### OFF CHARACTERISTICS

Collector-Emitter Sustaining Voltage(1) ( $I_C = 200 \text{ mAdc}, I_B = 0$ )	$V_{CE0(sus)}$	40	—	—	Vdc
Emitter-Base Breakdown Voltage ( $I_E = 0.1 \text{ mAdc}, I_C = 0$ )	$V_{(BR)EBO}$	4.0	—	—	Vdc
Collector Cutoff Current ( $V_{CE} = 30 \text{ Vdc}, I_B = 0$ )	$I_{CE0}$	—	—	0.1	mAdc
Collector Cutoff Current ( $V_{CE} = 30 \text{ Vdc}, V_{BE(off)} = 1.5 \text{ Vdc}, T_C = 200^\circ\text{C}$ ) ( $V_{CE} = 65 \text{ Vdc}, V_{BE(off)} = 1.5 \text{ Vdc}$ )	$I_{CEX}$	— —	— —	5.0 1.0	mAdc
Emitter Cutoff Current ( $V_{BE} = 4.0 \text{ Vdc}, I_C = 0$ )	$I_{EBO}$	—	—	0.1	mAdc

### ON CHARACTERISTICS

DC Current Gain ( $I_C = 250 \text{ mAdc}, V_{CE} = 5.0 \text{ Vdc}$ )	$h_{FE}$	10	—	—	—
Collector-Emitter Saturation Voltage ( $I_C = 250 \text{ mAdc}, I_B = 50 \text{ mAdc}$ )	$V_{CE(sat)}$	—	—	1.0	Vdc

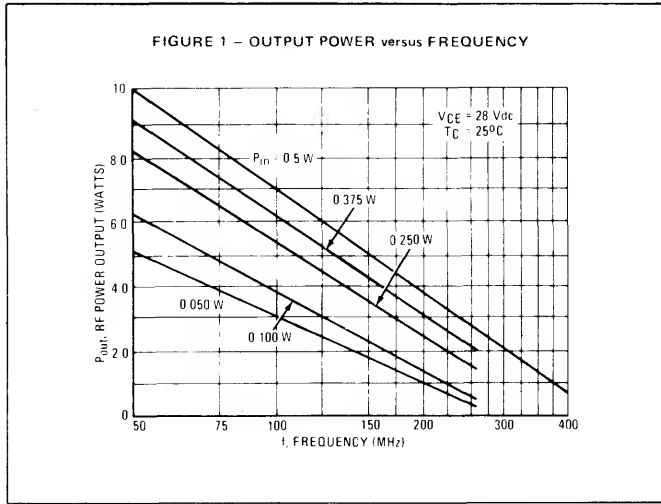
### SMALL SIGNAL CHARACTERISTICS

Current-Gain — Bandwidth Product ( $I_C = 100 \text{ mAdc}, V_{CE} = 28 \text{ Vdc}, f = 100 \text{ MHz}$ )	$f_T$	—	500	—	MHz
Output Capacitance ( $V_{CB} = 30 \text{ Vdc}, I_E = 0, f = 100 \text{ kHz}$ )	$C_{obo}$	—	8.0	10	pF

### FUNCTIONAL TEST (FIGURE 2)

Amplifier Power Gain ( $V_{CE} = 28 \text{ Vdc}, P_{out} = 2.5 \text{ Watts}, f = 175 \text{ MHz}$ )	$G_{pe}$	10	—	—	dB
Collector Efficiency ( $V_{CE} = 28 \text{ Vdc}, P_{out} = 2.5 \text{ Watts}, f = 175 \text{ MHz}$ )	$\eta$	50	—	—	%
Power Input ( $V_{CE} = 28 \text{ Vdc}, P_{out} = 2.5 \text{ Watts}, f = 175 \text{ MHz}$ )	$P_{in}$	—	—	0.25	Watt

(1) Pulsed thru a 25 mH inductor.



**FIGURE 2 – 175 MHz TEST CIRCUIT SCHEMATIC**

