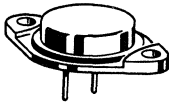


2N3713 thru 2N3716 (SILICON)

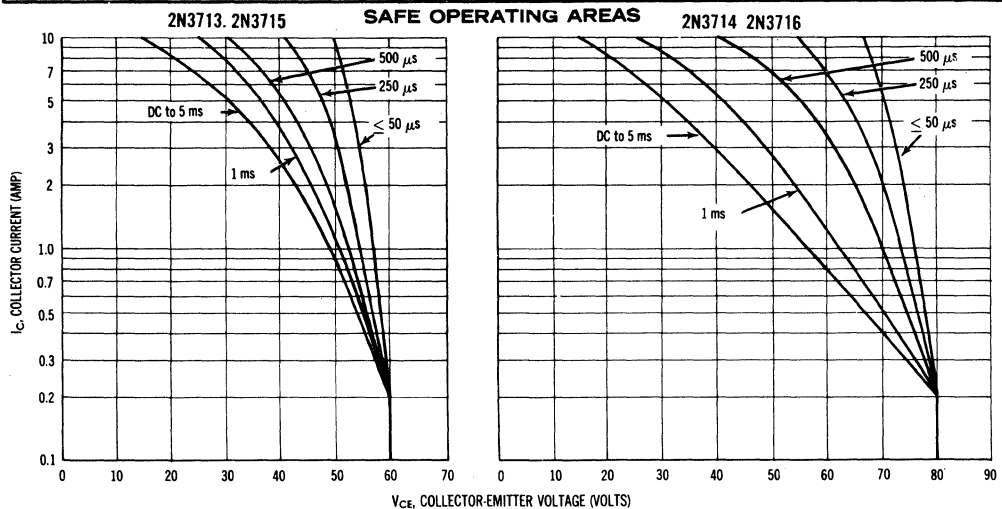


NPN silicon power transistors for medium-speed switching and amplifier applications. Complement to PNP types 2N3789 thru 2N3792.

CASE 11 (TO-3)

MAXIMUM RATINGS

Rating	Symbol	2N3713 2N3715	2N3714 2N3716	Unit
Collector-Base Voltage	V_{CB}	80	100	Volts
Collector-Emitter Voltage	V_{CEO}	60	80	Volts
Emitter-Base Voltage	V_{EB}	7.0	7.0	Volts
Collector Current	I_C	10	10	Amp
Base Current	I_B	4.0	4.0	Amp
Power Dissipation	P_D	150	150	Watts
Thermal Resistance	Θ_{JC}	1.17	1.17	$^{\circ}C/W$
Operating Junction and Storage Temperature Range	T_J and T_{stg}	-65 to +200		$^{\circ}C$



The Safe Operating Area Curves indicate $I_C - V_{CE}$ limits below which the device will not go into secondary breakdown. Collector load lines for specific circuits must fall within the applicable Safe Area to avoid causing a collector-emitter short. (Duty cycle of the excursions make no signifi-

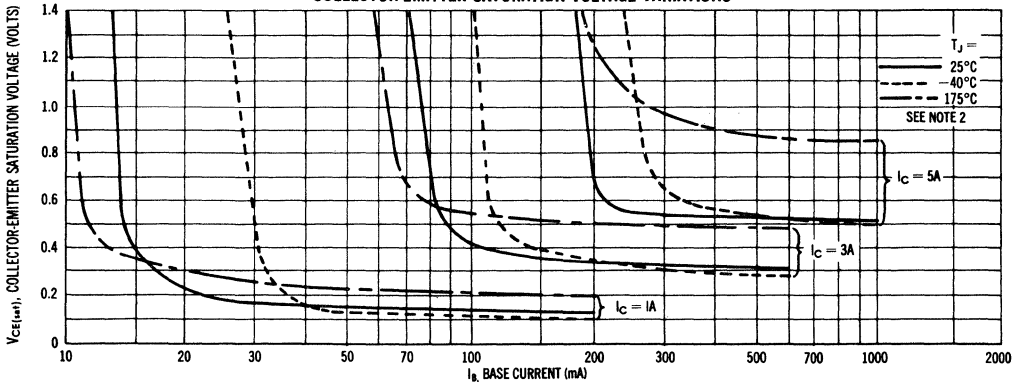
cant change in these safe areas.) To insure operation below the maximum T_J , the power-temperature derating curve must be observed for both steady state and pulse power conditions.

2N3713 thru 2N3716 (continued)
ELECTRICAL CHARACTERISTICS ($T_C = 25^\circ\text{C}$ unless otherwise noted)

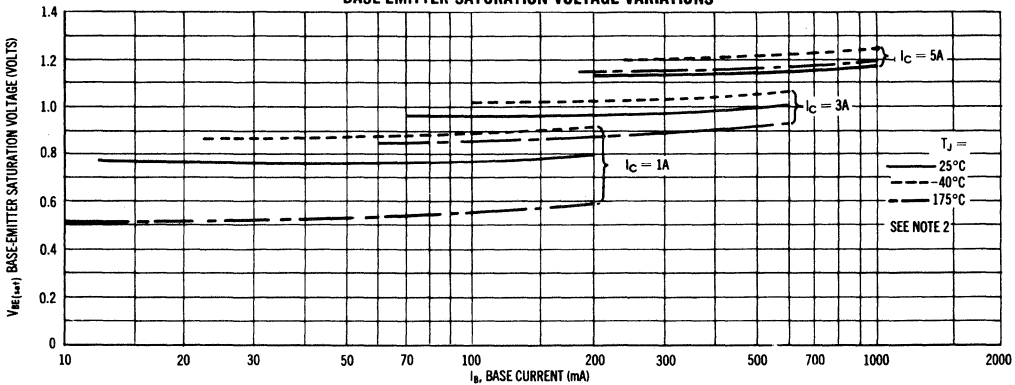
Characteristic	Types	Symbol	Min	Max	Unit
Emitter-Base Cutoff Current ($V_{EB} = 7 \text{ Vdc}$)		I_{EBO}	—	5.0	mAdc
Collector-Emitter Cutoff Current ($V_{CE} = 80 \text{ Vdc}$, $V_{BE} = -1.5 \text{ Vdc}$) ($V_{CE} = 100 \text{ Vdc}$, $V_{BE} = -1.5 \text{ Vdc}$) ($V_{CE} = 60 \text{ Vdc}$, $V_{BE} = -1.5 \text{ Vdc}$, $T_C = 150^\circ\text{C}$) ($V_{CE} = 80 \text{ Vdc}$, $V_{BE} = -1.5 \text{ Vdc}$, $T_C = 150^\circ\text{C}$)	2N3713, 2N3715 2N3714, 2N3716 2N3713, 2N3715 2N3714, 2N3716	I_{CEX}	—	1.0 1.0 10 10	mAdc
Collector-Emitter Sustaining Voltage* ($I_C = 200 \text{ mAdc}$, $I_B = 0$)	2N3713, 2N3715 2N3714, 2N3716	$V_{CEO(sus)}$ *	60 80	—	Vdc
DC Current Gain* ($I_C = 1 \text{ Adc}$, $V_{CE} = 2 \text{ Vdc}$) ($I_C = 3 \text{ Adc}$, $V_{CE} = 2 \text{ Vdc}$)	2N3713, 2N3714 2N3715, 2N3716 2N3713, 2N3714 2N3715, 2N3716	h_{FE} *	25 50 15 30	90 150 — —	—
Collector-Emitter Saturation Voltage* ($I_C = 5 \text{ Adc}$, $I_B = 0.5 \text{ Adc}$)	2N3713, 2N3714 2N3715, 2N3716	$V_{CE(sat)}$ *	—	1.0 0.8	Vdc
Base-Emitter Saturation Voltage* ($I_C = 5 \text{ Adc}$, $I_B = 0.5 \text{ Adc}$)	2N3713, 2N3714 2N3715, 2N3716	$V_{BE(sat)}$ *	—	2.0 1.5	Vdc
Base-Emitter Voltage* ($I_C = 3 \text{ Adc}$, $V_{CE} = 2 \text{ Vdc}$)		V_{BE} *	—	1.5	Vdc
Small Signal Current Gain ($V_{CE} = 10 \text{ Vdc}$, $I_C = 0.5 \text{ Adc}$, $f = 1 \text{ MHz}$)		h_{fe}	4.0	—	—
Switching Times ($I_C = 5 \text{ A}$, $I_{B1} = I_{B2} = 0.5 \text{ A}$) Rise Time Storage Time Fall Time		t_r t_s t_f	Typ		μs
			0.45 0.3 0.4		

*Use sweep test to prevent overheating

COLLECTOR-EMITTER SATURATION VOLTAGE VARIATIONS

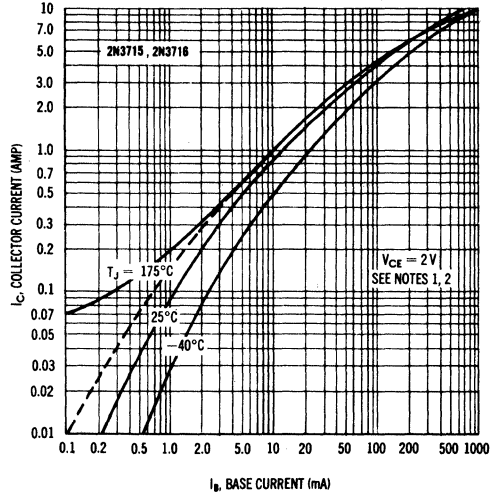
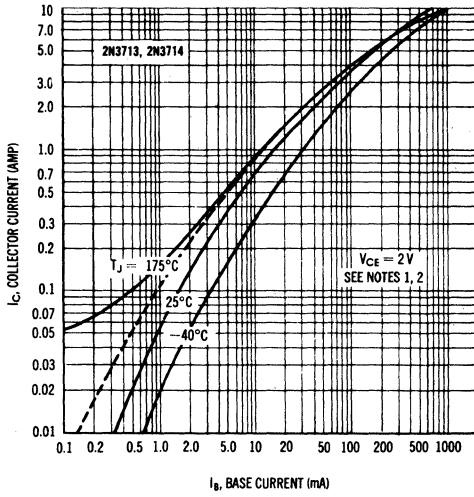


BASE-EMITTER SATURATION VOLTAGE VARIATIONS

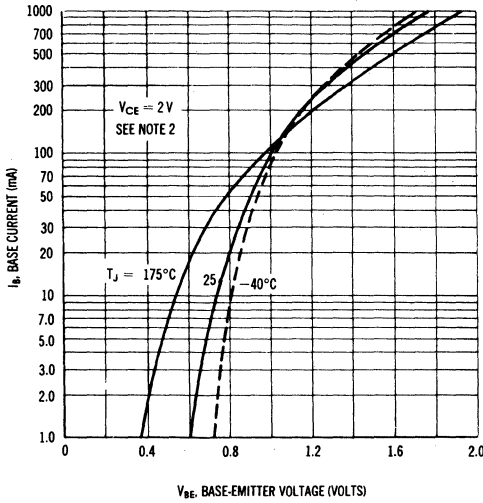


2N3713 thru 2N3716 (continued)

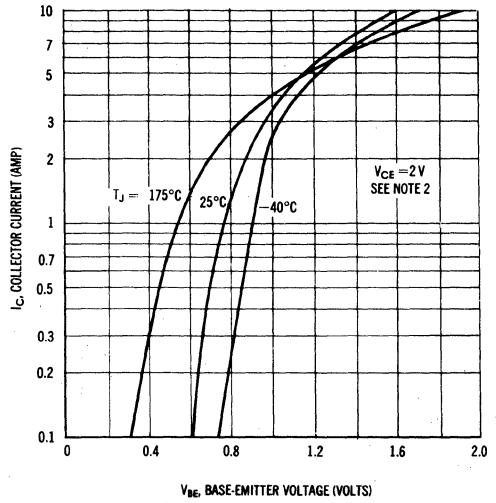
COLLECTOR CURRENT versus BASE CURRENT



BASE CURRENT-VOLTAGE VARIATIONS

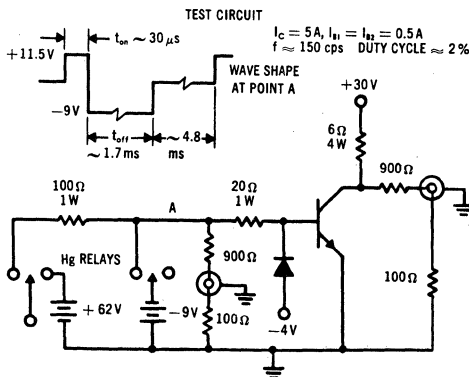


COLLECTOR CURRENT-VOLTAGE VARIATIONS

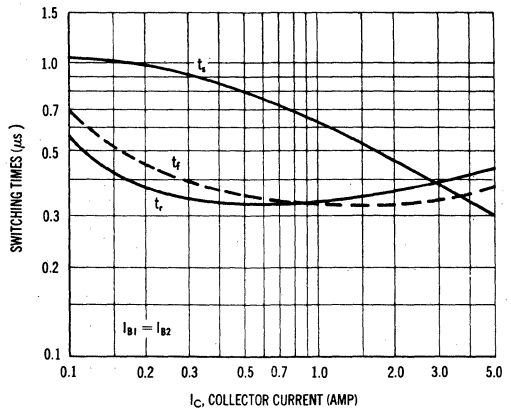


NOTE 1. Dotted line indicates metered base current plus the I_{CBO} of the transistor at 175°C.

NOTE 2. Pulse test: pulse width $\approx 200 \mu s$, duty cycle $\approx 1.5\%$

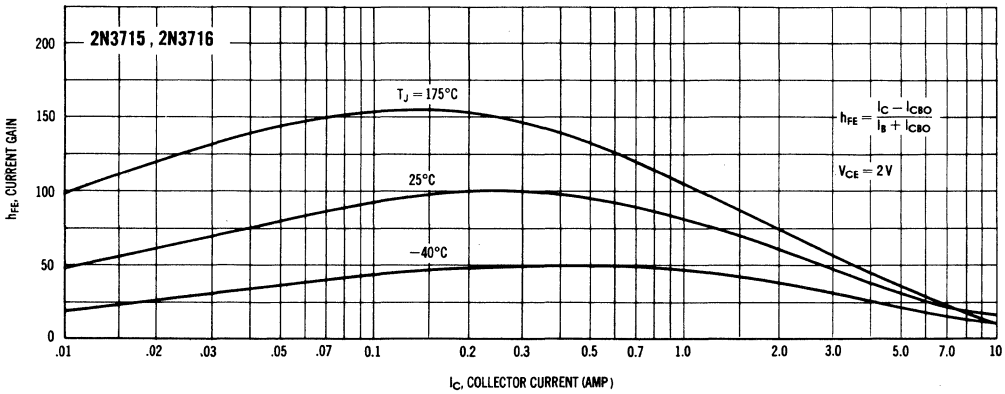
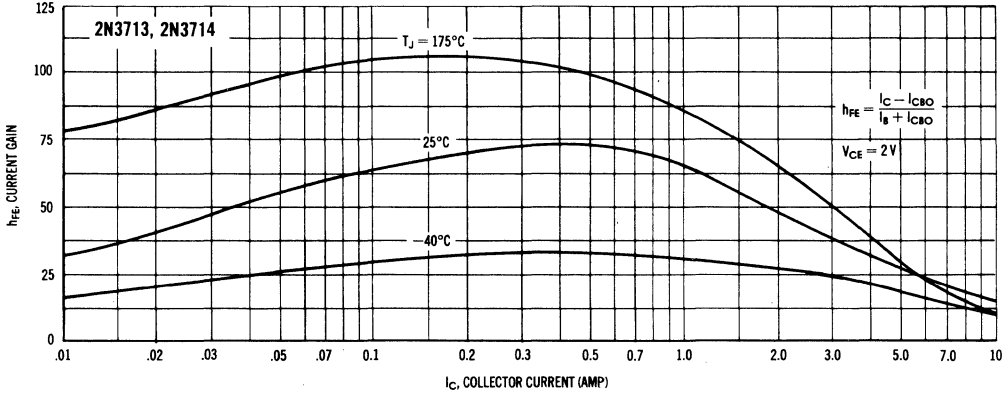


TYPICAL SWITCHING TIMES



2N3713 thru 2N3716 (continued)

CURRENT GAIN VARIATIONS



CURRENT-GAIN — BANDWIDTH PRODUCT versus COLLECTOR CURRENT

