

isc Silicon PNP Power Transistor
2N3740A
DESCRIPTION

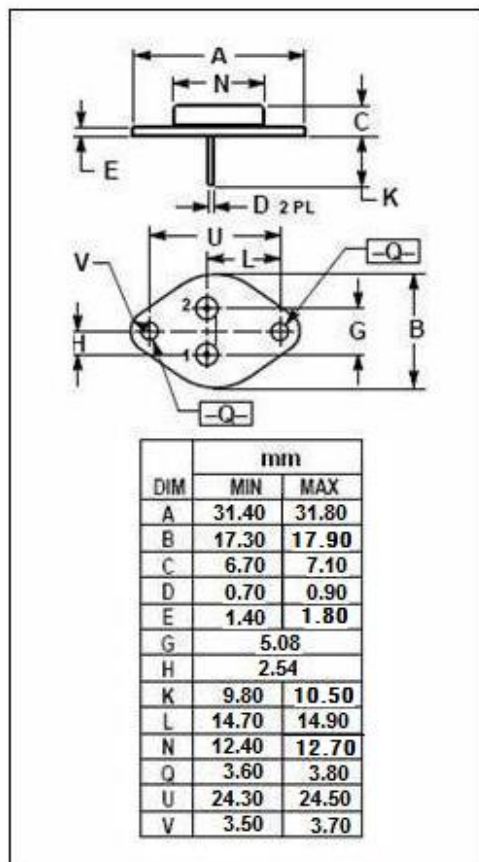
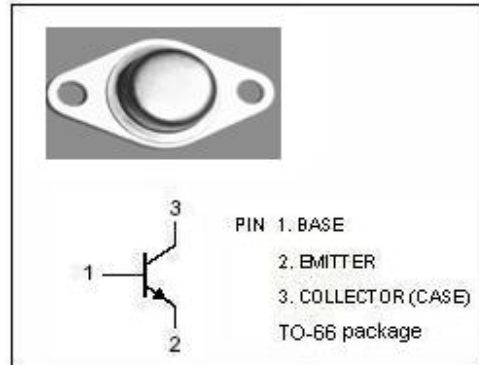
- Collector-Emitter Breakdown Voltage-
: $V_{CEO} = -60V$ (Min)
- Minimum Lot-to-Lot variations for robust device
Performance and reliable operation

APPLICATIONS

- Power amplifier and switching applications

ABSOLUTE MAXIMUM RATINGS ($T_a = 25^\circ C$)

SYMBOL	PARAMETER	VALUE	UNIT
V_{CBO}	Collector-Base Voltage	-60	V
V_{CEO}	Collector-Emitter Voltage	-60	V
V_{EBO}	Emitter-Base Voltage	-7	V
I_C	Collector Current-Continuous	-4	A
P_D	Collector Power Dissipation @ $T_C = 25^\circ C$	25	W
T_J	Junction Temperature	-65~200	$^\circ C$
T_{stg}	Storage Temperature Range	-65~200	$^\circ C$



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ELECTRICAL CHARACTERISTICS

T_C=25°C unless otherwise specified

SYMBOL	PARAMETER	CONDITIONS	MIN	TYP.	MAX	UNIT
V _{(BR)CEO}	Collector-Emitter Breakdown Voltage	I _C =-100mA	-60			V
I _{CBO}	Collector-Base Cutoff Current	V _{CB} =- 60V			-0.1	uA
I _{CEO}	Collector-Emitter Cutoff Current	V _{CE} = -40V			-1	uA
V _{CE(sat)}	Collector-Emitter Saturation Voltage	I _C = -1A; I _B =-0.125A			-0.6	V
V _{BE(on)}	Base-Emitter On Voltage	I _C =-0.25A; V _{CE} = -1V			-1	V
h _{FE-1}	DC Current Gain	I _C =-100mA; V _{CE} =-1V	40			
h _{FE-2}	DC Current Gain	I _C =-250mA; V _{CE} = -1V	30		100	
h _{FE-3}	DC Current Gain	I _C =-500mA; V _{CE} = -1V	20			
h _{FE-4}	DC Current Gain	I _C =-1A; V _{CE} = -1V	10			

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