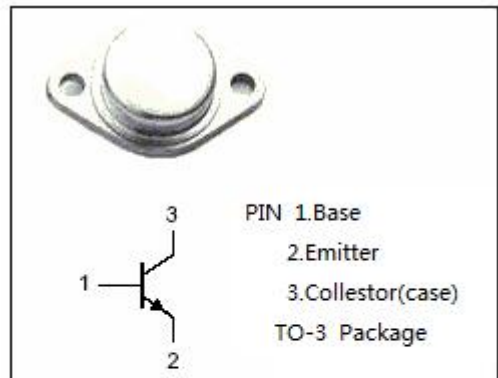


**isc Silicon PNP Power Transistor**
**2N3792**
**DESCRIPTION**

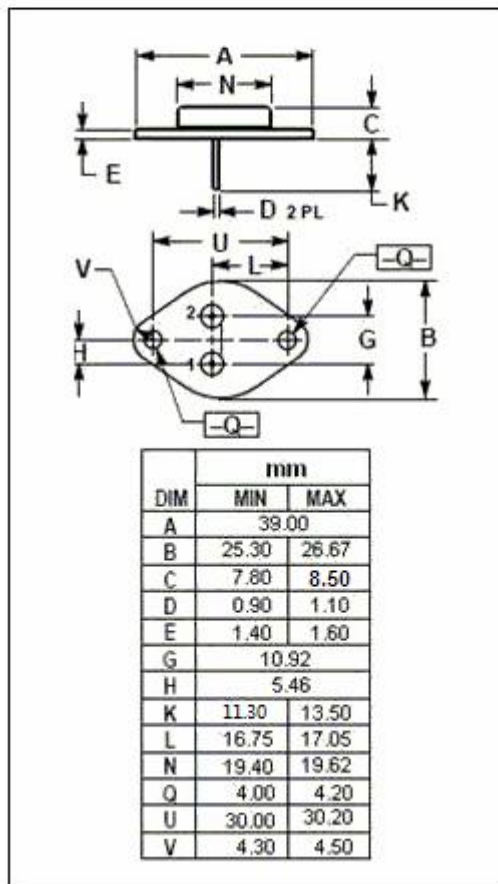
- Excellent Safe Operating Area
- Low Collector-Emitter Saturation Voltage
- 100% tested
- Minimum Lot-to-Lot variations for robust device performance and reliable operation.

**APPLICATIONS**

- Designed for medium-speed switching and amplifier applications.


**ABSOLUTE MAXIMUM RATINGS(T<sub>a</sub>=25°C)**

SYMBOL	PARAMETER	VALUE	UNIT
V <sub>CBO</sub>	Collector-Base Voltage	-80	V
V <sub>CEO</sub>	Collector-Emitter Voltage	-80	V
V <sub>EBO</sub>	Emitter-Base Voltage	-7	V
I <sub>c</sub>	Collector Current-Continuous	-10	A
P <sub>c</sub>	Collector Power Dissipation@T <sub>c</sub> =25°C	150	W
T <sub>J</sub>	Junction Temperature	-65~200	°C
T <sub>stg</sub>	Storage Temperature	-65~200	°C


**THERMAL CHARACTERISTICS**

SYMBOL	PARAMETER	MAX	UNIT
R <sub>th j-c</sub>	Thermal Resistance, Junction to Case	1.17	°C/W

## isc Silicon PNP Power Transistor

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## ELECTRICAL CHARACTERISTICS

T<sub>C</sub>=25°C unless otherwise specified

SYMBOL	PARAMETER	CONDITIONS	MIN	MAX	UNIT
V <sub>CEO(SUS)</sub> *	Collector-Emitter Sustaining Voltage	I <sub>C</sub> =-200mA; I <sub>B</sub> = 0	-80		V
I <sub>EBO</sub>	Emitter Cutoff Current	V <sub>EB</sub> = -7V; I <sub>C</sub> = 0		-5	mA
V <sub>CE(sat)</sub>	Collector-Emitter Saturation Voltage	I <sub>C</sub> = -5A; I <sub>B</sub> =- 0.5A		-1.0	V
V <sub>BE(ON)-1</sub>	Base-Emitter On Voltage	I <sub>C</sub> =-5A; V <sub>CE</sub> =-2V		-1.8	V
V <sub>BE(ON)-2</sub>	Base-Emitter On Voltage	I <sub>C</sub> =-10A; V <sub>CE</sub> =-4V		-4.0	V
h <sub>FE-1</sub>	DC Current Gain	I <sub>C</sub> = -1A; V <sub>CE</sub> = -2V	50	180	
h <sub>FE-2</sub>	DC Current Gain	I <sub>C</sub> = -3A; V <sub>CE</sub> = -2V	30		
f <sub>T</sub>	Current Gain-Bandwidth Product	I <sub>C</sub> = -0.5A; V <sub>CE</sub> = -10V; f= 1.0MHz	4		MHz

\*:Pulse test:Pulse width=300us,duty cycle≤2%

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