Power Transistor





Description:

PNP silicon power transistor in a TO-3 package. Designed for medium-speed switching and amplifier applications.

Features:

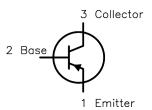
- · Excellent Safe Operating Areas
- hFE (Min) 25 and 50 @ I_C = 1A

Absolute Maximum Ratings

Parameter	Symbol	Rating	Unit		
Collector-Emitter Voltage	V _{CEO}	80			
Collector-Base Voltage	V _{CBO}	80	V		
Emitter-Base Voltage	V _{EBO}	7			
Continuous Collector Current	I _C	10	А		
Base Current Continuous	I _B	4			
Total Device Dissipation at T _c = 25°C Derate above 25°C	P_{D}	150 0.857	W mW/°C		
Operating Junction Temperature Range	T _j	-65 to +200	°C		

RoHS Compliant

PNP



Electrical Characteristics (T_a = 25°C unless otherwise specified)

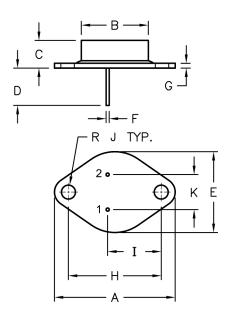
Parameter	Symbol	Test Conditions	Min.	Max.	Unit
OFF Characteristics	•	•			
Collector - Emitter Breakdown Voltage (Note 1)	V _{(BR)CEO}	I _C =200mA, I _B =0	80	-	V
Collector Cut-Off Current	I _{CEX}	V _{CE} =80V, V _{EB(off)} =1.5V	-	1	mA
Emitter Cut-Off Current	I _{EBO}	V _{EB} =7V, I _C =0	-	5	IIIA
ON Characteristics (Note 1)					
DC Current Gain	h _{FE}	V_{CE} =2V, I_{C} =1A	50	180	-
		V_{CE} =2V, I_{C} =3A	30	-	-
Collector - Emitter Saturation Voltage	V _{CE(sat)}	I _C =5A, I _B =0.5A	-	1	
Base-Emitter On Voltage		I _C =5A, I _B =2V	-	1.8	V
	V _{BE(on)}	I _C =10A, I _B =4V	-	4	
Small-Signal Characteristics					
Current Gain-Bandwidth Product	f _T	V _{CE} =10V, I _C =500mA, f=1MHz	4	_	MHz

Note 1. Plus Test: Pulse Width = 300 μ s, Duty Cycle \leq 2%.



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Dimensions	Min.	Max.
Α	38.75	39.96
В	19.28	22.23
С	7.96	9.23
D	11.18	12.19
E	25.2	26.67
F	0.92	1.09
G	1.38	1.62
Н	29.9	30.4
I	16.64	17.3
J	3.88	4.36
K	10.67	11.18

Dimensions: Millimetres

Pin Configuration:

- 1. Base
- 2. Emitter
- 3. Collector (Case)

Part Number Table

Description	Part Number		
Transistor, TO-3	2N3792		

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