2N3878



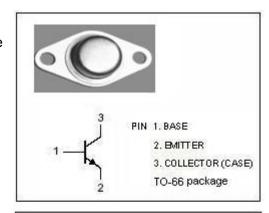
# isc Silicon NPN Power Transistor

### **DESCRIPTION**

- Excellent Safe Operating Area
- · Low Collector-Emitter Saturation Voltage
- 100% avalanche tested
- Minimum Lot-to-Lot variations for robust device performance and reliable operation.

### **APPLICATIONS**

· Designed for high speed switching and linear- amplifier applications.

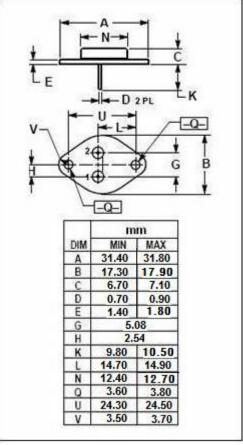


# ABSOLUTE MAXIMUM RATINGS(Ta=25℃)

SYMBOL	PARAMETER	VALUE	UNIT
$V_{CBO}$	Collector-Base Voltage	120	V
V <sub>CEO</sub>	Collector-Emitter Voltage	50	V
V <sub>EBO</sub>	Emitter-Base Voltage 7		V
Ic	Collector Current-Continuous 4		А
Pc	Collector Power Dissipation@Tc=25°C 35		W
TJ	Junction Temperature -65~200		$^{\circ}$ C
T <sub>stg</sub>	Storage Temperature -65~200		$^{\circ}\!\mathbb{C}$

## THERMAL CHARACTERISTICS

SYMBOL	PARAMETER	MAX	UNIT
R <sub>th j-c</sub>	Thermal Resistance,Junction to Case	5	°C/W





# **isc Silicon NPN Power Transistor**

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#### **ELECTRICAL CHARACTERISTICS**

T<sub>C</sub>=25℃ unless otherwise specified

SYMBOL	PARAMETER	CONDITIONS	MIN	MAX	UNIT
V <sub>CEO(SUS)</sub> *	Collector-Emitter Sustaining Voltage	I <sub>C</sub> =200mA; I <sub>B</sub> = 0	50		V
I <sub>EBO</sub>	Emitter Cutoff Current	V <sub>EB</sub> = 7V; I <sub>C</sub> = 0		10	mA
V <sub>CE(sat)</sub>	Collector-Emitter Saturation Voltage	I <sub>C</sub> = 4A; I <sub>B</sub> = 0.4A		2.0	V
V <sub>BE(ON)</sub>	Base-Emitter On Voltage	I <sub>C</sub> =4A;V <sub>CE</sub> = 2V		2.5	V
h <sub>FE-1</sub> *	DC Current Gain	I <sub>C</sub> = 0.5A; V <sub>CE</sub> = 2V	40	200	
h <sub>FE-2</sub> *	DC Current Gain	I <sub>C</sub> = 4A; V <sub>CE</sub> = 2V	8		
h <sub>FE-3</sub> *	DC Current Gain	I <sub>C</sub> = 4A; V <sub>CE</sub> = 5V	20		
h <sub>FE-4</sub> *	DC Current Gain	I <sub>C</sub> = 0.5A; V <sub>CE</sub> = 5V	50	200	

<sup>\*:</sup>Pulse test:Pulse width=300us,duty cycle≤2%

### **NOTICE:**

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