

NPN High Power Silicon Transistors

Rev. V1

Features

- Available in JAN, JANTX, JANTXV per MIL-PRF-19500/371
- TO-3 (TO-204AA) Package



Electrical Characteristics

| Parameter | Test Conditions | Symbol | Units | Min. | Max. | |
|--|--|----------------------|-------|---------------------|------------|--|
| Off Characteristics | | | | | | |
| Collector - Emitter Cutoff Current | V _{CE} = 400 Vdc, 2N3902 V _{CE} = 500 Vdc, 2N5157 | I _{CEO} | μAdc | _ | 100 100 | |
| Collector - Emitter Cutoff Current | V _{BE} = 1.5 Vdc, V _{CE} = 700 Vdc | I _{CEX} | μAdc | _ | 20 | |
| Collector - Emitter Cutoff Current | V _{BE} = 5 Vdc, 2N3902 V _{BE} = 6 Vdc, 2N5157 | I _{EBO} | μAdc | _ | 200 200 | |
| On Characteristics ¹ | | | | | | |
| Forward Current Transfer Ratio | $\begin{split} I_{C} &= 0.5 \text{ Adc, V}_{CE} = 5 \text{ Vdc} \\ I_{C} &= 1.0 \text{ Adc, V}_{CE} = 5 \text{ Vdc} \\ I_{C} &= 2.5 \text{ Adc, V}_{CE} = 5 \text{ Vdc} \\ I_{C} &= 3.5 \text{ Adc, V}_{CE} = 5 \text{ Vdc} \end{split}$ | H _{FE} | - | 25 30 10 5 | 90 | |
| Collector - Emitter Saturation Voltage | I _C = 1.0 Adc, I _B = 0.1 Adc I _C = 3.5 Adc, I _B = 0.7 Adc | V _{CE(SAT)} | Vdc | _ | 0.8 2.5 | |
| Base - Emitter Saturation Voltage | I_C = 1.0 Adc, I_B = 0.1 Adc I_C = 3.5 Adc, I_B = 0.7 Adc | V _{CE(SAT)} | Vdc | _ | 1.5 2.0 | |
| Dynamic Characteristics | | | | | | |
| Small-Signal Short-Circuit Forward Current Transfer Ratio | $I_{C} = 0.2 \text{ Adc}, V_{CE} = 10 \text{ Vdc}, f = 1 \text{ MHz}$ | H _{FE} | - | 2.5 | 25 | |
| Output Capacitance | $V_{CB} = 10 \text{ Vdc}, I_{E} = 0, 100 \text{ kHz} \le f \le 1 \text{ MHz}$ | C _{OBO} | pF | _ | 250 | |

^{1.} Pulse Test: Pulse Width = 300 µs, Duty Cycle ≤2.0%.

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| Parameter | Test Conditions | Symbol | Units | Min. | Max. |
|---------------------------|---|------------------|-------|------|------|
| Switching Characteristics | | | | | |
| Turn-On Time | V_{CC} = 125 Vdc; I_{C} = 1.0 Adc; $I_{B}1$ = 0.1 Adc | T _{ON} | μs | _ | 0.8 |
| Turn-Off Time | V_{CC} = 125 Vdc; I_{C} = 1.0 Adc; $I_{B}1$ = 0.1 Adc, $-I_{B}2$ = 0.50 Adc | T _{OFF} | μs | _ | 1.7 |

Safe Operating Area

DC Tests: $T_C = +25^{\circ}C$, I Cycle, t = 1.0 s (see Fig. 3 of MIL-PRF-19500/371)

Test 1: $V_{CE} = 28.6 \text{ Vdc}, I_{C} = 3.5 \text{ Adc}$ Test 2: $V_{CF} = 70 \text{ Vdc}, I_{C} = 1.43 \text{ Adc}$

 V_{CF} = 325 Vdc, I_{C} = 55 mAdc, 2N3902 Test 3: V_{CE} = 400 Vdc, I_{C} = 35 mAdc, 2N5157

Switching Tests:

Load Condition C (unclamped inductive load): $T_C = +25^{\circ}$ C, duty cycle <10%; $R_S = 0.1 \Omega$ (See Fig. 4 of MIL-PRF-19500/371)

Test 1: tP = approximately 3 ms (vary to obtain I_C), R_{BB1} = 20 Ω, V_{BB1} = 10 Vdc; R_{BB2} = 3 kΩ,

 V_{BB2} = 1.5 Vdc, V_{CC} = 50 Vdc, I_C = 3.5 Adc, L = 60 mH, R = 3 Ω ; R_L <14 Ω Test 2: tP = approximately 3 ms (vary to obtain I_C), R_{BB1} = 100 Ω, V_{BB1} = 10 Vdc; R_{BB2} = 3 kΩ,

 $V_{BB2} = 1.5 \text{ Vdc}, I_{C} = 0.6 \text{ Adc}, V_{CC} = 50 \text{ Vdc}, L = 200 \text{ mH}, R = 8 \Omega; R_{L} < 83 \Omega$

Load Condition (clamped inductive load): $T_C = +25^{\circ}C$, duty cycle <10% (See Fig. 5 of MIL-PRF-19500/371)

Test 1: tP = approximately 30 ms (vary to obtain I_C), $R_S = 0.1 \Omega$, $R_{BB1} = 20 \Omega$, $V_{BB1} = 10 \text{ Vdc}$;

 $R_{BB2} = 100 \Omega$, $V_{BB2} = 1.5 \text{ Vdc}$, $V_{CC} = 50 \text{ Vdc}$, $I_{C} = 3.5 \text{ Adc}$, L = 60 mH, $R = 3 \Omega$; $R_{L} < 0 \Omega$ (A suitable clamping circuit or diode can be used.) Clamp Voltage = 400 +0, -5 Vdc

Clamp Voltage = 500 +0, -5 Vdc 2N5157

(Clamped voltage must be reached)



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Absolute Maximum Ratings

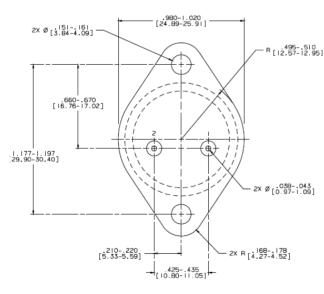
| Ratings | Symbol | Value |
|--|------------------------------------|--------------------|
| Collector - Emitter Voltage 2N3902 2N5157 | V _{CEO} | 400 Vdc 500 Vdc |
| Emitter - Base Voltage 2N3902 2N5157 | V _{EBO} | 5 Vdc 6 Vdc |
| Collector - Base Voltage | V_{CBO} | 700 Vdc |
| Base Current | I _B | 2.0 Adc |
| Collector Current | Ic | 3.5 Adc |
| Total Power Dissipation @ $T_A = +25^{\circ}C^2$ @ $T_A = +25^{\circ}C^3$ | P _T | 5 W 100 W |
| Operating & Storage Temperature Range | T _{OP} , T _{STG} | -65°C to +200°C |

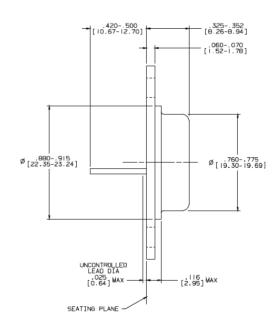
- 2. Derate linearly @ 28.57 mW / °C for T_A >+25°C.
- 3. Derate linearly @ 0.8 mW / °C for T_A>+75°C.

Thermal Characteristics

| Characteristics | Symbol | Max. Value |
|--------------------------------------|-----------------|------------|
| Thermal Resistance, Junction to Case | $R_{\theta JC}$ | 1.25°C/W |

Outline Drawing





Notes:

Dimensions in inches [mm]

Standard header type solid base.

Standard lead finish: per MIL-M-38510 type x or equivalent.

Lead not bent >15°

Dimensions based on JEDEC standard TO-3 publication 95, PA

2N3902 & 2N5157



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