

## isc Silicon PNP Power Transistor

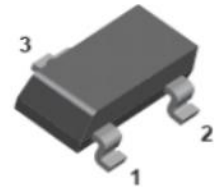
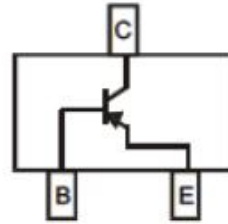
## 2N3906S

### DESCRIPTION

- Low voltage( max .40V )
- Low current ( max .200mA )

### APPLICATIONS

- Designed for high-speed switching
- Amplifier applications.



SOT-23

### ABSOLUTE MAXIMUM RATINGS( $T_a=25^{\circ}\text{C}$ )

SYMBOL	PARAMETER	VALUE	UNIT
$V_{CBO}$	Collector-Base Voltage	-40	V
$V_{CEO}$	Collector-Emitter Voltage	-40	V
$V_{EBO}$	Emitter-Base Voltage	-5	V
$I_C$	Collector Current-Continuous	-200	mA
$P_{tot}$	Total Power Dissipation	300	mW
$T_J$	Junction Temperature	-55~150	$^{\circ}\text{C}$
$T_{stg}$	Storage Temperature	-55~150	$^{\circ}\text{C}$

### THERMAL CHARACTERISTICS

SYMBOL	PARAMETER	MAX	UNIT
$R_{th\ j-a}$	thermal resistance from junction to ambient	250	K/W

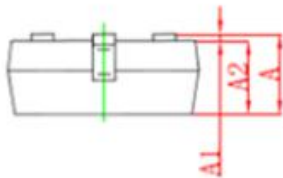
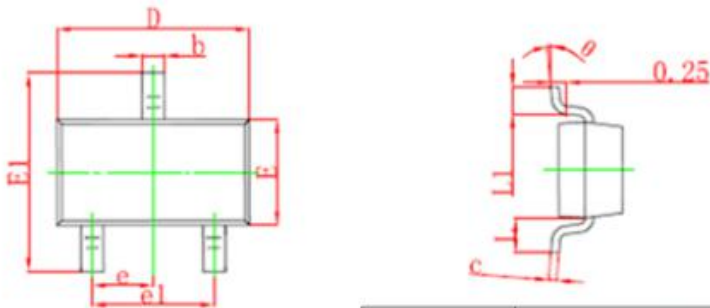
**isc Silicon PNP Power Transistor****2N3906S****ELECTRICAL CHARACTERISTICS** $T_C=25^\circ\text{C}$  unless otherwise specified

SYMBOL	PARAMETER	CONDITIONS	MIN	MAX	UNIT
$V_{(BR)CBO}$	Collector-base breakdown voltage	$I_C=-10\text{mA}$ , $I_E=0$	-40		V
$V_{(BR)CEO}$	Collector-emitter breakdown voltage	$I_C= -1\text{mA}$ , $I_B=0$	-40		V
$V_{(BR)EBO}$	Emitter-base breakdown voltage	$I_E= -10\text{mA}$ , $I_C=0$	-5		V
$V_{CE(sat)-1}$	Collector-Emitter Saturation Voltage	$I_C= -10\text{mA}$ ; $I_B= -1\text{mA}$		-0.25	V
$V_{CE(sat)-2}$	Collector-Emitter Saturation Voltage	$I_C= -50\text{mA}$ ; $I_B= -5\text{mA}$		-0.4	V
$V_{BE(sat)1}$	base-emitter saturation voltage	$I_C= -10\text{mA}$ ; $I_B=-1\text{mA}$		-0.85	V
$V_{CE(sat)2}$	Collector-Emitter Saturation Voltage	$I_C= -50\text{mA}$ ; $I_B= -5\text{mA}$		-0.95	V
$I_{CBO}$	collector cut-off current	$V_{CB} = -40\text{V}$ , $I_E = 0$		-0.1	$\mu\text{A}$
$I_{EBO}$	Emitter Cutoff Current	$V_{EB} = -5\text{V}$ ; $I_C=0$		-0.1	$\mu\text{A}$
$h_{FE-1}$	DC Current Gain	$I_C= -0.1\text{mA}$ ; $V_{CE}=-1\text{V}$	60		
$h_{FE-2}$	DC Current Gain	$I_C=-1\text{mA}$ ; $V_{CE}=-1\text{V}$	80		
$h_{FE-3}$	DC Current Gain	$I_C= -10\text{mA}$ ; $V_{CE}=-1\text{V}$	100	300	
$h_{FE-4}$	DC Current Gain	$I_C= -50\text{mA}$ ; $V_{CE}=-1\text{V}$	60		
$h_{FE-5}$	DC Current Gain	$I_C=-100\text{mA}$ ; $V_{CE}=-1\text{V}$	30		
$C_{ob}$	Output Capacitance	$V_{CB}=-5.0\text{Vdc}$ , $f=1.0\text{MHz}$ , $I_E=0$		4.5	pF
NF	Noise Figure	$V_{CE}=-5.0\text{V}$ , $f=1.0\text{kHz}$ , $I_C=100\mu\text{A}$ , $R_S=1.0\text{K}$		4.0	dB

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■SOT-23 Package Outline Dimensions



Symbol	Dimensions In Millimeters		Dimensions In Inches	
	Min	Max	Min	Max
A	0.900	1.150	0.035	0.045
A1	0.000	0.100	0.000	0.004
A2	0.900	1.050	0.035	0.041
b	0.300	0.500	0.012	0.020
c	0.100	0.200	0.004	0.008
D	2.800	3.000	0.110	0.118
E	1.200	1.400	0.047	0.055
E1	2.250	2.550	0.089	0.100
e	0.950TYP		0.037TYP	
e1	1.800	2.000	0.071	0.079
L	0.550REF		0.022REF	
L1	0.300	0.500	0.012	0.020
°	0°	8°	0°	8°

■SOT-23Suggested Pad Layout

