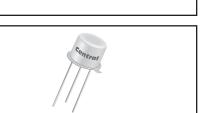
SILICON NPN TRANSISTORS

TO-39 CASE





www.centralsemi.com

DESCRIPTION:

The CENTRAL SEMICONDUCTOR 2N4237, 2N4238, and 2N4239 are silicon NPN transistors mounted in a hermetically sealed metal case, designed for power amplifier, power driver, and switching power supply applications.

MARKING: FULL PART NUMBER

MAXIMUM RATINGS:	T _C =25°C unless	otherwise noted)
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	SYMBOL	<u>2N4237</u>	<u>2N4238</u>	<u>2N4239</u>	UNITS
Collector-Base Voltage	V_{CBO}	50	80	100	V
Collector-Emitter Voltage	V_{CEO}	40	60	80	V
Emitter-Base Voltage	V_{EBO}		6.0		V
Continuous Collector Current	I_{C}		3.0		Α
Continuous Base Current	ΙΒ		0.5		Α
Power Dissipation	P_{D}		6.0		W
Operating and Storage Junction Temperature	T _J , T _{stg}	-	65 to +200)	°C
Thermal Resistance	ΘJC		29.2		°C/W

ELECTRICAL CHARACTERISTICS: ((T _C =25°C unless otherwise noted)
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TEST CONDITIONS	MIN	MAX	UNITS
V _{CB} =Rated V _{CBO}		100	μA
V _{CE} =45V, V _{EB} =1.5V (2N4237)		100	μΑ
V _{CE} =75V, V _{EB} =1.5V (2N4238)		100	μΑ
V _{CE} =90V, V _{EB} =1.5V (2N4239)		100	μΑ
V_{CE} =30V, V_{EB} =1.5V, T_{C} =150°C (2N4237)		1.0	mA
V_{CE} =50V, V_{EB} =1.5V, T_{C} =150°C (2N4238)		1.0	mA
V _{CE} =70V, V _{EB} =1.5V, T _C =150°C (2N4239)		1.0	mA
V _{CE} =Rated V _{CEO}		700	μΑ
V _{EB} =6.0V		500	μΑ
I _C =100mA (2N4237)	40		V
I _C =100mA (2N4238)	60		V
I _C =100mA (2N4239)	80		V
I _C =500mA, I _B =50mA		0.3	V
I _C =1.0A, I _B =0.1A		0.6	V
I _C =1.0A, I _B =0.1A		1.5	V
V _{CE} =1.0V, I _C =250mA		1.0	V
	TEST CONDITIONS V _{CB} =Rated V _{CBO} V _{CE} =45V, V _{EB} =1.5V (2N4237) V _{CE} =75V, V _{EB} =1.5V (2N4238) V _{CE} =90V, V _{EB} =1.5V (2N4239) V _{CE} =30V, V _{EB} =1.5V, T _C =150°C (2N4237) V _{CE} =50V, V _{EB} =1.5V, T _C =150°C (2N4238) V _{CE} =70V, V _{EB} =1.5V, T _C =150°C (2N4239) V _{CE} =Rated V _{CEO} V _{EB} =6.0V I _C =100mA (2N4237) I _C =100mA (2N4238) I _C =100mA (2N4239) I _C =500mA, I _B =50mA I _C =1.0A, I _B =0.1A I _C =1.0A, I _B =0.1A	TEST CONDITIONS V _{CB} =Rated V _{CBO} V _{CE} =45V, V _{EB} =1.5V (2N4237) V _{CE} =75V, V _{EB} =1.5V (2N4238) V _{CE} =90V, V _{EB} =1.5V, T _C =150°C (2N4237) V _{CE} =30V, V _{EB} =1.5V, T _C =150°C (2N4237) V _{CE} =50V, V _{EB} =1.5V, T _C =150°C (2N4238) V _{CE} =70V, V _{EB} =1.5V, T _C =150°C (2N4239) V _{CE} =Rated V _{CEO} V _{EB} =6.0V I _C =100mA (2N4237) I _C =100mA (2N4238) I _C =100mA (2N4239) I _C =500mA, I _B =50mA I _C =1.0A, I _B =0.1A I _C =1.0A, I _B =0.1A	VCB=Rated VCBO 100 VCE=45V, VEB=1.5V (2N4237) 100 VCE=75V, VEB=1.5V (2N4238) 100 VCE=90V, VEB=1.5V (2N4239) 100 VCE=30V, VEB=1.5V, TC=150°C (2N4237) 1.0 VCE=50V, VEB=1.5V, TC=150°C (2N4238) 1.0 VCE=70V, VEB=1.5V, TC=150°C (2N4239) 1.0 VCE=Rated VCEO 700 VEB=6.0V 500 IC=100mA (2N4237) 40 IC=100mA (2N4238) 60 IC=100mA (2N4239) 80 IC=500mA, IB=50mA 0.3 IC=1.0A, IB=0.1A 0.6 IC=1.0A, IB=0.1A 1.5

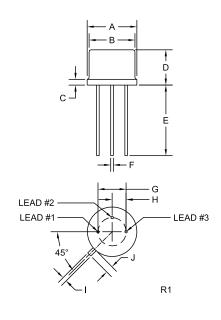
R3 (26-March 2015)





 $\textbf{ELECTRICAL CHARACTERISTICS - Continued:} \ (T_{\hbox{\scriptsize C}} = 25 ^{\circ} \hbox{\scriptsize C unless otherwise noted})$ TEST CONDITIONS SYMBOL MIN UNITS V_{CE} =1.0V, I_{C} =50mA 30 hFE V_{CE}=1.0V, I_C=250mA 30 250 hFE V_{CE} =1.0V, I_{C} =500mA 30 h_{FE} V_{CE}=1.0V, I_C=1.0A 15 hFE V_{CE} =10V, I_{C} =100mA, f=1.0kHz 30 h_{fe} V_{CE} =10V, I_{C} =100mA, f=1.0kHz 2.0 f_T MHz V_{CB} =10V, I_E =0, f=100kHz рF C_{ob} 100

TO-39 CASE - MECHANICAL OUTLINE



DIMENSIONS					
	INCHES		MILLIMETERS		
SYMBOL	MIN	MAX	MIN	MAX	
A (DIA)	0.335	0.370	8.51	9.40	
B (DIA)	0.315	0.335	8.00	8.51	
С	-	0.040	-	1.02	
D	0.240	0.260	6.10	6.60	
Е	0.500	-	12.70	-	
F (DIA)	0.016	0.021	0.41	0.53	
G (DIA)	0.200		5.08		
Н	0.100		2.54		
	0.028	0.034	0.71	0.86	
J	0.029	0.045	0.74	1.14	

TO-39 (REV: R1)

LEAD CODE:

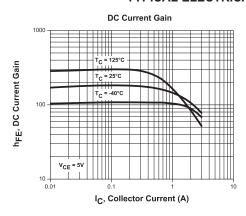
- 1) Emitter
- 2) Base
- 3) Collector

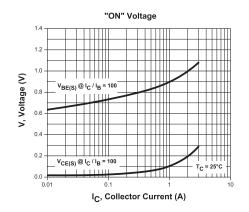
MARKING: FULL PART NUMBER

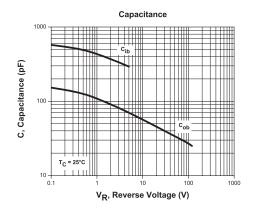
SILICON NPN TRANSISTORS

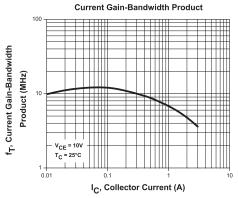


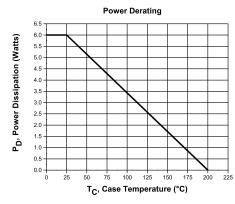
TYPICAL ELECTRICAL CHARACTERISTICS











R3 (26-March 2015)





SERVICES

- · Bonded Inventory
- · Custom Electrical Screening
- Custom Electrical Characteristic Curves
- SPICE Models
- Custom Packaging
- Package Base Options
- Custom Device Development/Multi Discrete Modules (MDM™)
- · Bare Die Available for Hybrid Applications

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Buyer understands and agrees that the foregoing liability limitations are essential elements of a purchase order or contract and that in the absence of such limitations, the material and economic terms of the purchase order or contract would be substantially different.

R3 (26-March 2015)