Unijunction Transistor

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Description:



A PN unijunction transistor in a TO–92 type package designed for use in pulse and timing circuits, sensing circuits and thyristor trigger circuits

Absolute maximum Ratings:

(T_A = +25°C unless otherwise specified)

Power Dissipation, PD	: 300mW
Derate Above 25°C	: 3.0mW/°C
RMS Emitter Current, IE(RMS)	: 50mA
Peak Pulse Emitter Current (Note 1) Current, i∈	: 1.5A
Emitter Reverse Voltage, VB2E	: 30V
Interbase Voltage, VB2B1	: 35V
Operating Junction Temperature Range, TJ	: -65°C to +125°C
Storage Temperature Range, Tstg	: -65°C to +150°C

Electrical Characteristics: (T_A = +25°C Unless otherwise specified)

Parameter	Symbol	Test Conditions	Min	Тур	Мах	Unit
Instrinsic Standoff Ratio		V _{B2B1} = 10V, Note3	0.70	-	0.85	-
Interbase Resistance	г вв		4.0	6.0	9.1	kΩ
Interbase Resistance Temperature Coefficient			0.1	-	0.9	%/°C
Emitter Saturation Voltage	VEB1(sat)	V _{B2B1} = 10V, I _E = 50mA, Note 4	-	2.5	-	V
Modulated interbase Current	B2(mod)	V _{B2B1} = 10V, IE = 50mA	-	15	-	mA
Emitter Reverse Current	IEB20	V _{B2E} = 30V, I _{B1} = 0	-	0.005	1	μA
Peak Point Emitter Current	lР	V _{B2B1} = 25V	-	1	5	μA
Valley Point Current	lv	V _{B2B1} = 20V, R _{B2} = 100Ω, Note 4	4	7	-	mA
Base-One Peak Pulse Voltage	V _{OB1}		5	8	-	V

Notes:

- 1. Duty Cycle ≦1% PRR = 10PPS.
- 2. Based upon power dissipation at TA = +25°C
- 3. Intrinsic standoff ratio is essentially constant with temperature and interbase voltage and is defined by the equation:
 - $V_P V_{BB} + V_D$
 - Where: VP = Peak Point Emitter Voltage; VBB = interbase Voltage;

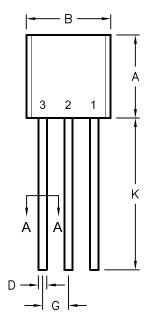
 V_D = Junction Diode Drop (~0.5V).

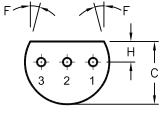
 Use Pulse techniques: Pulse width ~ 300µS, Duty Cycle ≤2% to avoid internal heating due to interbase modulation which may result in erroneous readings.

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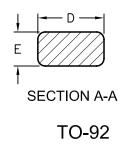


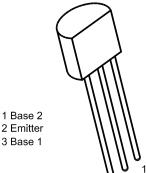


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Dim	Min	Max		
А	4.32	.32 5.33		
В	4.45	5.2		
С	3.18	4.19		
D	0.41	.41 0.55		
Е	0.35	0.5		
F	5	;0		
G	1.14 1.4			
Н	1.14	1.53		
К	12.7			

Dimensions : Millimetres





Part Number Table

Description	Part Number	
Unijunction Transistor, TO-92, PN	2N4871	

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