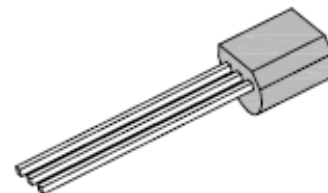


Small Signal Low Noise Transistors (NPN)

Features

- NPN silicon epitaxial transistor for switching and amplifier applications
- This device is designed for low noise, high gain, general purpose applications at collector currents from 1 μ A to 50mA
- RoHS compliance



TO-92



Mechanical Data

Case:	TO-92, Plastic Package
Terminals:	Solderable per MIL-STD-202G, Method 208
Weight:	0.18 gram

Absolute Maximum Ratings* ($T_{Ambient}=25^{\circ}C$ unless noted otherwise)

Symbol	Description	2N5088	2N5089	Unit
V_{CEO}	Collector-Emitter Voltage	30	25	V
V_{CBO}	Collector-Base Voltage	35	30	V
V_{EBO}	Emitter-Base Voltage	4.5		V
I_c	Collector Current Continuous	100		mA
T_J, T_{STG}	Operation and Storage Junction Temperature Range	-55 to +150		$^{\circ}C$

* These ratings are limiting values above which the serviceability of any semiconductor device may be impaired.

Notes

1. These ratings are based on a maximum junction temperature of 150 degrees C.
2. These are steady state limits. The factory should be consulted on applications involving pulsed or low duty cycle operations.

Small Signal Low Noise Transistors (NPN)

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Thermal Characteristics ($T_{Ambient}=25^{\circ}\text{C}$ unless noted otherwise)

Symbol	Description	2N5088	2N5089	Unit
P_D	Power Dissipation at $T_A=25^{\circ}\text{C}$	625		mW
	Derate above 25°C	5.0		mW/ $^{\circ}\text{C}$
P_D	Power Dissipation at $T_C=25^{\circ}\text{C}$	1.5		W
	Derate above 25°C	12		mW/ $^{\circ}\text{C}$
R_{θJA}	Thermal Resistance Junction to Ambient Air (Note 1)	357		$^{\circ}\text{C/W}$
R_{θJC}	Thermal Resistance Junction to Case	125		$^{\circ}\text{C/W}$
T_J, T_{STG}	Operation and Storage Junction Temperature Range	-55 to +150		$^{\circ}\text{C}$

Note: 1. Measured with the device soldered into a typical printed circuit board.

Electrical Characteristics ($T_{Ambient}=25^{\circ}\text{C}$ unless noted otherwise)

Off Characteristics

Symbol	Description	2N5088		2N5089		Unit	Conditions
		Min.	Max.	Min.	Max.		
V_{(BR)CBO}	Collector-Base Breakdown Voltage	35	-	30	-	V	$I_C=100\mu\text{A}$, $I_E=0$
V_{(BR)CEO*}	Collector-Emitter Breakdown Voltage	30	-	25	-	V	$I_C=1\text{mA}$, $I_B=0$
I_{CBO}	Collector Cut-Off Current	-	50	-	50	nA	
		V _{CB} =20V, I _E =0		V _{CB} =15V, I _E =0			
I_{EBO}	Emitter Cut-Off Current	-	50	-	100	nA	
		V _{EB} =3V, I _C =0		V _{EB} =4.5V, I _C =0			

Small Signal Low Noise Transistors (NPN)

2N5088/2N5089

On Characteristics

Symbol	Description	2N5088		2N5089		Unit	Conditions
		Min.	Max.	Min.	Max.		
h_{FE}	D.C. Current Gain	300	900	400	1200		V _{CE} =5V, I _C =0.1mA
		350	-	450	-		V _{CE} =5V, I _C =1mA
		300	-	400	-		*V _{CE} =5V, I _C =10mA
V_{CE(sat)}	Collector Emitter Saturation Voltage	-	0.5	-	0.5	V	I _C =10mA, I _B =1mA
V_{BE(on)*}	Base Emitter Saturation Voltage	-	0.8	-	0.8	V	I _C =10mA, V _{CE} =5V

Small signal Characteristics

Symbol	Description	2N5088		2N5089		Unit	Conditions
		Min.	Max.	Min.	Max.		
f_T	Current Gain-Bandwidth Product	50	-	50	-	MHz	V _{CE} =5V, I _C =0.5mA, f=20MHz
C_{cb}	Collector-Base Capacitance	-	4.0	-	4.0	pF	V _{CB} =5V, f=100KHz
C_{eb}	Emitter-Base Capacitance	-	10	-	10	pF	V _{EB} =0.5V, f=100KHz
h_{fe}	Small Signal Current Gain	350	1400	450	1800		V _{CE} =5V, I _C =1mA f=1KHz
NF	Noise Figure	-	3.0	-	2.0	dB	V _{CE} =5V, I _C =100μA, R _s =10KΩ, f=10Hz to 15.7 KHz

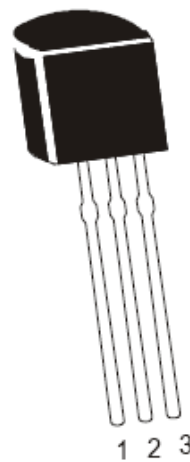
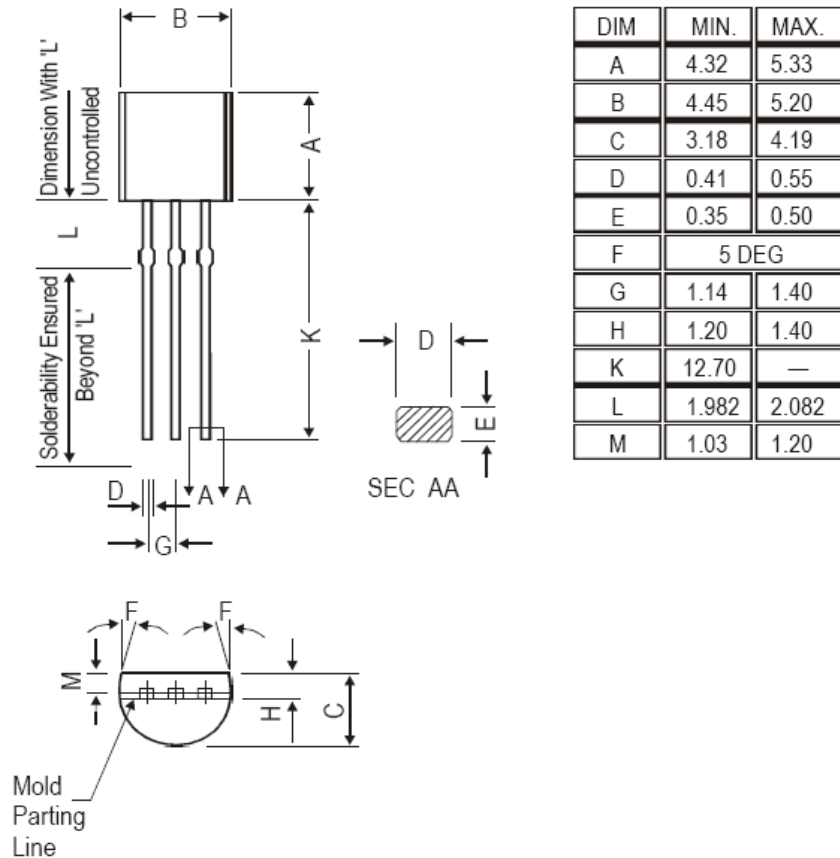
*Pulse Test: Pulse Width ≤ 300μs, Duty Cycle ≤ 2%

Small Signal Low Noise Transistors (NPN)

2N5088/2N5089

Dimensions in mm

TO-92



PIN CONFIGURATION
 1. EMITTER
 2. BASE
 3. COLLECTOR

Small Signal Low Noise Transistors (NPN)

2N5088/2N5089

How to contact us:

US HEADQUARTERS

28040 WEST HARRISON PARKWAY, VALENCIA, CA 91355-4162

Tel: (800) TAITRON (800) 824-8766 (661) 257-6060

Fax: (800) TAITFAX (800) 824-8329 (661) 257-6415

Email: taitron@taitroncomponents.com

Http://www.taitroncomponents.com

TAITRON COMPONENTS MEXICO, S.A .DE C.V.

BOULEVARD CENTRAL 5000 INTERIOR 5 PARQUE INDUSTRIAL ATITALAQUIA, HIDALGO C.P.
42970 MEXICO

Tel: +52-55-5560-1519

Fax: +52-55-5560-2190

TAITRON COMPONENTS INCORPORATED REPRESENTAÇÕES DO BRASIL LTDA

RUA DOMINGOS DE MORAIS, 2777, 2.ANDAR, SALA 24 SAÚDE - SÃO PAULO-SP 04035-001 BRAZIL

Tel: +55-11-5574-7949

Fax: +55-11-5572-0052

TAITRON COMPONENTS INCORPORATED, SHANGHAI REPRESENTATIVE OFFICE

METROBANK PLAZA, 1160 WEST YAN' AN ROAD, SUITE 1503, SHANGHAI, 200052, CHINA

Tel: +86-21-5424-9942

Fax: +86-21-5424-9931