### **NPN Power Silicon Transistor**

#### Features

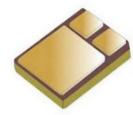
- JANS and JANSR Qualified to MIL-PRF-19500/544
- JEDEC Registered 2N5153
- Lightweight & Low Power
- Ideal for Space, Military, and Other High Reliability Applications
- Surface Mount U3 Package

#### **Electrical Characteristics**

Parameter	Test Conditions	Symbol	Units	Min.	Max.
Off Characteristics	1				
Collector - Emitter Breakdown Voltage	$I_{\rm C}$ = 100 mAdc, $I_{\rm B}$ = 0	V <sub>(BR)CEO</sub>	Vdc	80	
Emitter - Base Cutoff Current	$V_{EB} = 4.0 \text{ Vdc}, I_{C} = 0$ $V_{EB} = 5.5 \text{ Vdc}, I_{C} = 0$	I <sub>EBO</sub>	µAdc mAdc	_	1.0 1.0
Collector - Emitter Cutoff Current	$V_{CE}$ = 60 Vdc, $V_{BE}$ = 0 $V_{CE}$ = 100 Vdc, $V_{BE}$ = 0	I <sub>CES</sub>	µAdc mAdc	—	1.0 1.0
Collector - Emitter Cutoff Current	$V_{CE}$ = 40 Vdc, I <sub>B</sub> = 0	I <sub>CEO</sub>	µAdc	—	50
On Characteristics <sup>1</sup>					
Forward Current Transfer Ratio	$I_{C} = 50 \text{ mAdc}, V_{CE} = 5.0 \text{ Vdc} \\ I_{C} = 2.5 \text{ Adc}, V_{CE} = 5.0 \text{ Vdc} \\ I_{C} = 5.0 \text{ Adc}, V_{CE} = 5.0 \text{ Vdc} $	$H_{FE}$	-	50 70 40	200 —
Collector - Emitter Saturation Voltage	$I_C$ = 2.5 Adc, $I_B$ = 250 mAdc $I_C$ = 5.0 Adc, $I_B$ = 500 mAdc	$V_{CE(SAT)}$	Vdc	—	0.75 1.50
Emitter - Base Voltage Non-Saturation	$I_{C}$ = 2.5 Adc, $V_{CE}$ = 5.0 Vdc	V <sub>BE(ON)</sub>	Vdc	_	1.45
Emitter - Base Saturation Voltage	$I_{C}$ = 2.5 Adc, $I_{B}$ = 250 mAdc $I_{C}$ = 5.0 Adc, $I_{B}$ = 500 mAdc	$V_{BE(SAT)}$	Vdc	_	1.45 2.20
Dynamic Characteristics					
Magnitude of Common Emitter Small-Signal Short-Circuit Forward Current Transfer Ratio	$I_{C}$ = 500 mAdc, $V_{CE}$ = 5.0 Vdc, f = 10 mHz	H <sub>FE</sub>	-	7	
Small-Signal Short-Circuit Forward Current Transfer Ratio	$I_{C}$ = 100 mAdc, $V_{CE}$ = 5.0 Vdc, f = 10 mHz	$H_{FE}$	-	50	
Output Capacitance	$V_{CB}$ = 10 Vdc, $I_E$ = 0, f = 1 MHz	C <sub>OBO</sub>	pF	—	250
Switching Characteristics					
Turn-On Time	$I_{\rm C}$ = 5.0 Adc; $I_{\rm B1}$ = 500 mAdc	$T_{ON}$	μs	—	0.5
Turn-Off Time	R <sub>L</sub> = 6 Ω	$T_{OFF}$	μs	—	1.5
Storage Time	I <sub>B2</sub> = -500 mAdc	Ts	μs	—	1.4
Fall Time	V <sub>BE(OFF)</sub> = 3.7 Vdc	T <sub>f</sub>	μs	_	0.5
Safe Operating Area					
$ \begin{array}{llllllllllllllllllllllllllllllllllll$	.0 Adc 60 mAdc				

1. Pulse Test: Pulse Width = 300  $\mu$ s, Duty Cycle  $\leq$ 2.0%.

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## **NPN Power Silicon Transistor**

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## Absolute Maximum Ratings<sup>2,3</sup>

Ratings	Symbol	Value
Collector - Emitter Voltage	V <sub>CEO</sub>	80 Vdc
Collector - Base Voltage	V <sub>CBO</sub>	100 Vdc
Emitter - Base Voltage	V <sub>EBO</sub>	5.5 Vdc
Collector Current	Ι <sub>C</sub>	2 Adc
Total Power Dissipation (a) $T_A = 25^{\circ}C$ (b) $T_C = 25^{\circ}C$	PT	1.16 W 100 W
Operating & Storage Temperature Range	$T_{OP}, T_{STG}$	-65°C to +200°C

2. Derate linearly 5.7 mW/°C for  $T_A > +25^{\circ}C$ 3. Derate linearly 571 mW/°C for  $T_A > +25^{\circ}C$ 

#### **Thermal Characteristics**

Characteristics	Symbol	Max. Value
Thermal Resistance, Junction to Case	$R_{ extsf{ heta}JC}$	1.75°C/W

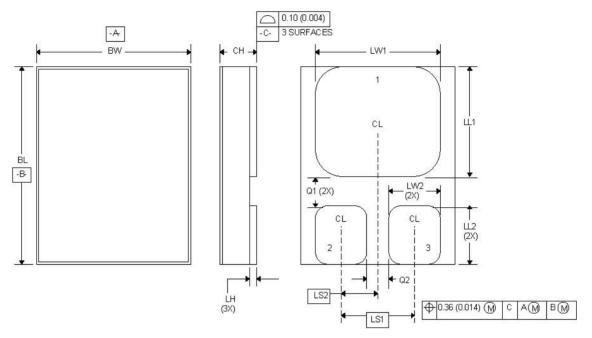
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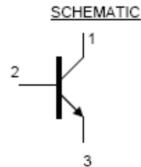
**Outline Drawing (U3)** 



1. Dimensions are in inches.

Millimeters are given for general information only.
In accordance with ASME Y14.5M, diameters are equivalent to φx symbology.

4. Terminal 1 - collector, terminal 2 -base, terminal 3 - emitter.



	Dimensions					
Ltr	Inches		Millimeters			
	Min.	Max.	Min.	Max.		
BL	0.395	0.405	10.03	10.29		
BW	0.291	0.301	7.40	7.65		
СН	0.1085	0.1205	2.76	3.06		
LH	0.010	0.020	0.25	0.51		
LW <sub>1</sub>	0.281	0.291	7.14	7.39		
LW <sub>2</sub>	0.090	0.100	2.29	2.54		
LL1	0.220	0.230	5.59	5.84		
LL <sub>2</sub>	0.115	0.125	2.92	3.18		
LS₁	0.150 BSC		3.81 BSC			
LS <sub>2</sub>	0.075 BSC		1.91 BSC			
<b>Q</b> 1	0.030		0.762			
Q <sub>2</sub>	0.030		0.762			

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