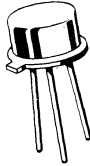


2N5160 (SILICON)



PNP silicon RF power transistors designed for amplifier, frequency multiplier or oscillator applications in military and industrial equipment. Suitable for use as Class A, B, or C output driver, or pre-driver stages in VHF and UHF.

CASE 79 (TO-39)

Collector connected to case



STYLE 1
PIN 1. EMITTER
2. BASE
3. COLLECTOR

MAXIMUM RATINGS

Rating	Symbol	Value	Unit
Collector-Emitter Voltage	V_{CEO}	40	Vdc
Collector-Base Voltage	V_{CB}	60	Vdc
Emitter-Base Voltage	V_{EB}	4.0	Vdc
Collector Current	I_C	0.4	Adc
Total Device Dissipation @ $T_C = 25^\circ\text{C}$ Derate above 25°C	P_D	5.0 28.6	Watts mW/ $^\circ\text{C}$
Operating and Storage Junction Temperature Range	T_J, T_{stg}	-65 to +200	$^\circ\text{C}$

ELECTRICAL CHARACTERISTICS ($T_A = 25^\circ\text{C}$ unless otherwise noted)

Characteristic	Symbol	Min	Typ	Max	Unit
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OFF CHARACTERISTICS

Collector-Emitter Sustaining Voltage ($I_C = 5.0 \text{ mAdc}, I_B = 0$)	$V_{CEO(sus)}$	40	-	-	Vdc
Emitter-Base Breakdown Voltage ($I_E = 0.1 \text{ mAdc}, I_C = 0$)	BV_{EBO}	4.0	-	-	Vdc
Collector Cutoff Current ($V_{CE} = 28 \text{ Vdc}, I_B = 0$)	I_{CEO}	-	-	20	μAdc
Collector Cutoff Current ($V_{CE} = 60 \text{ Vdc}, V_{BE} = 0$)	I_{CES}	-	-	0.1	mAdc
Collector Cutoff Current ($V_{CB} = 28 \text{ Vdc}, I_E = 0$)	I_{CBO}	-	-	1.0	μAdc

ON CHARACTERISTICS

DC Current Gain ($I_C = 50 \text{ mAdc}, V_{CE} = 5.0 \text{ Vdc}$)	h_{FE}	10	-	-	-
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DYNAMIC CHARACTERISTICS

Current-Gain-Bandwidth Product ($I_C = 50 \text{ mAdc}, V_{CE} = 15 \text{ Vdc}, f = 200 \text{ MHz}$)	f_T	500	900	-	MHz
Collector-Base Capacitance ($V_{CB} = 28 \text{ Vdc}, I_E = 0, f = 0.1 \text{ to } 1.0 \text{ MHz}$)	C_{cb}	-	2.5	4.0	pF

FUNCTIONAL TESTS

Common-Emitter Amplifier Power Gain ($V_{CE} = 28 \text{ Vdc}, P_{in} = 0.16 \text{ Watt}, f = 400 \text{ MHz}$) ($V_{CE} = 28 \text{ Vdc}, P_{in} = 50 \text{ mW}, f = 175 \text{ MHz}$)	G_{PE}	8.0 -	8.8 14.5	- -	dB
Power Output ($V_{CE} = 28 \text{ Vdc}, P_{in} = 0.16 \text{ Watt}, f = 400 \text{ MHz}$) ($V_{CE} = 28 \text{ Vdc}, P_{in} = 50 \text{ mW}, f = 175 \text{ MHz}$)	P_{out}	1.0 -	1.2 1.4	- -	Watt
Collector Efficiency ($V_{CE} = 28 \text{ Vdc}, P_{in} = 0.16 \text{ Watt}, f = 400 \text{ MHz}$)	η	45	55	-	%

FIGURE 1 - 400-MHz TEST CIRCUIT

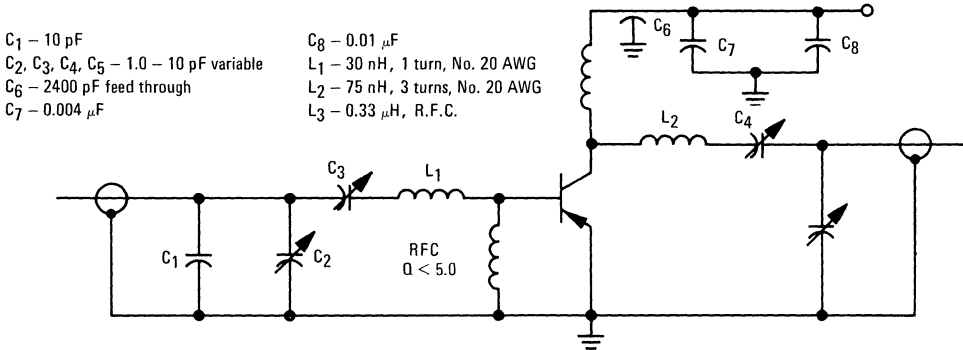


FIGURE 2 - POWER OUTPUT versus FREQUENCY

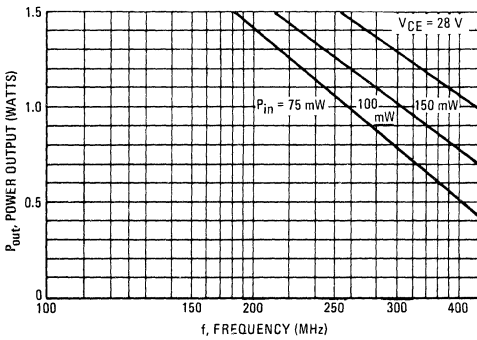


FIGURE 3 - POWER OUTPUT versus POWER INPUT

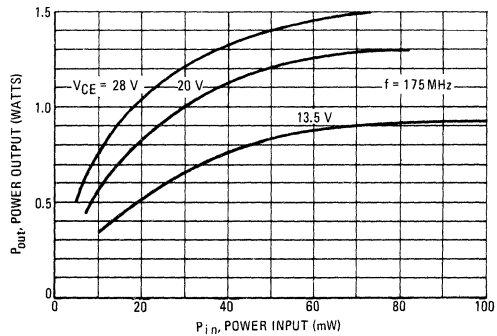


FIGURE 4 - PARALLEL INPUT IMPEDANCE versus FREQUENCY

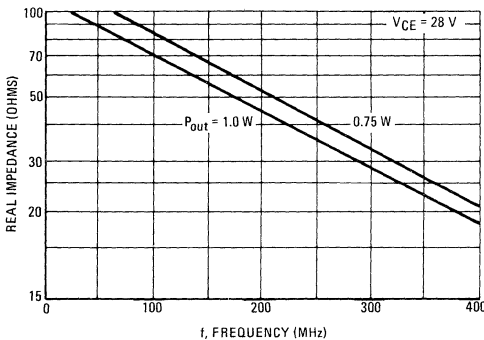


FIGURE 5 - PARALLEL INPUT IMPEDANCE versus FREQUENCY

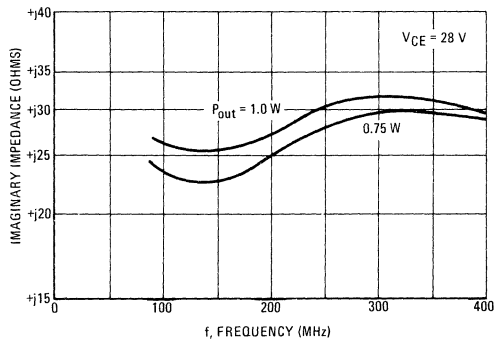


FIGURE 6 – PARALLEL OUTPUT CAPACITANCE versus FREQUENCY

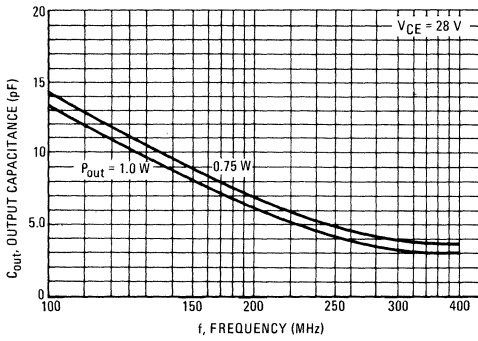


FIGURE 7 – CURRENT-GAIN-BANDWIDTH PRODUCT versus COLLECTOR CURRENT

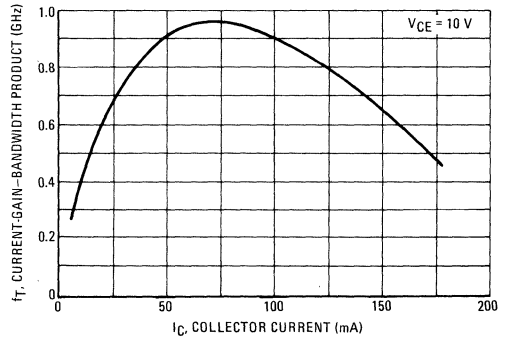


FIGURE 8 – 2N5160 300-MHz COMPLEMENTARY POWER OUTPUT CIRCUIT

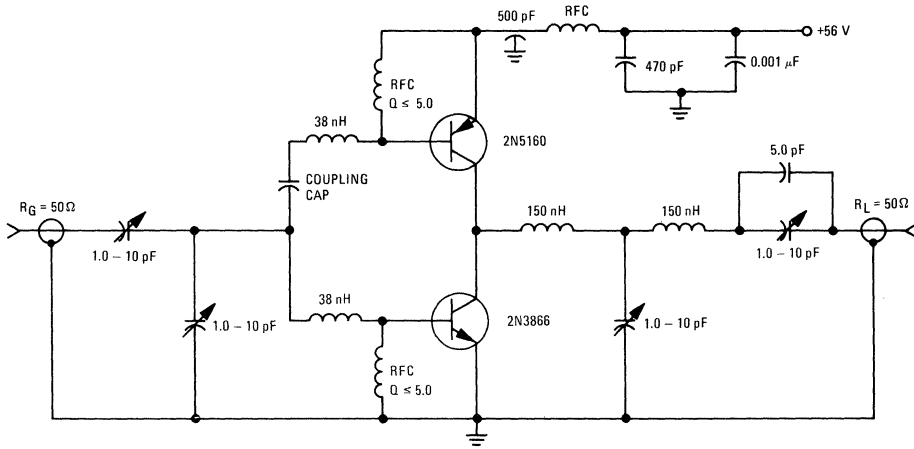


FIGURE 9 – COMPLEMENTARY CIRCUIT – POWER OUTPUT versus POWER INPUT

