

n-channel JFET designed for . . .



■ Low and Medium Frequency Amplifiers

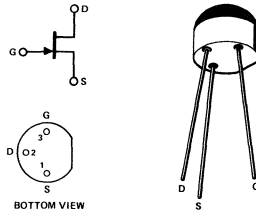
BENEFITS

- Low Cost

*ABSOLUTE MAXIMUM RATINGS (25°C)

Gate-Drain or Gate-Source Voltage	-25 V
Gate Current	10 mA
Total Device Dissipation (25°C Free Air Temperature)	350 mW
Power Derating (to +125°C)	3.5 mW/°C
Storage Temperature Range	-55 to +125°C
Operating Temperature Range	-55 to +125°C
Lead Temperature (1/16" from case for 10 seconds)	300°C

TO-106
See Section 5



*ELECTRICAL CHARACTERISTICS (25°C unless otherwise noted)

		Characteristic	Min	Max	Unit	Test Conditions	
1	S	I _{GSS} Gate Reverse Current		-10	nA	V _{GS} = -15 V, V _{DS} = 0	T _A = 85°C
					-0.6		
3	A	BV _{GSS} Gate-Source Breakdown Voltage	-25			I _G = -10 μA, V _{DS} = 0	
4	T	V _{GS(off)} Gate-Source Cutoff Voltage	-0.4	-8.0	V	V _{DS} = 15 V, I _D = 1 μA	
5	C	V _{GS} Gate-Source Voltage		-7.5		V _{DS} = 15 V, I _D = 100 μA	
6		I _{DSS} Saturation Drain Current	1.0	40	mA	V _{DS} = 15 V, V _{GS} = 0	
7		r _{ds(on)} Drain-Source ON Resistance		500	Ω	V _{GS} = 0, I _D = 0	
8	D	g _{fs} Common-Source Forward Transconductance	2000	9000		V _{DS} = 15 V, V _{GS} = 0	f = 1 kHz
9			g _{os} Common-Source Output Conductance				200
10	N	g _{fs} Common-Source Forward Transconductance	1800			V _{DS} = 15 V, V _{GS} = 0	f = 1 MHz
11			C _{iSS} Common-Source Input Capacitance				20
12	C	C _{rSS} Common-Source Reverse Transfer Capacitance		5.0			
13		NF Common-Source Spot Noise Figure		3.0	dB	V _{DS} = 15 V, I _D = 1 mA	R _G = 150k Ω, f = 1 kHz
14		e _N Equivalent Short Circuit Input Noise Voltage		50	$\frac{nV}{\sqrt{Hz}}$	V _{DS} = 15 V, I _D = 1 mA	NBW = 150 Hz

*JEDEC registered data